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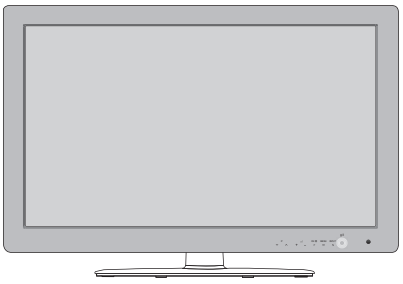
LED LCD TV
SERVICE MANUAL

CHASSIS : LD01S

MODEL : 19LV2300 19LV2300-ZA

CAUTION
BEFORE SERVICING THE CHASSIS,
READ THE SAFETY PRECAUTIONS IN THIS MANUAL.



P/NO : MFL67002109 (1106-REV00)

Printed in Korea

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SAFETY PRECAUTIONS

IMPORTANT SAFETY NOTICE

Many electrical and mechanical parts in this chassis have special safety-related characteristics. These parts are identified by ⚠ in the Schematic Diagram and Exploded View.
It is essential that these special safety parts should be replaced with the same components as recommended in this manual to prevent Shock, Fire, or other Hazards.
Do not modify the original design without permission of manufacturer.

General Guidance

An **isolation Transformer should always be used** during the servicing of a receiver whose chassis is not isolated from the AC power line. Use a transformer of adequate power rating as this protects the technician from accidents resulting in personal injury from electrical shocks.

It will also protect the receiver and it's components from being damaged by accidental shorts of the circuitry that may be inadvertently introduced during the service operation.

If any fuse (or Fusible Resistor) in this TV receiver is blown, replace it with the specified.

When replacing a high wattage resistor (Oxide Metal Film Resistor, over 1 W), keep the resistor 10 mm away from PCB.

Keep wires away from high voltage or high temperature parts.

Before returning the receiver to the customer,

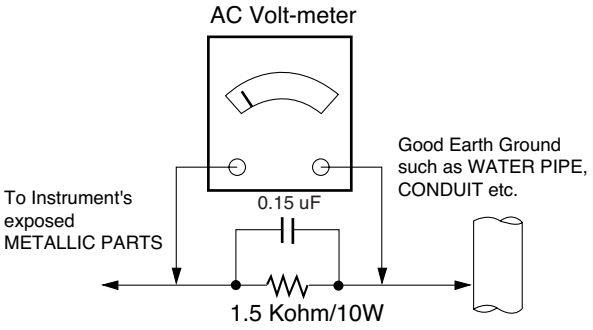
always perform an **AC leakage current check** on the exposed metallic parts of the cabinet, such as antennas, terminals, etc., to be sure the set is safe to operate without damage of electrical shock.

Leakage Current Cold Check(Antenna Cold Check)
With the instrument AC plug removed from AC source, connect an electrical jumper across the two AC plug prongs. Place the AC switch in the on position, connect one lead of ohm-meter to the AC plug prongs tied together and touch other ohm-meter lead in turn to each exposed metallic parts such as antenna terminals, phone jacks, etc.
If the exposed metallic part has a return path to the chassis, the measured resistance should be between 1 MΩ and 5.2 MΩ.
When the exposed metal has no return path to the chassis the reading must be infinite.
An other abnormality exists that must be corrected before the receiver is returned to the customer.

Leakage Current Hot Check (See below Figure)
Plug the AC cord directly into the AC outlet.

Do not use a line Isolation Transformer during this check.
Connect 1.5 K / 10 watt resistor in parallel with a 0.15 uF capacitor between a known good earth ground (Water Pipe, Conduit, etc.) and the exposed metallic parts.
Measure the AC voltage across the resistor using AC voltmeter with 1000 ohms/volt or more sensitivity.
Reverse plug the AC cord into the AC outlet and repeat AC voltage measurements for each exposed metallic part. Any voltage measured must not exceed 0.75 volt RMS which is corresponds to 0.5 mA.
In case any measurement is out of the limits specified, there is possibility of shock hazard and the set must be checked and repaired before it is returned to the customer.

Leakage Current Hot Check circuit



When 25A is impressed between Earth and 2nd Ground for 1 second, Resistance must be less than 0.1 Ω
*Base on Adjustment standard

SERVICING PRECAUTIONS

CAUTION: Before servicing receivers covered by this service manual and its supplements and addenda, read and follow the SAFETY PRECAUTIONS on page 3 of this publication.
NOTE: If unforeseen circumstances create conflict between the following servicing precautions and any of the safety precautions on page 3 of this publication, always follow the safety precautions. Remember: Safety First.

General Servicing Precautions

1. Always unplug the receiver AC power cord from the AC power source before;
   a. Removing or reinstalling any component, circuit board module or any other receiver assembly.
   b. Disconnecting or reconnecting any receiver electrical plug or other electrical connection.
   c. Connecting a test substitute in parallel with an electrolytic capacitor in the receiver.
   CAUTION: A wrong part substitution or incorrect polarity installation of electrolytic capacitors may result in an explosion hazard.

2. Test high voltage only by measuring it with an appropriate high voltage meter or other voltage measuring device (DVM, FETVOM, etc) equipped with a suitable high voltage probe.
   Do not test high voltage by "drawing an arc".
3. Do not spray chemicals on or near this receiver or any of its assemblies.
4. Unless specified otherwise in this service manual, clean electrical contacts only by applying the following mixture to the contacts with a pipe cleaner, cotton-tipped stick or comparable non-abrasive applicator; 10 % (by volume) Acetone and 90 % (by volume) isopropyl alcohol (90 % - 99 % strength)
   CAUTION: This is a flammable mixture.
   Unless specified otherwise in this service manual, lubrication of contacts in not required.
5. Do not defeat any plug/socket B+ voltage interlocks with which receivers covered by this service manual might be equipped.
6. Do not apply AC power to this instrument and/or any of its electrical assemblies unless all solid-state device heat sinks are correctly installed.
7. Always connect the test receiver ground lead to the receiver chassis ground before connecting the test receiver positive lead.
   Always remove the test receiver ground lead last.
8. Use with this receiver only the test fixtures specified in this service manual.
   CAUTION: Do not connect the test fixture ground strap to any heat sink in this receiver.

Electrostatically Sensitive (ES) Devices

Some semiconductor (solid-state) devices can be damaged easily by static electricity. Such components commonly are called Electrostatically Sensitive (ES) Devices. Examples of typical ES devices are integrated circuits and some field-effect transistors and semiconductor "chip" components. The following techniques should be used to help reduce the incidence of component damage caused by static by static electricity.

1. Immediately before handling any semiconductor component or semiconductor-equipped assembly, drain off any electrostatic charge on your body by touching a known earth ground. Alternatively, obtain and wear a commercially available discharging wrist strap device, which should be removed to prevent potential shock reasons prior to applying power to the unit under test.
2. After removing an electrical assembly equipped with ES devices, place the assembly on a conductive surface such as aluminum foil, to prevent electrostatic charge buildup or exposure of the assembly.
3. Use only a grounded-tip soldering iron to solder or unsolder ES devices.
4. Use only an anti-static type solder removal device. Some solder removal devices not classified as "anti-static" can generate electrical charges sufficient to damage ES devices.
5. Do not use freon-propelled chemicals. These can generate electrical charges sufficient to damage ES devices.
6. Do not remove a replacement ES device from its protective package until immediately before you are ready to install it. (Most replacement ES devices are packaged with leads electrically shorted together by conductive foam, aluminum foil or comparable conductive material).
7. Immediately before removing the protective material from the leads of a replacement ES device, touch the protective material to the chassis or circuit assembly into which the device will be installed.
   CAUTION: Be sure no power is applied to the chassis or circuit, and observe all other safety precautions.
8. Minimize bodily motions when handling unpackaged replacement ES devices. (Otherwise harmless motion such as the brushing together of your clothes fabric or the lifting of your foot from a carpeted floor can generate static electricity sufficient to damage an ES device.)

General Soldering Guidelines

1. Use a grounded-tip, low-wattage soldering iron and appropriate tip size and shape that will maintain tip temperature within the range or 500 °F to 600 °F.
2. Use an appropriate gauge of RMA resin-core solder composed of 60 parts tin/40 parts lead.
3. Keep the soldering iron tip clean and well tinned.
4. Thoroughly clean the surfaces to be soldered. Use a mall wire-bristle (0.5 inch, or 1.25 cm) brush with a metal handle.
   Do not use freon-propelled spray-on cleaners.
5. Use the following unsoldering technique
   a. Allow the soldering iron tip to reach normal temperature. (500 °F to 600 °F)
   b. Heat the component lead until the solder melts.
   c. Quickly draw the melted solder with an anti-static, suction-type solder removal device or with solder braid.
   CAUTION: Work quickly to avoid overheating the circuit board printed foil.
6. Use the following soldering technique.
   a. Allow the soldering iron tip to reach a normal temperature (500 °F to 600 °F)
   b. First, hold the soldering iron tip and solder the strand against the component lead until the solder melts.
   c. Quickly move the soldering iron tip to the junction of the component lead and the printed circuit foil, and hold it there only until the solder flows onto and around both the component lead and the foil.
   CAUTION: Work quickly to avoid overheating the circuit board printed foil.
   d. Closely inspect the solder area and remove any excess or splashed solder with a small wire-bristle brush.

IC Remove/Replacement

Some chassis circuit boards have slotted holes (oblong) through which the IC leads are inserted and then bent flat against the circuit foil. When holes are the slotted type, the following technique should be used to remove and replace the IC. When working with boards using the familiar round hole, use the standard technique as outlined in paragraphs 5 and 6 above.

Removal

1. Desolder and straighten each IC lead in one operation by gently prying up on the lead with the soldering iron tip as the solder melts.
2. Draw away the melted solder with an anti-static suction-type solder removal device (or with solder braid) before removing the IC.

Replacement

1. Carefully insert the replacement IC in the circuit board.
2. Carefully bend each IC lead against the circuit foil pad and solder it.
3. Clean the soldered areas with a small wire-bristle brush. (It is not necessary to reapply acrylic coating to the areas).

"Small-Signal" Discrete Transistor Removal/Replacement

1. Remove the defective transistor by clipping its leads as close as possible to the component body.
2. Bend into a "U" shape the end of each of three leads remaining on the circuit board.
3. Bend into a "U" shape the replacement transistor leads.
4. Connect the replacement transistor leads to the corresponding leads extending from the circuit board and crimp the "U" with long nose pliers to insure metal to metal contact then solder each connection.

Power Output, Transistor Device Removal/Replacement

1. Heat and remove all solder from around the transistor leads.
2. Remove the heat sink mounting screw (if so equipped).
3. Carefully remove the transistor from the heat sink of the circuit board.
4. Insert new transistor in the circuit board.
5. Solder each transistor lead, and clip off excess lead.
6. Replace heat sink.

Diode Removal/Replacement

1. Remove defective diode by clipping its leads as close as possible to diode body.
2. Bend the two remaining leads perpendicular y to the circuit board.
3. Observing diode polarity, wrap each lead of the new diode around the corresponding lead on the circuit board.
4. Securely crimp each connection and solder it.
5. Inspect (on the circuit board copper side) the solder joints of the two "original" leads. If they are not shiny, reheat them and if necessary, apply additional solder.

Fuse and Conventional Resistor

Removal/Replacement

1. Clip each fuse or resistor lead at top of the circuit board hollow stake.
2. Securely crimp the leads of replacement component around notch at stake top.
3. Solder the connections.
   CAUTION: Maintain original spacing between the replaced component and adjacent components and the circuit board to prevent excessive component temperatures.

Circuit Board Foil Repair

Excessive heat applied to the copper foil of any printed circuit board will weaken the adhesive that bonds the foil to the circuit board causing the foil to separate from or "lift-off" the board. The following guidelines and procedures should be followed whenever this condition is encountered.

At IC Connections

To repair a defective copper pattern at IC connections use the following procedure to install a jumper wire on the copper pattern side of the circuit board. (Use this technique only on IC connections).

1. Carefully remove the damaged copper pattern with a sharp knife. (Remove only as much copper as absolutely necessary).
2. carefully scratch away the solder resist and acrylic coating (if used) from the end of the remaining copper pattern.
3. Bend a small "U" in one end of a small gauge jumper wire and carefully crimp it around the IC pin. Solder the IC connection.
4. Route the jumper wire along the path of the out-away copper pattern and let it overlap the previously scraped end of the good copper pattern. Solder the overlapped area and clip off any excess jumper wire.

At Other Connections

Use the following technique to repair the defective copper pattern at connections other than IC Pins. This technique involves the installation of a jumper wire on the component side of the circuit board.

1. Remove the defective copper pattern with a sharp knife. Remove at least 1/4 inch of copper, to ensure that a hazardous condition will not exist if the jumper wire opens.
2. Trace along the copper pattern from both sides of the pattern break and locate the nearest component that is directly connected to the affected copper pattern.
3. Connect insulated 20-gauge jumper wire from the lead of the nearest component on one side of the pattern break to the lead of the nearest component on the other side. Carefully crimp and solder the connections.
   CAUTION: Be sure the insulated jumper wire is dressed so the it does not touch components or sharp edges.

SPECIFICATION

NOTE : Specifications and others are subject to change without notice for improvement.

1. Application range

This specification is applied to the LCD/ LED LCD TV used LD01S chassis.

2. Requirement for Test

Each part is tested as below without special appointment.

1) Temperature: 25 ºC ± 5 ºC(77 ºF ± 9 ºF), CST: 40 ºC ± 5 ºC
2) Relative Humidity : 65 % ± 10 %
3) Power Voltage
   : Standard input voltage (AC 100-240 V~, 50 / 60 Hz)
   * Standard Voltage of each products is marked by models.
4) Specification and performance of each parts are followed each drawing and specification by part number in accordance with BOM.
5) The receiver must be operated for about 5 minutes prior to the adjustment.

3. Test method

1) Performance: LGE TV test method followed
2) Demanded other specification
   - Safety: CE, IEC specification
   - EMC:CE, IEC

4. Model General Specification

No.	Item	Specification	Remarks
1	Market	EU(PAL Market-36Countries)	**DTV-T/C & Analog** Germany, Netherlands, Switzerland, Hungary, Austria, Slovenia, Sweden, Denmark, Finland, Norway, Bulgaria
			DTV-T & Analog UK, France, Spain, Italy, Belgium, Russia, Luxemburg, Greece, Czech, Croatia,Turkey, Moroco, Ireland, Latvia, Estonia, Lithuania, Poland, Portugal, Romania, Ukraine, Slovakia
			Analog Only Kazakhstan, Albania, Bosnia, Serbia
2	Broadcasting system	1) PAL-BG 2) PAL-DK 3) PAL-I/I' 4) SECAM L/L' 5) DVB-T/C/S (ID TV)	
3	Receiving system	Analog : Upper Heterodyne Digital : COFDM, QAM	
4	Scart Jack (1EA)	PAL, SECAM	Scart Jack is Full scart and support RF-OUT(analog & DTV)
5	Video Input RCA(1EA)	PAL, SECAM, NTSC	4System : PAL, SECAM, NTSC, PAL60
6	Component Input(1EA)	Y/Cb/Cr, Y/Pb/Pr	
7	RGB Input	RGB-PC	Analog(D-SUB 15PIN)
8	HDMI Input (3EA)	HDMI1-DTV (DVI) HDMI2-DTV HDMI3-DTV	PC(HDMI version 1.3) Support HDCP
9	Audio Input (3EA)	RGB/DVI Audio, Component, AV	L/R Input
10	SDPIF out (1EA)	SPDIF out	
11	Earphone out (1EA)	Antenna, AV1, AV2, AV3, Component, RGB, HDMI1, HDMI2, HDMI3, HDMI4	
12	USB (1EA)	For SVC (download)	
		DivX	
13	DVB	DVB-T	CI : UK, Finland, Denmark, Norway, Sweden, Russia, Spain, Ireland, Luxemburg, Belgium, Netherland CI+ : France(Canal+), Italy(DGTVi)
		DVB-C	CI : Switzerland, Austria, Slovenia, Hungary, Bulgaria CI+ : Switzerland(UPC,Cablecom), Netherland(Ziggo), Germany(KDG,CWB), Finland(labwise)
		DVB-S	CI+ : Germany(Astra HD+)

5. Component Video Input (Y, CB/PB, CR/PR)

No.	Specification				Remark
	Resolution	H-freq(kHz)	V-freq(Hz)		
1.	720x480	15.73	60.00	SDTV,DVD 480i	
2.	720x480	15.63	59.94	SDTV,DVD 480i	
3.	720x480	31.47	59.94	480p	
4.	720x480	31.50	60.00	480p	
5.	720x576	15.625	50.00	SDTV,DVD 625 Line	
6.	720x576	31.25	50.00	HDTV 576p	
7.	1280x720	45.00	50.00	HDTV 720p	
8.	1280x720	44.96	59.94	HDTV 720p	
9.	1280x720	45.00	60.00	HDTV 720p	
10.	1920x1080	31.25	50.00	HDTV 1080i	
11.	1920x1080	33.75	60.00	HDTV 1080i	
12.	1920x1080	33.72	59.94	HDTV 1080i	
13.	1920x1080	56.250	50	HDTV 1080p	
14.	1920x1080	67.5	60	HDTV 1080p	

6. RGB (PC)

No.	Specification				Proposed	Remark
	Resolution	H-freq(kHz)	V-freq(Hz)	Pixel Clock(MHz)		
1.	720*400	31.468	70.08	28.321		For only DOS mode
2.	640*480	31.469	59.94	25.17	VESA	Input 848*480 60 Hz, 852*480 60 Hz -> 640*480 60 Hz Display
3.	800*600	37.879	60.31	40.00	VESA	
4.	1024*768	48.363	60.00	65.00	VESA(XGA)	
5.	1280*768	47.78	59.87	79.5	WXGA	
6.	1360*768	47.72	59.8	84.75	WXGA	FHD Model
7.	1366*768	47.56	59.6	84.75	WXGA	WXGA Model
8.	1200*1024	63.901	60.02	100.075	SXGA	FHD model
9.	1280*720	45	60	74.25	720p	DTV Standard
10.	1920*1080	67.5	60	148.5	WUXGA	FHD model

7. HDMI Input

(1) DTV Mode

No.	Resolution	H-freq(kHz)	V-freq.(Hz)	Pixel clock(MHz)	Proposed	Remark
1.	720*480	31.469/31.5	59.94/60	27.00/27.03	SDTV 480P	
2.	720*576	31.25	50	54	SDTV 576P	
3.	1280*720	37.500	50	74.25	HDTV 720P	
4.	1280*720	44.96/45	59.94/60	74.17/74.25	HDTV 720P	
5.	1920*1080	33.72/33.75	59.94/60	74.17/74.25	HDTV 1080I	
6.	1920*1080	28.125	50.00	74.25	HDTV 1080I	
7.	1920*1080	26.97/27	23.97/24	74.17/74.25	HDTV 1080P	
8.	1920*1080	33.716/33.75	29.976 /30.00	74.25	HDTV 1080P	
9.	1920*1080	56.250	50	148.5	HDTV 1080P	
10.	1920*1080	67.43/67.5	59.94/60	148.35/148.50	HDTV 1080P	

(2) PC Mode

No.	Resolution	H-freq(kHz)	V-freq.(Hz)	Pixel clock(MHz)	Proposed	Remark
1.	720*400	31.468	70.08	28.321		HDCP
2.	640*480	31.469	59.94	25.17	VESA	HDCP
3.	800*600	37.879	60.31	40.00	VESA	HDCP
4.	1024*768	48.363	60.00	65.00	VESA(XGA)	HDCP
5.	1280*768	47.78	59.87	79.5	WXGA	HDCP
6.	1360*768	47.72	59.8	84.75	WXGA	HDCP
7.	1280*720	45	60	74.25		HDCP
8.	1280*1024	63.981	60.02	108.875	SXGA	HDCP/FHD model
9.	1920*1080	67.5	60	148.5	WUXGA	HDCP/FHD model

ADJUSTMENT INSTRUCTION

1. Application Range

This specification sheet is applied to all of the LCD/ LED LCD TV with LD01S chassis.

2. Designation

1) The adjustment is according to the order which is designated and which must be followed, according to the plan which can be changed only on agreeing.
2) Power Adjustment: Free Voltage
3) Magnetic Field Condition: Nil.
4) Input signal Unit: Product Specification Standard
5) Reserve after operation: Above 5 Minutes (Heat Run)
   Temperature : at 25 °C ± 5 °C
   Relative humidity : 65 % ± 10 %
   Input voltage : 220 V, 60 Hz
6) Adjustment equipments: Color Analyzer(CA-210 or CA-110), DDC Adjustment Jig equipment, Service remote control.
7) Push The “IN STOP” key - For memory initialization.

Case1 : Software version up
1. After downloading S/W by USB, TV set will reboot automatically
2. Push “In-stop” key
3. Push “Power on” key
4. Function inspection
5. After function inspection, Push “In-stop” key.

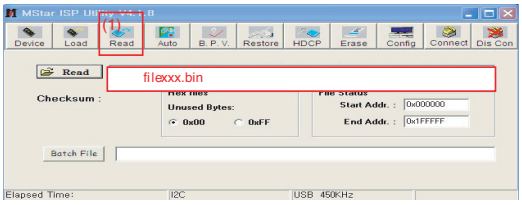
Case2 : Function check at the assembly line
1. When TV set is entering on the assembly line, Push “In-stop” key at first.
2. Push “Power on” key for turning it on.
   -> If you push “Power on” key, TV set will recover channel information by itself.
3. After function inspection, Push “In-stop” key.

3. Main PCB check process

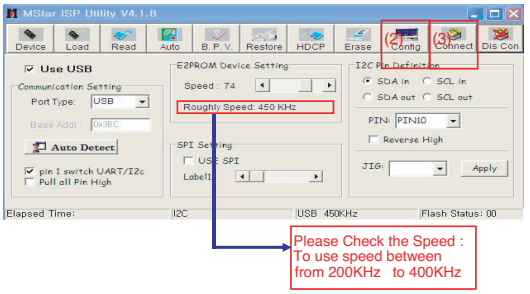
* APC - After Manual-Insult, executing APC

* Boot file Download

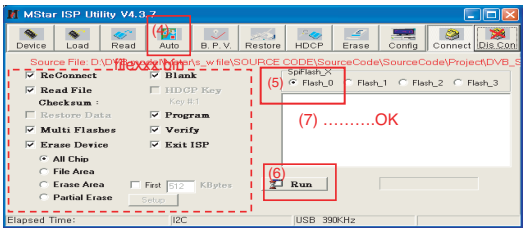
1) Execute ISP program “Mstar ISP Utility” and then click “Config” tab.



2) Set as below, and then click “Auto Detect” and check “OK” message.
   If “Error” is displayed, Check connection between computer, jig, and set.
3) Click “Read” tab, and then load download file(XXXX.bin) by clicking “Read”.
4) Click “Connect” tab. If “Can’t” is displayed, check connection between computer, jig, and set.

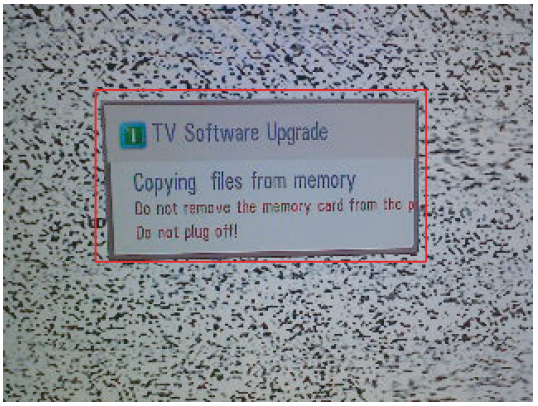


5) Click “Auto” tab and set as below.
6) Click “Run”.
7) After downloading, check “OK” message.

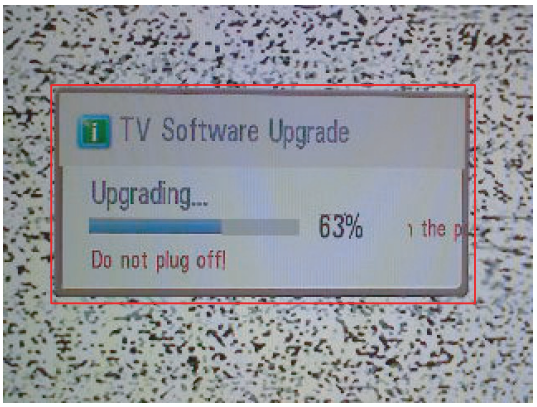


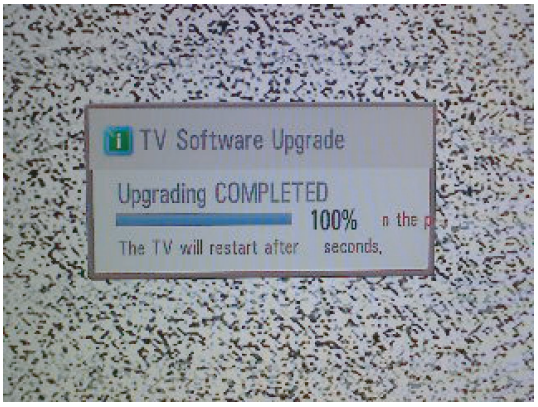
* USB DOWNLOAD

1) Put the USB Stick to the USB socket.
2) Automatically detecting update file in USB Stick.
   - If your downloaded program version in USB Stick is Low, it didn’t work. But your downloaded version is High, USB data is automatically detecting
3) Show the message “Copying files from memory”.



4) Updating is starting.





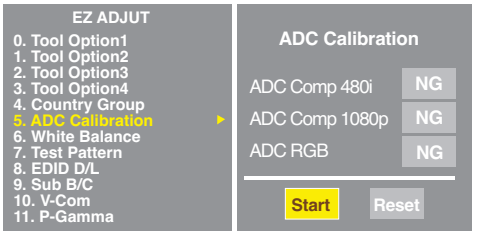
5) Uploading completed, the TV will restart automatically.
6) If your TV is turned on, check your updated version and Tool option.(explain the Tool option, next stage)
   * If downloading version is more high than your TV have, TV can lost all channel data. In this case, you have to channel recover. if all channel data is cleared, you didn't have a DTV/ATV test on production line.

*** After downloading, have to adjust Tool Option again.**

1) Push "IN-START" key in service remote control.
2) Select "Tool Option 1" and push "OK" key.
3) Punch in the number. (Each model has their number)
4) Completed selecting Tool option.

3.1. ADC Process

(1) ADC
- Enter Service Mode by pushing "ADJ" key,
- Enter Internal ADC mode by pushing "▶" key at "5. ADC Calibration".



<Caution> Using 'power on' key of the Adjustment remote control, power on TV.

* ADC Calibration Protocol (RS232)

Item	CMD1	CMD2	Data0		
Adjust 'Mode In'	A	A	0	0	When transfer the 'Mode In', Carry the command.
ADC Adjust	A	D	1	0	Automatically adjustment (The use of a internal pattern)

Adjust Sequence
- aa 00 00 [Enter Adjust Mode]
- xb 00 40 [Component1 Input (480i)]
- ad 00 10 [Adjust 480i Comp1]
- xb 00 60 [RGB Input (1024*768)]
- ad 00 10 [Adjust 1024*768 RGB]
- aa 00 90 End Adjust mode

* Required equipment : Adjustment remote control.

3.2. Function Check

* Check display and sound
- Check Input and Signal items. (cf. work instructions)
  1) TV
  2) AV (SCART1/SCART2/CVBS)
  3) COMPONENT (480i)
  4) RGB (PC : 1024 x 768 @ 60 Hz)
  5) HDMI
  6) PC Audio In

  * Display and Sound check is executed by Remote control.

4. Total Assembly line process

4.1. Adjustment Preparation

· W/B Equipment condition
CA210
: CCFL/EEFL -> CH9, Test signal: Inner pattern(80IRE)
LED -> CH14, Test signal: Inner pattern(80IRE)

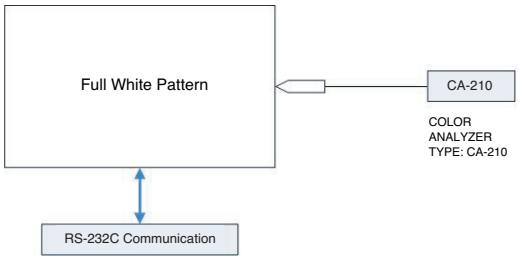
· Above 5 minutes H/run in the inner pattern. (“power on” key of adjustment remote control)

Cool	9,300	K	X=0.285(±0.002)	
			Y=0.293(±0.002)	<Test Signal>
Medium	8,000	K	X=0.295(±0.002)	Inner pattern
			Y=0.305(±0.002)	(204 gray, 80 IRE)
Warm	6,500	K	X=0.313(±0.002)	
			Y=0.329(±0.002)	

· Edge LED W/B Table is process of time (Only LGD Module)
CA210: CH14, Test signal : Inner pattern(80IRE)

GP2R	Aging Time (Min.)	Cool		Medium		Warm	
		X	Y	X	Y	X	Y
		269	273	285	293	313	329
1	0-2	279	288	295	308	319	338
2	3-5	278	286	294	306	318	336
3	6-9	277	285	293	305	317	335
4	10-19	276	283	292	303	316	333
5	20-35	274	280	290	300	314	330
6	36-49	272	277	288	297	312	327
7	50-79	271	275	287	295	311	325
8	80-149	270	274	286	294	310	324
9	Over 150	269	273	285	293	309	323

* Connecting picture of the measuring instrument
(On Automatic control)
Inside Pattern is used when W/B is controlled. Connect to auto controller or push Adjustment remote control POWER ON -> Enter the mode of White-Balance, the pattern will come out.



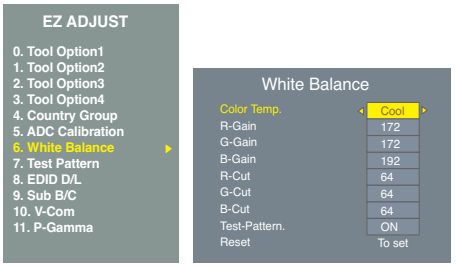
* Auto-control interface and directions
1) Adjust in the place where the influx of light like floodlight around is blocked. (Illumination is less than 10 lux).
2) Adhere closely the Color Analyzer (CA210) to the module less than 10 cm distance, keep it with the surface of the Module and Color Analyzer’s prove vertically.(80° ~ 100°).
3) Aging time
- After aging start, keep the power on (no suspension of power supply) and heat-run over 5 minutes.
- Using ‘no signal’ or ‘full white pattern’ or the others, check the back light on.

• Auto adjustment Map(RS-232C)
RS-232C COMMAND
[CMD ID DATA]
Wb 00 00 White Balance Start
Wb 00 ff White Balance End

	RS-232C COMMAND [CMD ID DATA]			MIN	CENTER (DEFAULT)			MAX
	Cool	Mid	Warm		Cool	Mid	Warm	
R Gain	jg	Ja	jd	00	172	192	192	192
G Gain	jh	Jb	je	00	172	192	192	192
B Gain	ji	Jc	jf	00	192	192	172	192
R Cut					64	64	64	128
G Cut					64	64	64	128
B Cut					64	64	64	128

** Caution **
Color Temperature : COOL, Medium, Warm.
One of R Gain/G Gain/ B Gain should be kept on 0xC0, and adjust other two lower than C0.
(when R/G/B Gain are all C0, it is the FULL Dynamic Range of Module)

* Manual W/B process using adjusts Remote control.
• After enter Service Mode by pushing “ADJ” key,
• Enter White Balance by pushing “▶” key at “6. White Balance”.



* After you finished all adjustments, Press “In-start” key and compare Tool option and Area option value with its BOM, if it is correctly same then unplug the AC cable. If it is not same, then correct it same with BOM and unplug AC cable. For correct it to the model’s module from factory Jig model.
* Push the “IN STOP” key after completing the function inspection. And Mechanical Power Switch must be set “ON”.

4.2. DDC EDID Write (RGB 128Byte)

• Connect D-sub Signal Cable to D-sub Jack.
• Write EDID Data to EEPROM(24C02) by using DDC2B protocol.
• Check whether written EDID data is correct or not.
* For Service main Assembly, EDID have to be downloaded to Insert Process in advance.

4.3. DDC EDID Write (HDMI 256Byte)

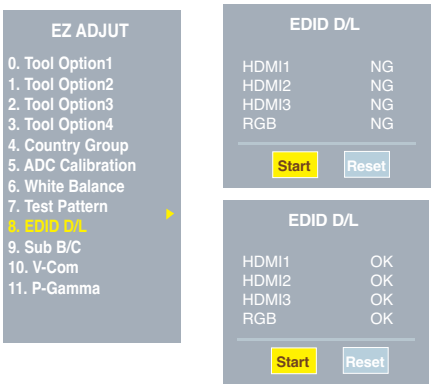
• Connect HDMI Signal Cable to HDMI Jack.
• Write EDID Data to EEPROM(24C02) by using DDC2B protocol.
• Check whether written EDID data is correct or not.
* For Service main Assembly, EDID have to be downloaded to Insert Process in advance.

4.4. EDID DATA

1) All Data : HEXA Value
2) Changeable Data :
*: Serial No : Controlled / Data:01
**: Month : Controlled / Data:00
***: Year : Controlled
****: Check sum

- Auto Download

- After enter Service Mode by pushing “ADJ” key,
- Enter EDID D/L mode.
- Enter “START” by pushing “OK” key.



* Caution : Never connect HDMI & D-sub Cable when EDID download.

* Edid data and Model option download (RS232)

Item	CMD1	CMD2	Data0		
Download ‘Mode In’	A	A	0	0	When transfer the ‘Mode In’, Carry the command.
Download	A	E	00	10	Automatically Download (The use of a internal pattern)

- Manual Download

* Caution
1) Use the proper signal cable for EDID Download.
- Analog EDID : Pin3 exists
- Digital EDID : Pin3 exists
2) Never connect HDMI & D-sub Cable at the same time.
3) Use the proper cables below for EDID Writing.
4) Download HDMI1, HDMI2, separately because HDMI1 is different from HDMI2.

For Analog EDID	For HDMI EDID
D-sub to D-sub	DVI-D to HDMI or HDMI to HDMI
	 

Item	Condition	Data(Hex)
Manufacturer ID	GSM	1E6D
Version	Digital : 1	01
Revision	Digital : 3	03

1) HD RGB EDID data

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
00	00	FF	FF	FF	FF	FF	FF	00	1E	6D	a		b			
10	c		01	03	68	10	09	78	0A	EE	91	A3	54	4C	99	26
20	0F	50	54	A1	08	00	81	C0	61	40	45	40	31	40	01	01
30	01	01	01	01	01	01	1B	21	50	A0	51	00	1E	30	48	88
40	35	00	A0	5A	00	00	00	1C	01	1D	00	72	51	D0	1E	20
50	6E	28	55	00	A0	5A	00	00	00	1E	00	00	00	FD	00	3A
60	3E	1F	46	10	00	0A	20	20	20	20	20	20	d			
70	d														00	e
80	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
90	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
A0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
B0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
C0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
D0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
E0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
F0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF

2) HD HDMI EDID data

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
00	00	FF	FF	FF	FF	FF	FF	00	1E	6D	a		b			
10	c		01	03	80	10	09	78	0A	EE	91	A3	54	4C	99	26
20	0F	50	54	A1	08	00	81	C0	61	40	45	40	31	40	01	01
30	01	01	01	01	01	01	1B	21	50	A0	51	00	1E	30	48	88
40	35	00	A0	5A	00	00	00	1C	01	1D	00	72	51	D0	1E	20
50	6E	28	55	00	A0	5A	00	00	00	1E	00	00	00	FD	00	3A
60	3E	1F	46	10	00	0A	20	20	20	20	20	20	d			
70	d														01	e
80	02	03	20	F1	4E	10	1F	84	13	05	14	03	02	12	20	21
90	22	15	01	26	15	07	50	09	57	07	f					
A0	f		80	18	71	1C	16	20	58	2C	25	00	A0	5A	00	00
B0	00	9E	01	1D	00	80	51	D0	0C	20	40	80	35	00	A0	5A
C0	00	00	00	1E	8C	0A	D0	8A	20	E0	2D	10	10	3E	96	00
D0	A0	5A	00	00	00	18	02	3A	80	18	71	38	2D	40	58	2C
E0	45	00	A0	5A	00	00	00	1E	01	1D	80	D0	72	1C	16	20
F0	10	2C	25	80	A0	5A	00	00	00	9E	00	00	00	00	00	0e

* Detail EDID Options are below
ⓐ Product ID

Model Name	HEX	EDID Table	DDC Function
HD Model	0000	00 00	Analog/Digital

ⓑ Serial No: Controlled on production line.
ⓒ Month, Year: Week : ‘01’ -> ‘01’
Year : ‘2011’ -> ‘15’ fix
ⓓ Model Name(Hex):

MODEL	MODEL NAME(HEX)
all	00 00 00 FC 00 4C 47 20 54 56 0A 20 20 20 20 20 20 20

ⓔ Checksum: Changeable by total EDID data.
ⓕ Vendor Specific(HDMI)

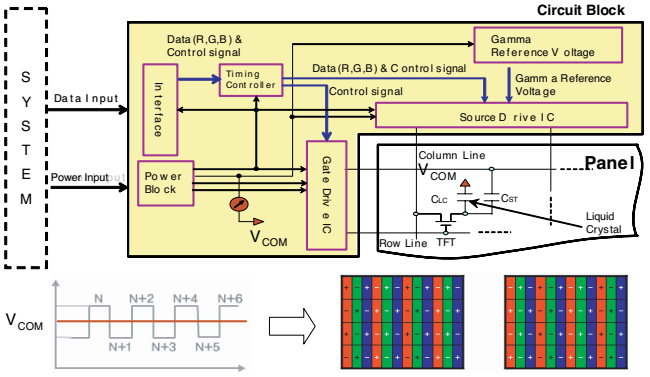
INPUT	MODEL NAME(HEX)
HDMI1	65030C001000
HDMI2	65030C002000
HDMI3	65030C003000

4.5. V-COM Adjust(Only LGD(M+S) Module)

- Why need Vcom adjustment?

■ The Vcom (Common Voltage) is a Reference Voltage of Liquid Crystal Driving.

-> Liquid Crystal need for Polarity Change with every frame.



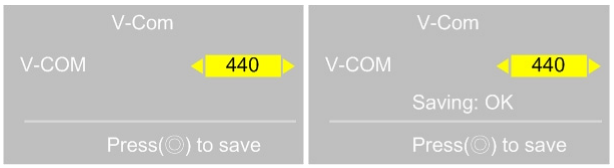
- Adjust sequence

· Press the PIP key of th ADJ remote control.(This PIP key is hot key to enter the VCOM adjusting mode)
(Or After enter Service Mode by pushing “ADJ” key, then Enter V-Com Adjust mode by pushing “▶” key at “10. V-Com”.)

· As pushing the right or the left key on the remote control, and find the V-COM value which is no or minimized the Flicker. (If there is no flicker at default value, Press the exit key and finish the VCOM adjustment.)

· Push the “OK” key to store value. Then the message “Saving OK” is pop.

· Press the exit key to finish VCOM adjustment.



(Visual Adjust and control the Voltage level)

4.6. Outgoing condition Configuration

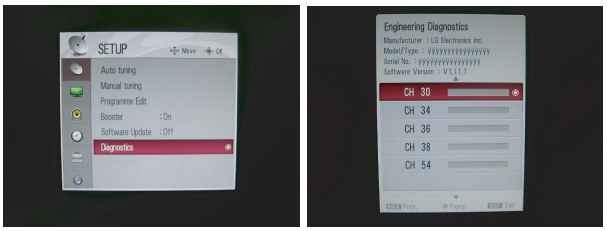
When pressing IN-STOP key by Service remote control, Red LED are blinked alternatively. And then Automatically turn off. (Must not AC power OFF during blinking)

4.7. Hi-pot Test

Confirm whether is normal or not when between power board’s ac block and GND is impacted on 1.5 kV(dc) or 2.2 kV(dc) for one second.

5. Model name & Serial number D/L

- Press “Power on” key of service remote control. (Baud rate : 115200 bps)
- Connect RS232 Signal Cable to RS-232 Jack.
- Write Serial number by use RS-232.
- Must check the serial number at the Diagnostics of SET UP menu. (Refer to below.)



5.1. Signal TABLE

CMD	LENGTH	ADH	ADL	DATA_1	. . .	Data_n	CS	DELAY

CMD : A0h
LENGTH : 85~94h (1~16 bytes)
ADH : EEPROM Sub Address high (00~1F)
ADL : EEPROM Sub Address low (00~FF)
Data : Write data
CS : CMD + LENGTH + ADH + ADL + Data_1 +…+ Data_n
Delay : 20ms

5.2. Command Set

No.	Adjust mode	CMD(hex)	LENGTH(hex)	Description
1	EEPROM WRITE	A0h	84h+n	n-bytes Write(n=1~16)

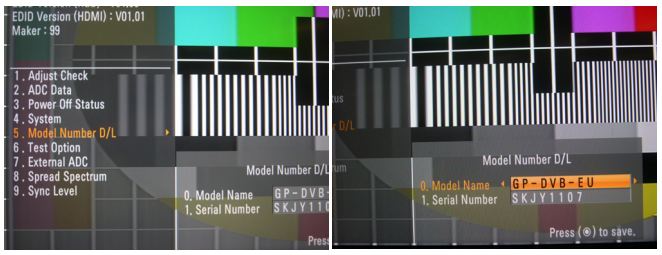
* Description
FOS Default write : <7mode data> write
Vtotal, V_Frequency, Sync_Polarity, Htotal, Hstart, Vstart, 0, Phase
Data write : Model Name and Serial Number write in EEPROM,.

5.3. Method & notice

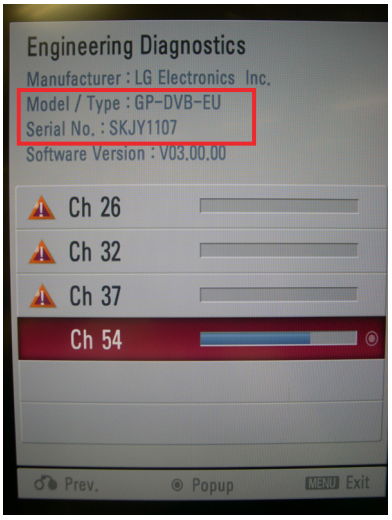
A. Serial number D/L is using of scan equipment.
B. Setting of scan equipment operated by Manufacturing Technology Group.
C. Serial number D/L must be conformed when it is produced in production line, because serial number D/L is mandatory by D-book 4.0.

* Manual Download (Model Name and Serial Number)
If the TV set is downloaded by OTA or Service man, Sometimes model name or serial number is initialized.(Not always)
There is impossible to download by bar code scan, so It need Manual download.

1) Press the “Instart” key of Adjustment remote control.
2) Go to the menu ‘5.Model Number D/L’ like below photo.
3) Input the Factory model name(ex 42LD450-ZA) or Serial number like photo.

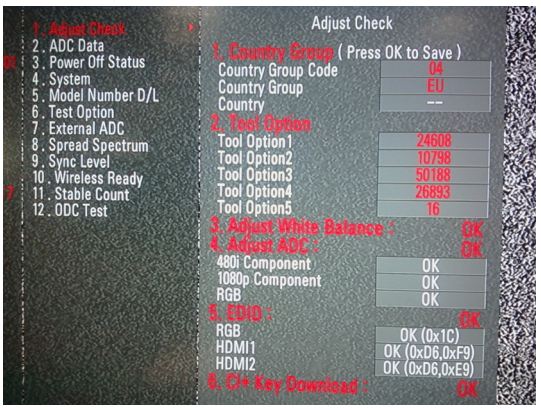


4) Check the model name Instart menu. -> Factory name displayed. (ex 42LD450-ZA)
5) Check the Diagnostics. (DTV country only) -> Buyer model displayed. (ex 42LD450)



6. CI+ Key Download method

(1) Download Procedure
1) Press “Power on” key of a service remote control.
(Baud rate : 115200 bps)
2) Connect RS232-C Signal Cable.
3) Write CI+ Key through RS-232-C.
4) Check whether the key was downloaded or not at ‘In Start’ menu. (Refer to below.)



=> Check the Download to CI+ Key value in LGset.
1. Check the method of CI+ Key value.
a. Check the method on Instart menu.
b. Check the method of RS232C Command.
1) Into the main assembly mode (RS232 : aa 00 00)

CMD 1	CMD 2	Data 0	
A	A	0	0

2) Check the key download for transmitted command.
(RS232 : ci 00 10)

CMD 1	CMD 2	Data 0	
C	I	1	0

3) Result value
- Normally status for download : OKx
- Abnormally status for download : NGx

2. Check the method of CI+ Key value. (RS232)
1) Into the main assembly mode (RS232 : aa 00 00)

CMD 1	CMD 2	Data 0	
A	A	0	0

2) Check the method of CI+ key by command.
(RS232 : ci 00 20)

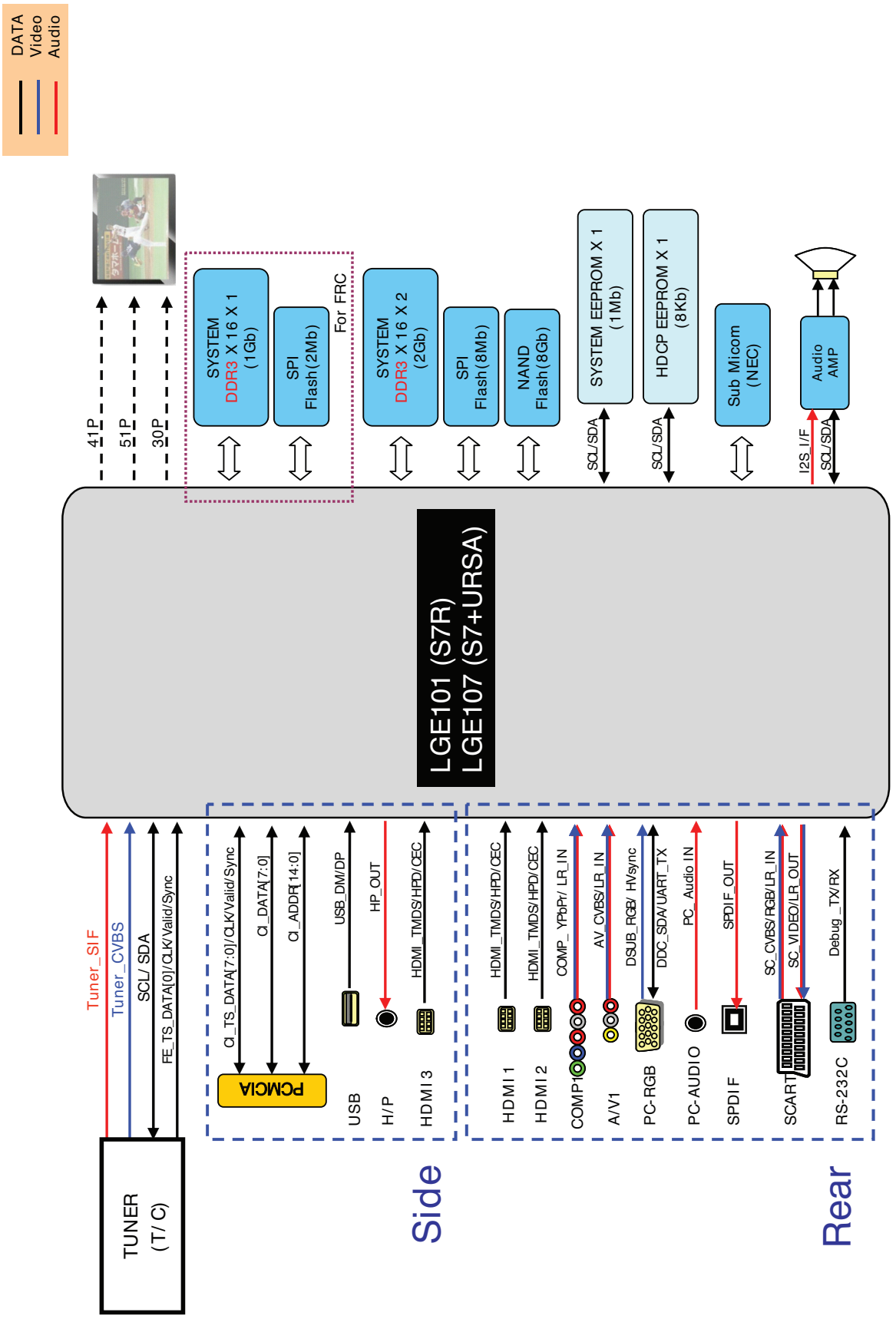
CMD 1	CMD 2	Data 0	
C	I	2	0

3) Result value
i 01 OK 1d1852d21c1ed5dcx
→ CI+ key Value

7. Local Dimming Function Check

Step1) Turn on TV.
Step2) Press “P-only” key, entrance to power only mode and Press “Exit” key.
Step3) Press “Tilt” key, entrance to Local Dimming mode.
Step4) At the Local Dimming mode, module Edge Backlight moving right to left Back light of module moving.
Step5) Confirm the Local Dimming mode.
Step6) Press “Exit” key.

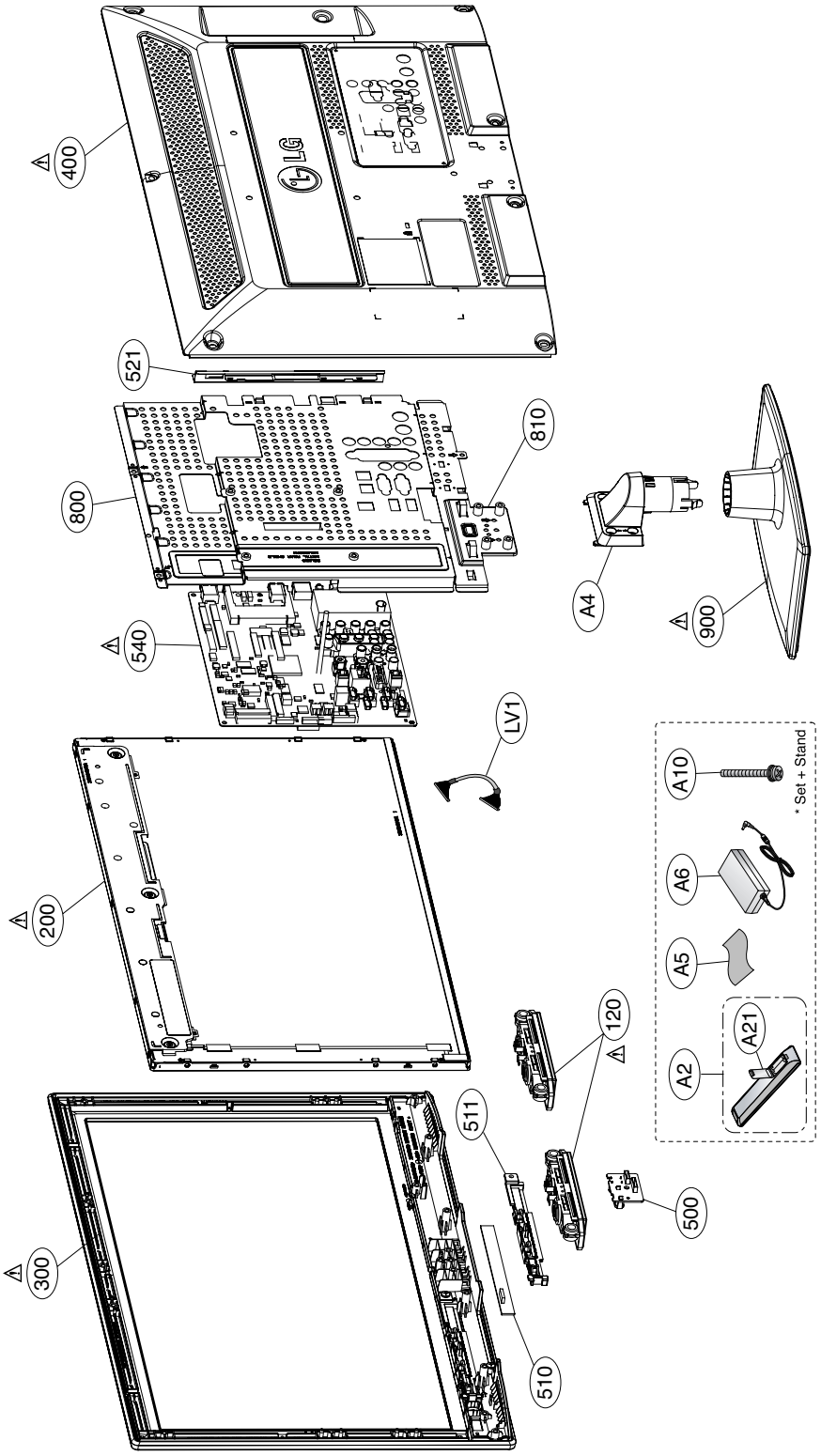
BLOCK DIAGRAM



EXPLODED VIEW

IMPORTANT SAFETY NOTICE

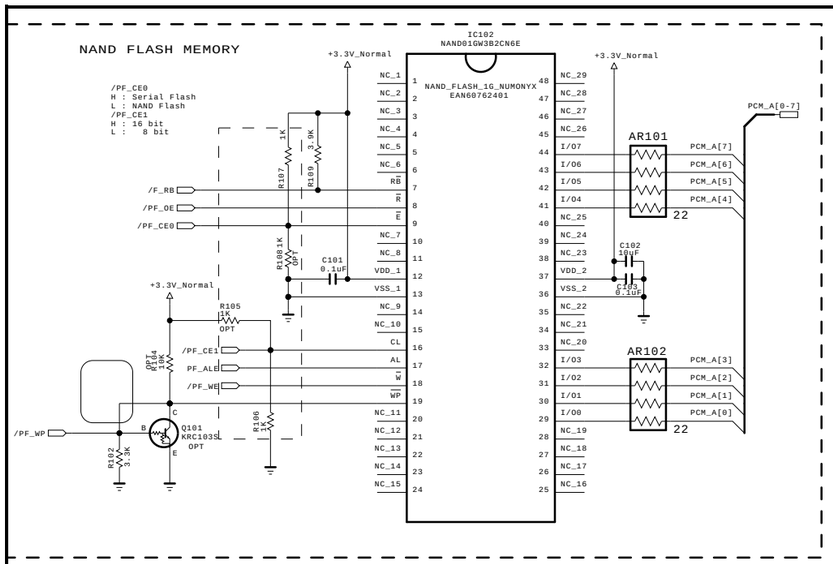
Many electrical and mechanical parts in this chassis have special safety-related characteristics. These parts are identified by ⚠ in the Schematic Diagram and EXPLODED VIEW.
It is essential that these special safety parts should be replaced with the same components as recommended in this manual to prevent X-RADIATION, Shock, Fire, or other Hazards.
Do not modify the original design without permission of manufacturer.



S7M-PLUS_DivX_MS10

LGE107DC-RP [S7M+ DIVX/MS10]

NAND FLASH MEMORY

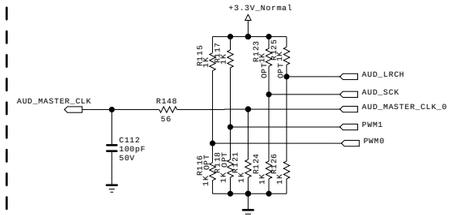


<T3 CHIP Config(AUD_LRCH)>

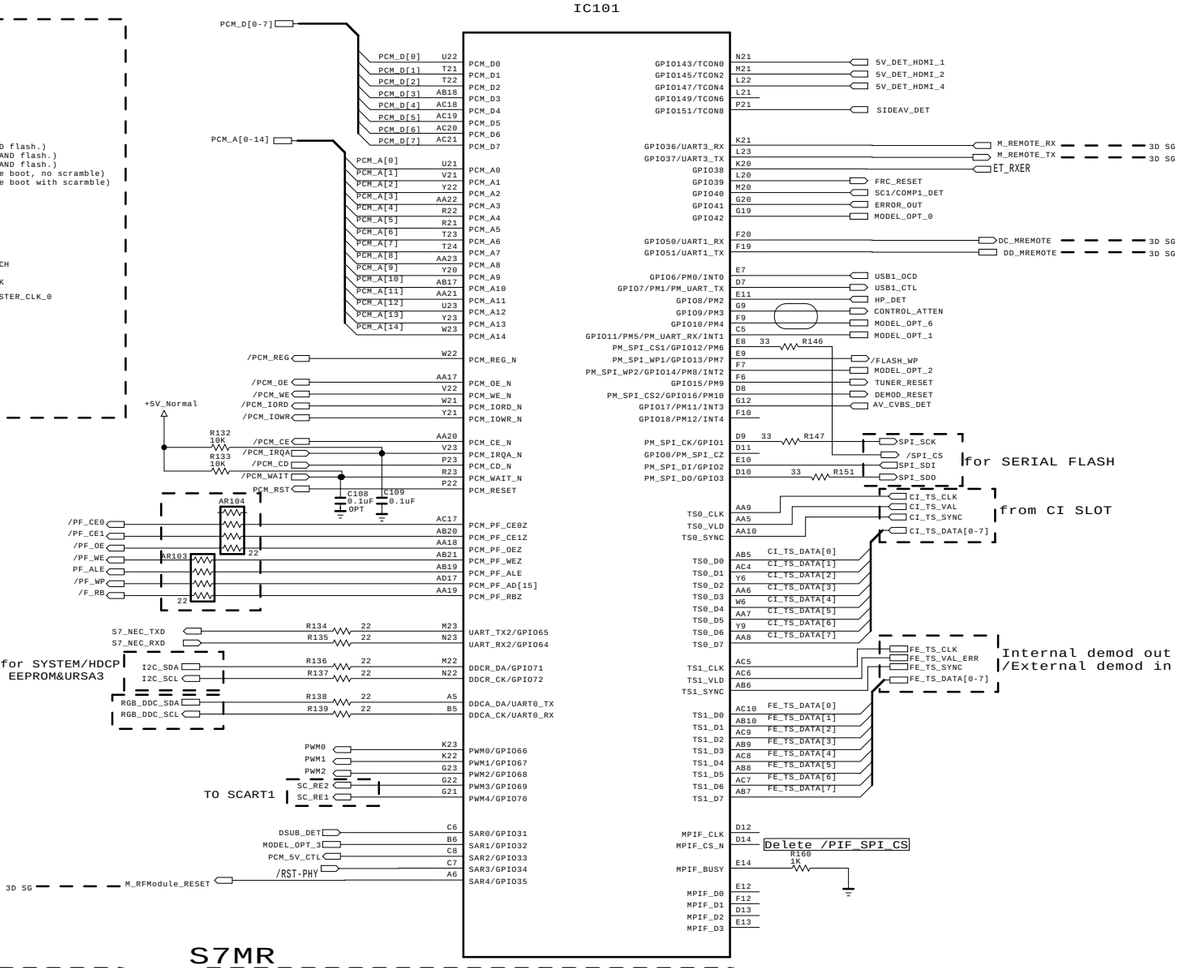
Boot from SPI flash : 1'b0
Boot from NOR flash : 1'b1

<T3 CHIP Config>
(AUD_SCK, AUD_MASTER_CLK, PWM1, PWM0)

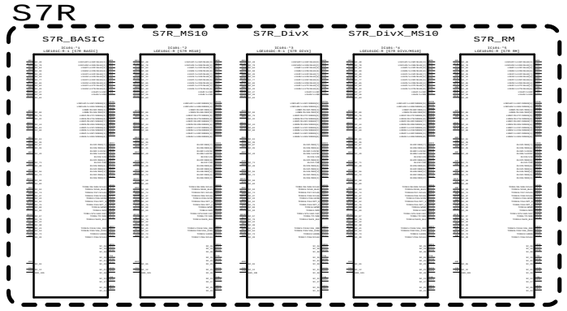
MIPS_no_EJ_NOR8 : 4'h3 (MIPS as host. No EJ PAD. Byte mode NAND flash.)
MIPS_EJ1_NOR8 : 4'h4 (MIPS as host. EJ use PAD1. Byte mode NAND flash.)
MIPS_EJ2_NOR8 : 4'h5 (MIPS as host. EJ use PAD2. Byte mode NAND flash.)
B51_Secure_no_scramble : 4'hb (8051 as host. Internal SPI flash secure boot, no scramble)
B51_Sesure_scramble : 4'hc (8051 as host. Internal SPI flash secure boot with scarmble)



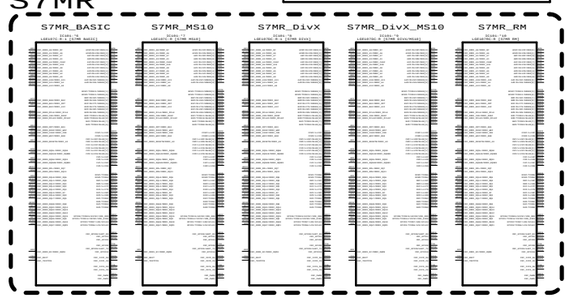
IC101



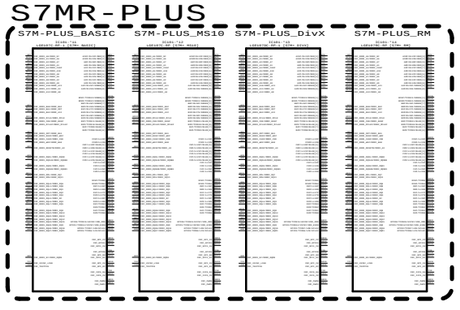
S7R

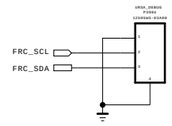


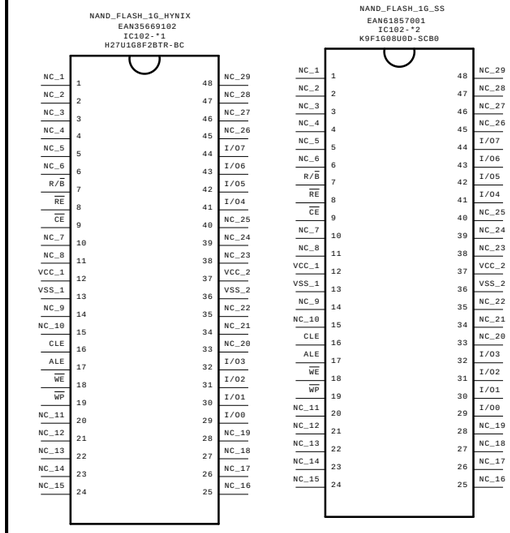
S7MR

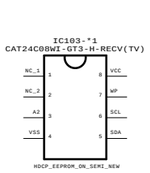


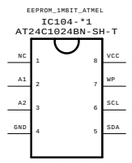
S7MR-PLUS

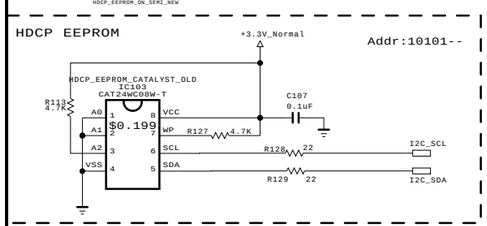


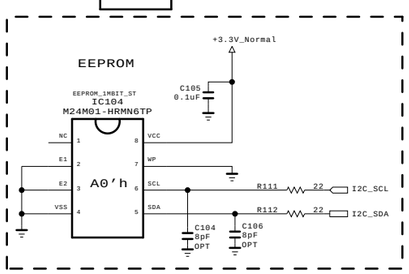


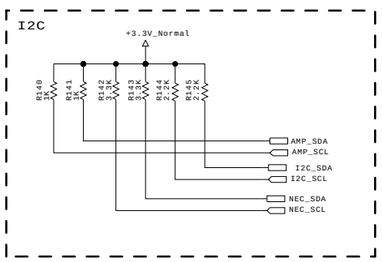


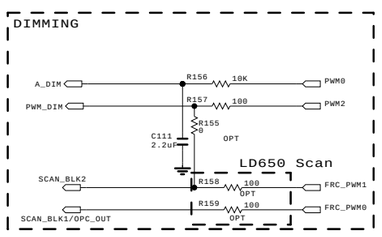












THE ⚠ SYMBOL MARK OF THIS SCHEMETIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFATURES SPECFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE ⚠ SYMBOL MARK OF THE SCHEMETIC.

SECRET
LGElectronics

LG ELECTRONICS

MODEL	GP3_Saturn7M	DATE	Ver. 0.1
BLOCK	FLASH/EEPROM/GPIO	SHEET	1

MODEL OPTION

PIN NAME	PIN NO.	LOW	HIGH
MODEL_OPT_0	G19	NO FRC	FRC_HW_OPT
MODEL_OPT_4	E18	50/60Hz LVDS	100/120Hz LVDS
MODEL_OPT_1	C5	PHM_OFF	PHM_ON
MODEL_OPT_2	F7	NON_DVB_T2	DVB_T2
MODEL_OPT_3	B6	HD	FHD
MODEL_OPT_5	D18	Ready	default
MODEL_OPT_6	F9	LCD	OLED

NO_FRC : LOW LOW
U3_INTERNAL : HIGH LOW
U5_EXTERNALBOOT :HIGH HIGH
reserved for FRC : LOW HIGH

OPT_0	OPT_4
LOW	LOW
HIGH	LOW
HIGH	HIGH
LOW	HIGH

--> This option is only applied in EU.
In case of NON_EU, default value set LOW.

-->In case of GP2, This port was used for GIP/NON_GIP

--> MODEL_OPT_5, MODEL_OPT_6
: Only 3D_SG GPIO OUTPUT CONTROL

MODEL OPTION

+3.3V_Normal

IF_AGC_SEL R201 OPT 100 → MODEL_OPT_0
LNA2_CTL R202 BOOT_FR_OPT 100 → MODEL_OPT_1
RF_SWITCH_CTL R203 RF_SW_OPT 100 → MODEL_OPT_2
R204 100 → MODEL_OPT_3
R210 OPT 100 → MODEL_OPT_4
3D_GPIO_1 R213 0 → MODEL_OPT_5
3D_GPIO_2 R216 3D_SG 0 → MODEL_OPT_6
3D_SG

R214 OPT 1K
R205 OPT 1K
R206 FHD 1K
R208 DVB_T2 1K
R211 PHM_ON 1K
R226 FRC_HW_OPT 1K

R215 OPT 1K
R209 OPT 1K
R237 50/60Hz LVDS 1K
R207 HD 1K
R209 NON_DVB_T2 1K
R212 PHM_OFF 1K
R227 NO_FRC 1K

RSDS Power OPT

VDD_RSDS:88mA
OPT
L213 BLM18PG121SN1D
L214 BLM18PG121SN1D
+2.5V_Normal
VDD33
FRC
C4005 0.1uF

VDDC 1.26V

+1.26V_VDDC
VDDC : 2026mA
C228 10uF
C275 10uF
C276 10uF
C277 0.1uF
C280 0.1uF
C283 0.1uF
C292 0.1uF
C299 0.1uF
C4006 0.1uF
C4011 0.1uF
C4012 0.1uF
C4013 0.1uF
C4014 0.1uF
C4024 0.1uF
OPT

Normal Power 3.3V

+3.3V_Normal
VDD33
VDD33_T/VDDP/U3_VD33_2:47mA
L204 BLM18PG121SN1D
AVDD_MEMPLL:24mA
AU33:31mA
FRC_AVDD:60mA
L215 BLM18PG121SN1D
AU33
+2.5V_Normal
FRC_AVDD
L221 BLM18PG121SN1D
FRC
FRC_MPLL:4mA
FRC_VDD33_DDR:50mA
FRC_VDD33_DDR
FRC_LPLL:13mA
FRC_LPLL
L206 BLM18PG121SN1D
VDD33_DVI:163mA
VDD33_DVI
AVDD_DMPLL
L207 BLM18PG121SN1D
L217 BLM18PG121SN1D
AVDD_DMPLL/AVDD_NODIE:7.362mA

Normal 2.5V

AVDD2P5/ADC2P5:162mA
+2.5V_Normal
AVDD2P5
L211 BLM18PG121SN1D
AU25
L212 BLM18PG121SN1D
AU25:10mA
AVDD25_PGA
L219 BLM18PG121SN1D
AVDD25_PGA:13mA

DDR3 1.5V

+1.5V_DDR
AVDD_DDR0:55mA
AVDD_DDR0
AVDD_DDR1:55mA
L280 BLM18PG121SN1D
AVDD_DDR_FRC:55mA
+1.5V_FRC_DDR
L281 BLM18PG121SN1D
AVDD_DDR_FRC
MVREF
R4014 1K 1/10W 1%
R4015 1K 1/10W 1%

RSDS Power OPT

+1.26V_VDDC
L228 BLM18SG700TN1D
MIU0VDDC
MIU1VDDC
L226 BLM18SG700TN1D
L225 BLM18SG700TN1D
FRC
FRCVDDC

S7M-PLUS_DivX_MS10 IC101 LGE107DC-RP [S7M+ DIVX/MS10]

HDMI
CK+_HDMI1 F1 A_RXCP
CK-_HDMI1 F2 A_RXCN
D0+_HDMI1 G2 A_RX0P
D0-_HDMI1 G3 A_RX0N
D1+_HDMI1 H3 A_RX1P
D1-_HDMI1 G1 A_RX1N
D2+_HDMI1 H1 A_RX2P
D2-_HDMI1 H2 A_RX2N
DDC_SDA_1 F5 DDCDA_DA/GPIO24
DDC_SCL_1 F4 DDCDA_CK/GPIO23
HPD1 E6 HOTPLUGA/GPIO19
CK+_HDMI2 D3 B_RXCP
CK-_HDMI2 C1 B_RXCN
D0+_HDMI2 D1 B_RX0P
D0-_HDMI2 D2 B_RX0N
D1+_HDMI2 E2 B_RX1P
D1-_HDMI2 E3 B_RX1N
D2+_HDMI2 F3 B_RX2P
D2-_HDMI2 E1 B_RX2N
DDC_SDA_2 D4 DDCDB_DA/GPIO26
DDC_SCL_2 E4 DDCDB_CK/GPIO25
HPD2 D5 HOTPLUGB/GPIO20
CK+_HDMI4 AA2 C_RXCP
CK-_HDMI4 AA1 C_RXCN
D0+_HDMI4 AB1 C_RX0P
D0-_HDMI4 AA3 C_RX0N
D1+_HDMI4 AB3 C_RX1P
D1-_HDMI4 AB2 C_RX1N
D2+_HDMI4 AC2 C_RX2P
D2-_HDMI4 AC1 C_RX2N
DDC_SDA_4 AB4 DDCDC_DA/GPIO28
DDC_SCL_4 AA4 DDCDC_CK/GPIO27
HPD4 AC3 HOTPLUGC/GPIO21
CEC_REMOTE_S7 D6 CEC/GPIO5

DSUB
SCART1_RGB/COMP1
DSUB_HSYNC G5 HSYNC0
DSUB_VSYNC G6 VSYNC0
DSUB_R+ R228 33 C204 0.047uF K1 RIN0P
R229 68 C205 0.047uF L3 RIN0M
DSUB_G+ R230 33 C206 0.047uF K3 GIN0P
R231 68 C207 0.047uF K2 GIN0M
DSUB_B+ R232 33 C208 0.047uF J3 BIN0P
R233 68 C209 0.047uF J2 BIN0M
C210 1000pF J1 SOGIN0
R4024 22
R4025
R4026 10K
R4023 10K
SC1_ID G4 HSYNC1
SC1_FB H6 VSYNC1
SC1_R+/COMP1_Pr+ R253 33 C211 0.047uF K5 RIN1P
R254 68 C212 0.047uF K4 RIN1M
SC1_G+/COMP1_Y+ R255 33 C213 0.047uF J4 GIN1P
R256 68 C214 0.047uF K6 GIN1M
SC1_B+/COMP1_Pb+ R257 33 C215 0.047uF H4 BIN1P
R258 68 C216 0.047uF J6 BIN1M
SC1_SOG_IN C217 1000pF J5 SOGIN1
R236 0
NON_EU

COMP2
H5 HSYNC2
COMP2_Pr+ R237 33 C218 0.047uF N3 RIN2P
R238 68 C219 0.047uF N2 RIN2M
COMP2_Y+ R239 33 C220 0.047uF M2 GIN2P
R240 68 C221 0.047uF M1 GIN2M
COMP2_Pb+ R241 33 C222 0.047uF L2 BIN2P
R242 68 C223 0.047uF L1 BIN2M
C224 1000pF M3 SOGIN2
C248 0.047uF

CVBS In/OUT
TU_CVBS R244 33 C225 0.047uF N4 CVBS0P
SC1_CVBS_IN R245 33 C226 0.047uF N6 CVBS1P
AV_CVBS_IN R246 33 C227 0.047uF L4 CVBS2P
SIDEAV_CVBS_IN R401 33 C405 0.047uF L5 CVBS3P
R248 33 C229 0.047uF L6 CVBS4P
AV_CVBS_IN2 R249 33 C230 0.047uF M4 CVBS5P
R250 33 C231 0.047uF M5 CVBS6P
R251 33 C232 0.047uF K7 CVBS7P
Delete CHB_CVBS_IN
C203 1000pF OPT
TP210 M6 CVBS_OUT1
DTV/MNT_VOUT M7 CVBS_OUT2
R252 68 C233 0.047uF N5 VCOM0
Close to MSTAR
AV_CVBS_IN2 TP211

S7M-PLUS_DivX_MS10 IC101 LGE107DC-RP [S7M+ DIVX/MS10]

Close to MSTAR
DTV_IF
R288 100 C257 0.1uF IF_P_MSTAR
R289 100 C258 0.1uF IF_N_MSTAR
C250 0.1uF R4002 47
C251 0.1uF R4003 47
TU_SIF
ANALOG SIF
Close to MSTAR
C252 OPT 1000pF

VIFP W2 TP201
VIFM W1 TP202
IP V2
IM V1
SSIF/SIFP Y2
SSIF/SIFM Y1
QP U3 TP203
QM V3 TP204
IFAGC Y5
RF_TAGC Y4 TP205
TGPIO0/UPGAIN U1
TGPIO1/DNGAIN U2
TGPIO2/I2C_CLK R3
TGPIO3/I2C_SDA T3
XTALIN T2
XTALOUT T1
SPDIF_IN/GPIO177 G14
SPDIF_OUT/GPIO178 G13
DM_P0 B7
DP_P0 A7
DM_P1 AF17
DP_P1 AE17
I2S_IN_BCK/GPIO175 F14
I2S_IN_SD/GPIO176 F13
I2S_IN_WS/GPIO174 F15
I2S_OUT_BCK/GPIO181 D20
I2S_OUT_MCK/GPIO179 E20
I2S_OUT_SD/GPIO182 D19
I2S_OUT_SD1/GPIO183 F18
I2S_OUT_SD2/GPIO184 E18
I2S_OUT_SD3/GPIO185 D18
I2S_OUT_WS/GPIO180 E19
LINE_IN_0L N1
LINE_IN_0R P3
LINE_IN_1L P1
LINE_IN_1R P2
LINE_IN_2L P4
LINE_IN_2R P5
LINE_IN_3L R6
LINE_IN_3R T6
LINE_IN_4L U5
LINE_IN_4R V5
LINE_IN_5L U6
LINE_IN_5R V6
LINE_OUT_0L U4
LINE_OUT_1L W3
LINE_OUT_1R W4
LINE_OUT_0R Y3
LINE_OUT_2R W5
LINE_OUT_3R TP209
MIC_DET_IN R4
MICCM T5 C234 OPT 2.2uF
MICIN R5 C235 2.2uF OPT
AUCOM T4
VRM P7
VAG R7
VRP P6
HP_OUT_1L R1 L203 5.6uH HEAD_PHONE
HP_OUT_1R R2 L205 5.6uH HEAD_PHONE
CM2012F5R6KT
ET_RXD0 E21
ET_TXD0 E22
ET_RXD1 D21
ET_TXD1 F21
ET_REFCLK E23
ET_TX_EN D22
ET_MDC F22
ET_MDIO D23
ET_CRS F23
AVLINK F8
IRINT G8
TESTPIN K8
RESET A4
U3_RESET Y17

+3.3V_Normal
L227 BLM18PG121SN1D
C4064 0.1uF
R4019 1K
Close to MSTAR
R4020 10K
C4065 0.022uF 16V
IF_AGC_MAIN
TU/DEMOD_I2C
AMP_SCL
AMP_SDA
FULL_NIM R291 22
FULL_NIM R292 22
DEMOD_SCL
DEMOD_SDA
TU_SCL
TU_SDA
R287 1M
X201 24MHz
C261 27pF
C262 27pF
LED_DRIVER_D/L_SDA
R296 100
SPDIF_OUT
B/T USB
SIDE_USB_DM
SIDE_USB_DP
SIDE USB
NEC_SDA
COMP2_DET
NEC_SCL
I2S_I/F
AUD_SCK
AUD_MASTER_CLK_0
AUD_LRCH
LED_DRIVER_D/L_SCL
MODEL_OPT_4
MODEL_OPT_5
AUD_LRCK
AUDIO IN
SC1/COMP1_L_IN
SC1/COMP1_R_IN
AV_L_IN
AV_R_IN
SIDEAV_L_IN
SIDEAV_R_IN
COMP2_L_IN
COMP2_R_IN
PC_L_IN
PC_R_IN
C236 2.2uF
C237 2.2uF
C238 2.2uF
C239 2.2uF
C4059 2.2uF
C4060 2.2uF
C242 2.2uF
C243 2.2uF
C244 2.2uF
C245 2.2uF
C246 2.2uF OPT
C247 2.2uF OPT
AUDIO OUT
SCART1_Lout
SCART1_Rout
TP207
TP208
L202 BLM18SG121TN1D
C249 4.7uF
C253 1uF
C256 0.1uF
C263 10uF
H/P OUT
HP_LOUT
HP_ROUT
HEAD_PHONE
C268 4.7uF
C272 4.7uF
ET_RXD0
ET_TXD0
ET_RXD1
ET_TXD1
ET_REF_CLK
ET_TX_EN
ET_MDC
ET_MDIO
ET_CRS
TP206
R298 OPT 100
IR
SOC_RESET
FRC R205 22
+3.3V_Normal
10K
FRC_RESET
R4018 FRC 22
R4017 10K OPT
R4006 10K

S7M-PLUS_DivX_MS10 IC101 LGE107DC-RP [S7M+ DIVX/MS10]

+1.26V_VDDC
VDDC_1 H11
VDDC_2 H12
VDDC_3 H13
VDDC_4 H14
VDDC_5 H15
VDDC_6 J12
VDDC_7 J13
VDDC_8 J14
VDDC_9 J15
VDDC_10 J16
VDDC_11 L18
A_DVDD H16 MIU0VDDC
B_DVDD K19 MIU1VDDC
FRC_VDDC_0 L19
FRC_VDDC_1 M18
FRC_VDDC_2 M19
FRC_VDDC_3 N18
FRC_VDDC_4 N19
FRC_VDDC_5 N20
FRC_VDDC_6 P18
FRC_VDDC_7 P19
FRC_VDDC_8 P20
U3_DVDD_DDR Y12 FRCVDDC
AVDD1P2 J11
DVDD_NODIE L7 C4045 1uF
AVDD2P5_ADC_1 H7
AVDD2P5_ADC_2 J7
AVDD25_REF J8
AVDD_AU25 L8 AU25
PVDD_1 W15 AVDD2P5
PVDD_2 Y15 AVDD2P5
AVDD25_PGA U8
AVDD_NODIE M8 AVDD_DMPLL
AVDD_DVI_1 N9 VDD33_DVI
AVDD_DVI_2 P9
AVDD3P3_CVBS N8
AVDD_DMPLL P8 AVDD_DMPLL
AVDD_AU33 T7 AU33
AVDD_EAR33 U7
AVDD33_T T9 VDD33
VDDP_1 R8
VDDP_2 R9
VDDP_3 T8
FRC_VD33_2_1 V20
FRC_VD33_2_2 W20
FRC_AVDD_RSDS_1 U19 VDD_RSDS
FRC_AVDD_RSDS_2 U20
FRC_AVDD_RSDS_3 V19
FRC_AVDD W19 FRC_AVDD
FRC_AVDD_LPLL U18 FRC_LPLL
FRC_AVDD_MPLL T20
FRC_VDD33_DDR Y14
AVDD_MEMPLL W19 VDD33
FRC_AVDD_MEMPLL W14
AVDD_DDR0_D_1 D15
AVDD_DDR0_D_2 D16
AVDD_DDR0_D_3 E15
AVDD_DDR0_D_4 E16
AVDD_DDR0_C E17
AVDD_DDR1_D_1 F16 AVDD_DDR0
AVDD_DDR1_D_2 F17
AVDD_DDR1_D_3 G16
AVDD_DDR1_D_4 G17
AVDD_DDR1_C H17
FRC_AVDD_DDR_D_1 AB11 AVDD_DDR_FRC
FRC_AVDD_DDR_D_2 AB12
FRC_AVDD_DDR_D_3 AC11
FRC_AVDD_DDR_D_4 AC12
FRC_AVDD_DDR_C AA12
MVREF G15
NC_1 Y7
NC_2 Y8
GND_1 G18
GND_2 H9
GND_3 H10
GND_4 H18
GND_5 H19
GND_6 J10
GND_7 J17
GND_8 J18
GND_9 J19
GND_10 K9
GND_11 K10
GND_12 K11
GND_13 K12
GND_14 K13
GND_15 K14
GND_16 K15
GND_17 K16
GND_18 K17
GND_19 K18
GND_20 L9
GND_21 L10
GND_22 L11
GND_23 L12
GND_24 L13
GND_25 L14
GND_26 L15
GND_27 L16
GND_28 L17
GND_29 M9
GND_30 M10
GND_31 M11
GND_32 M12
GND_33 M13
GND_34 M14
GND_35 M15
GND_36 M16
GND_37 M17
GND_38 N10
GND_39 N11
GND_40 N12
GND_41 N13
GND_42 N14
GND_43 N15
GND_44 N16
GND_45 N17
GND_46 P10
GND_47 P11
GND_48 P12
GND_49 P13
GND_50 P14
GND_51 P15
GND_52 P16
GND_53 P17
GND_54 R10
GND_55 R11
GND_56 R12
GND_57 R13
GND_58 R14
GND_59 R15
GND_60 R16
GND_61 R17
GND_62 R18
GND_63 T10
GND_64 T11
GND_65 T12
GND_66 T13
GND_67 T14
GND_68 T15
GND_69 T16
GND_70 T17
GND_71 T18
GND_72 U10
GND_73 U11
GND_74 U12
GND_75 U13
GND_76 U14
GND_77 U15
GND_78 U16
GND_79 U17
GND_80 V7
GND_81 V8
GND_82 V9
GND_83 V10
GND_84 V11
GND_85 V12
GND_86 V13
GND_87 V14
GND_88 V15
GND_89 V16
GND_90 V17
GND_91 V18
GND_92 W7
GND_93 W8
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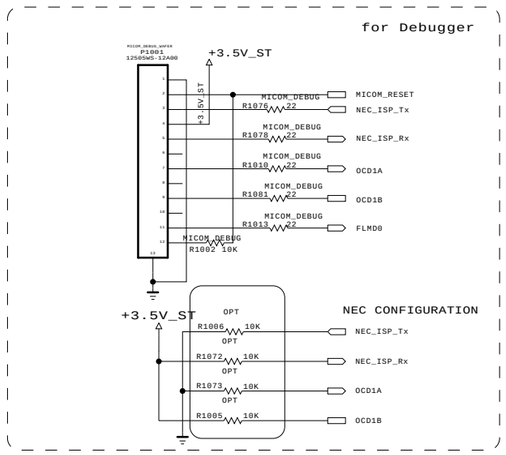
GND_96 W11
GND_97 W12
GND_98 W13
GND_99 W16
GND_100 W17
GND_101 W18
GND_102 Y13
GND_103 Y18
GND_104 AA13
GND_105 AB13
GND_106 AC13
GND_107 D17
GND_108 H23
GND_109 AF13
GND_110 J9
GND_FU U9 BLM18SG121TN1D L223
PGA_VCOM

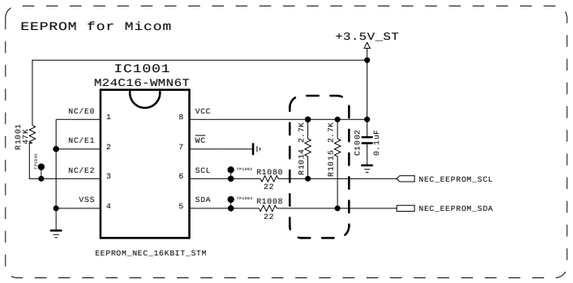
THE ⚠ SYMBOL MARK OF THIS SCHEMETIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFATURES SPECFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE ⚠ SYMBOL MARK OF THE SCHEMETIC.

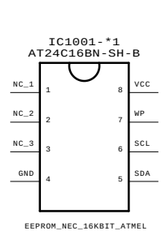
SECRET
LGElectronics

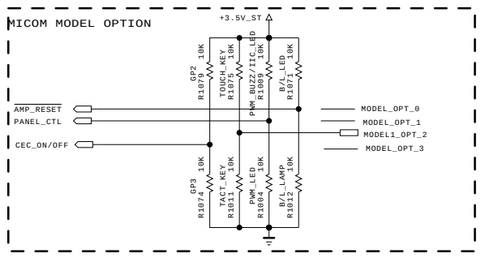
LG ELECTRONICS

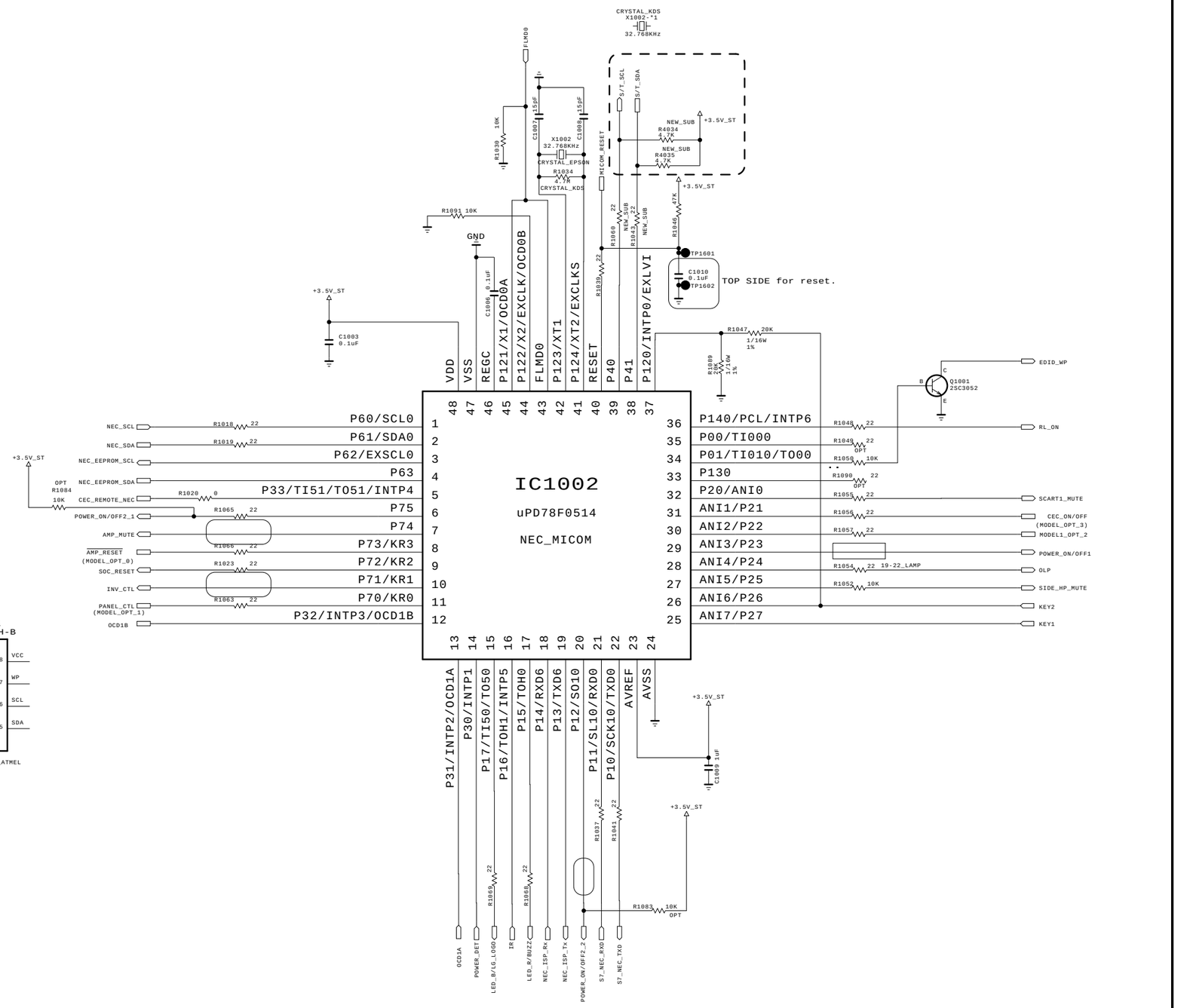
MODEL	GP2R	DATE	20101023
BLOCK	MAIN2, HW OPT	SHEET	2 /











2011Y,GP2R, 101125 Update

MODEL OPTION

PIN NAME	PIN NO.	HIGH	LOW
MODEL_OPT_0	8	B/L_LED	B/L_LAMP
MODEL_OPT_1	11	PWM_BUZZ/IIC_LED	PWM_LED
MODEL_OPT_2	30	TOUCH_KEY	TACT_KEY
MODEL_OPT_3	31	GP2	GP3

PWM_BUZZ/IIC_LED :Using IIC for LED Breathing & PWM Buzz
PWM_LED : Using PWM Signal for LED Lighting

MODEL_OPT_0	MODEL_OPT_1	MODEL_OPT_2	MODEL_OPT_3	Description
LOW	LOW	LOW	LOW	LK330/LK430 for KR/US 10Y EYE-Q Sensor KEY & PWM LED & No Buzz & No LED Blink
LOW	LOW	LOW	HIGH	LK330/LK430/LK530 KEY & PWM LED & No Buzz & No LED Blink
LOW : LED HIGH : LAMP	HIGH	HIGH	LOW	LV25/LV35/LV45/LW45/LV55/LK45/LK55 S/T & IIC LED & NO BUZZ & LED Blink
	HIGH	LOW	LOW	TBD IIC LED(09Y IIC Protocol) & No BUZZ
	LOW	HIGH	LOW	TBD S/T & IIC LED & No Buzz & LED Blink

2010Y,GP2

MODEL OPTION

PIN NAME	PIN NO.	HIGH	LOW
MODEL_OPT_0	8	B/L_LED	B/L_LAMP
MODEL_OPT_1	11	PWM_BUZZ/IIC_LED	PWM_LED
MODEL_OPT_2	30	TOUCH_KEY	TACT_KEY
MODEL_OPT_3	31	GPIO_LED	NON_GPIO_LED

PWM_BUZZ/IIC_LED : For model that use LED Lighting used IIC
PWM_LED : For model that use LED Lighting used PWM Signal

MODEL_OPT_0	MODEL_OPT_1	MODEL_OPT_2	MODEL_OPT_3	
LOW	LOW	LOW	LOW	LD350/450/550 PWM LED & No Buzz & No LED Blink
HIGH	LOW	HIGH	LOW	19/22/26LE5300/5300 IIC LED & PWM IIC BUZZ
HIGH	HIGH	HIGH	LOW	32/37/42/47/55LE5300 IIC LED & PWM BUZZ
LOW	HIGH	LOW	LOW	LD420 IIC LED(09Y IIC Protocol) & No BUZZ
HIGH	LOW	LOW	HIGH	LE7300 GPIO LED & NO BUZZ

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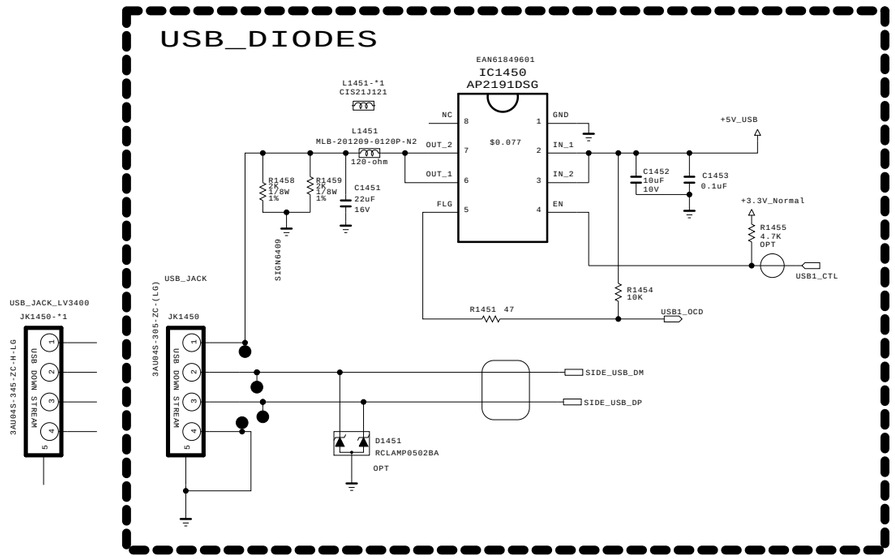
SECRET
LGElectronics

LG ELECTRONICS

MODEL	GP2R	DATE	20101125
BLOCK	MICOM Rev.4	SHEET	5



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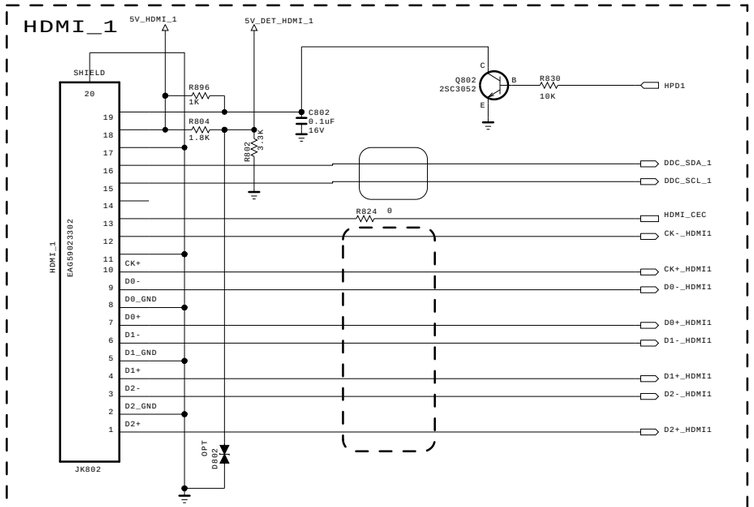
SECRET
LGElectronics

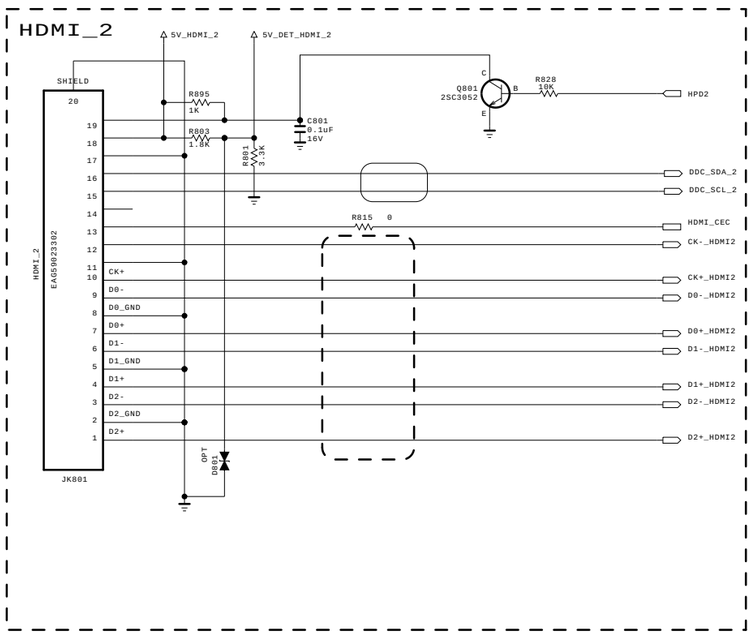
LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	USB_OCP_DIODE	SHEET	7 /

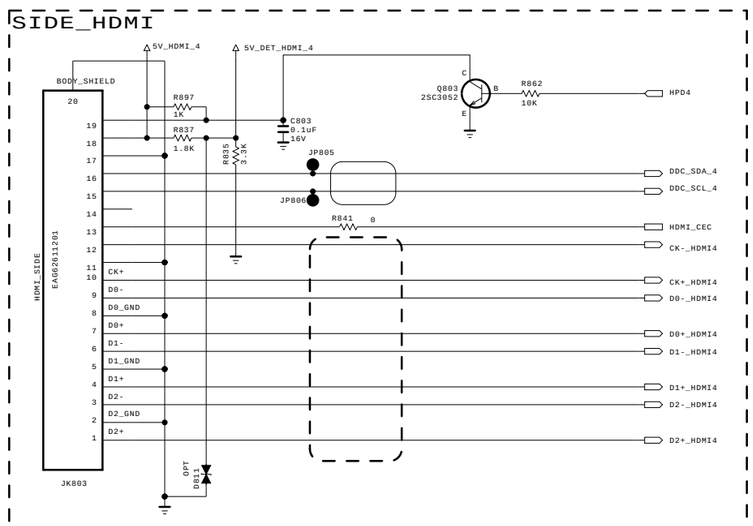
HDMI_1



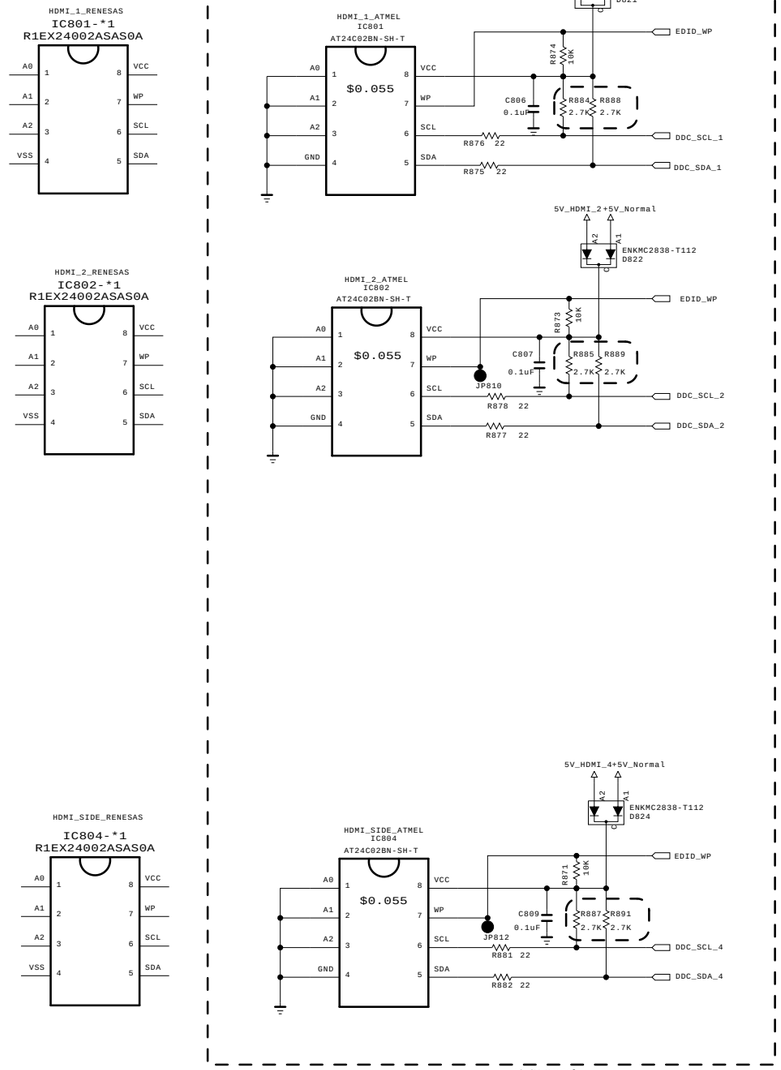
HDMI_2



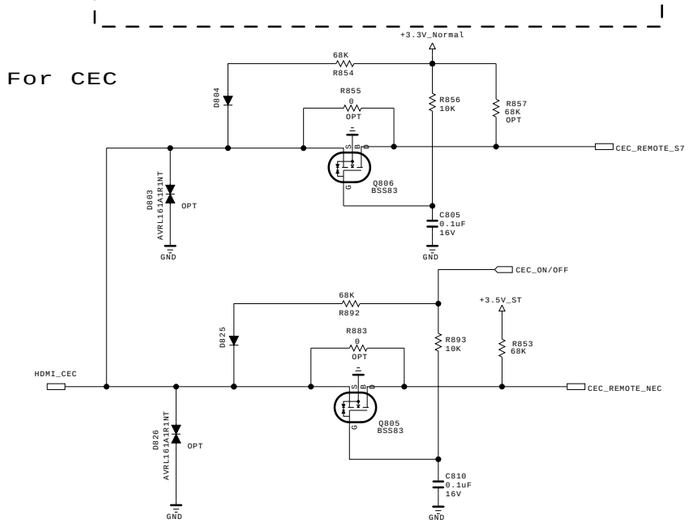
SIDE_HDMI



HDMI EEPROM



For CEC



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SECRET
LGElectronics

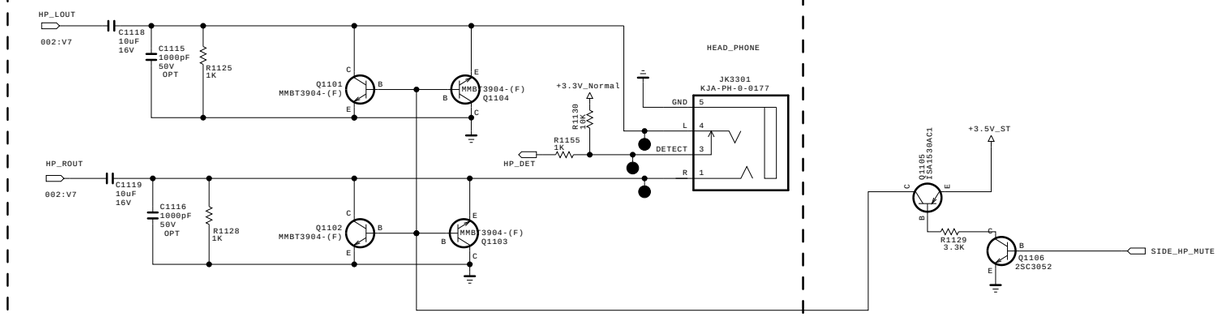
LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	HDMI	SHEET	8

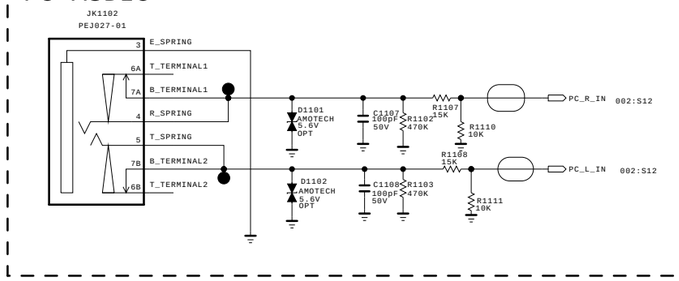
RGB/SPDIF/PC/HP

New Item Development

EARPHONE BLOCK

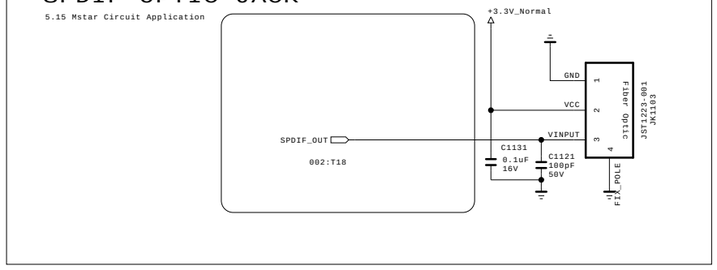


PC AUDIO

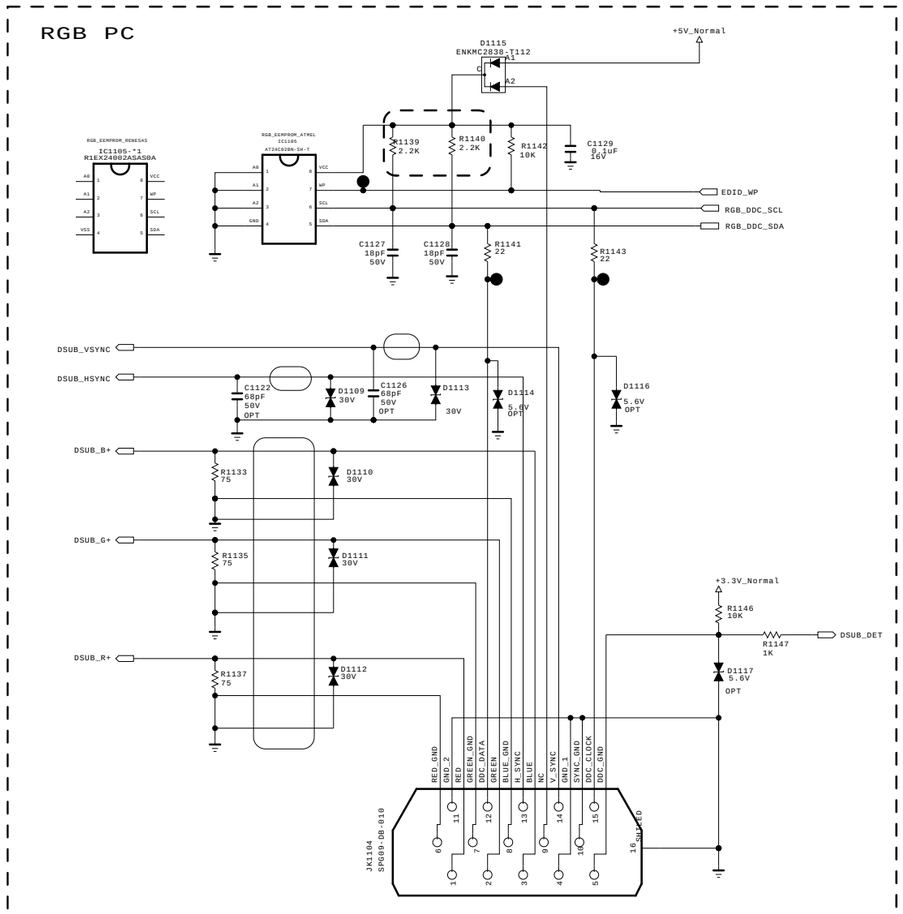


SPDIF OPTIC JACK

5.15 Mstar Circuit Application



RGB PC



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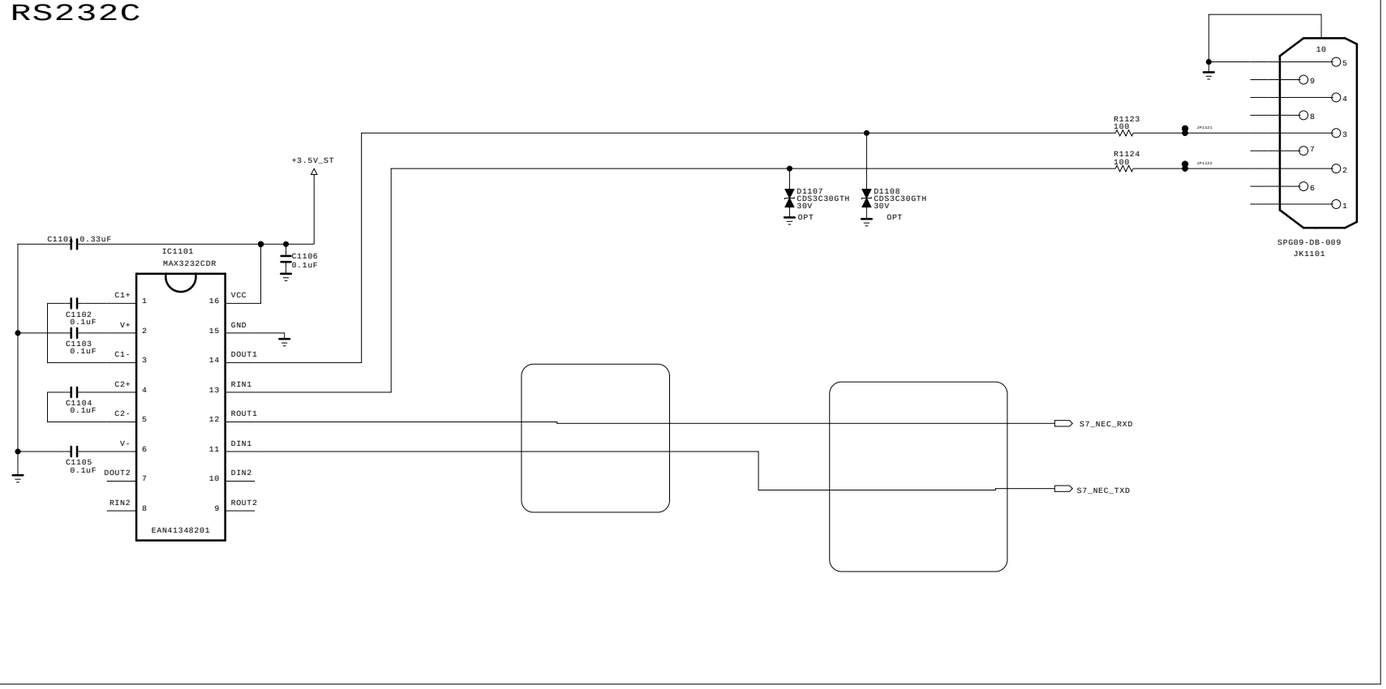
SECRET
LGElectronics

LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	RGB/SPDIF/HP	SHEET	9

RS232C



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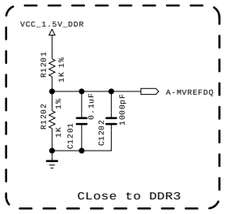
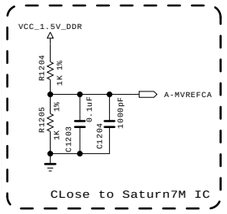
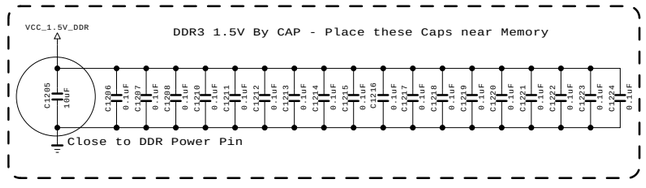
SECRET
LGElectronics

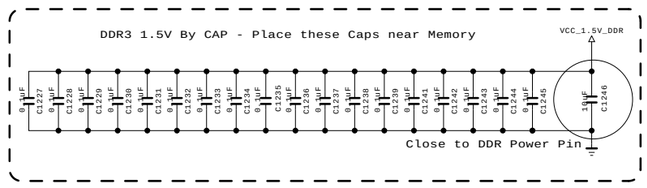
LG ELECTRONICS

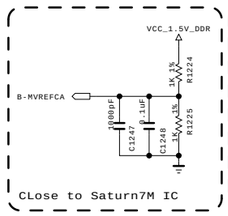
MODEL	GP2R	DATE	20101023
BLOCK	RS232C_9PIN	SHEET	10

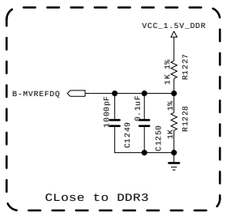


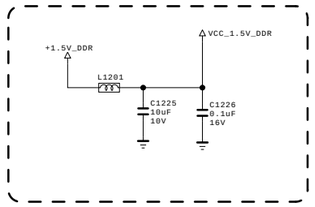


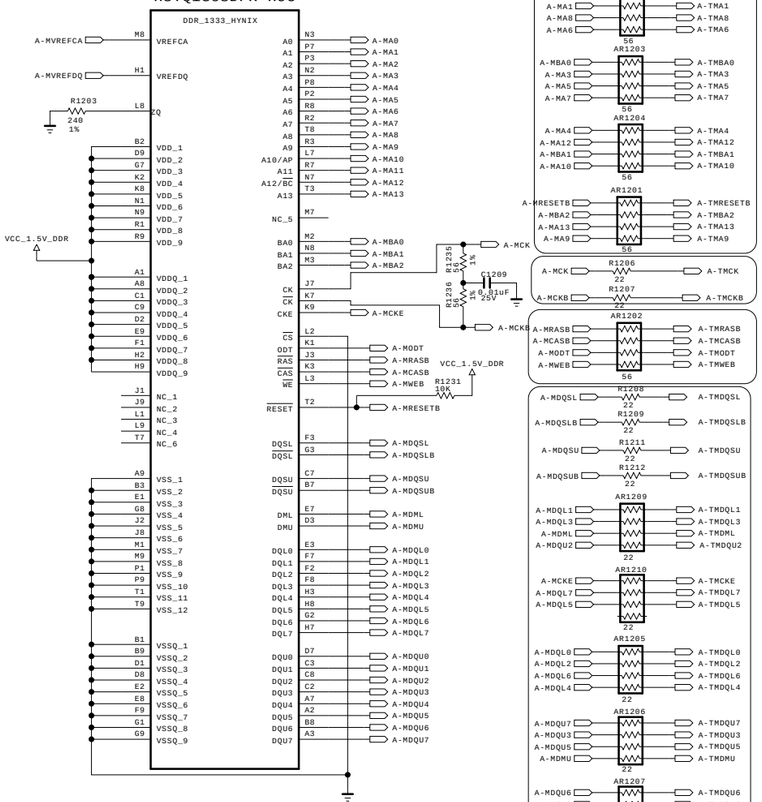


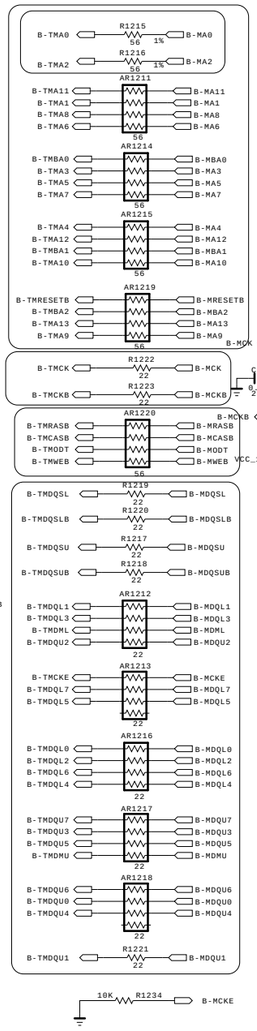


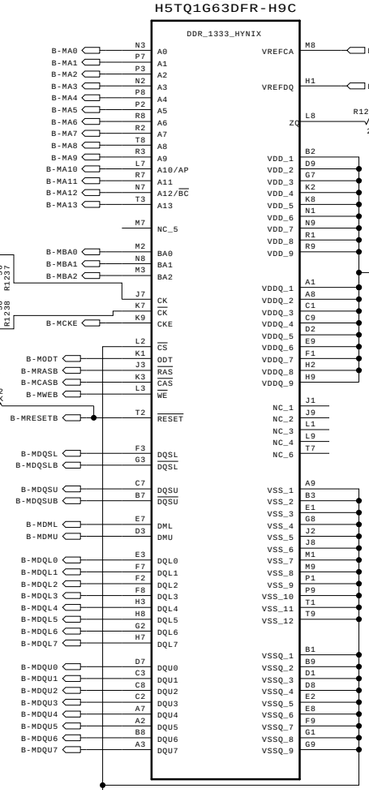


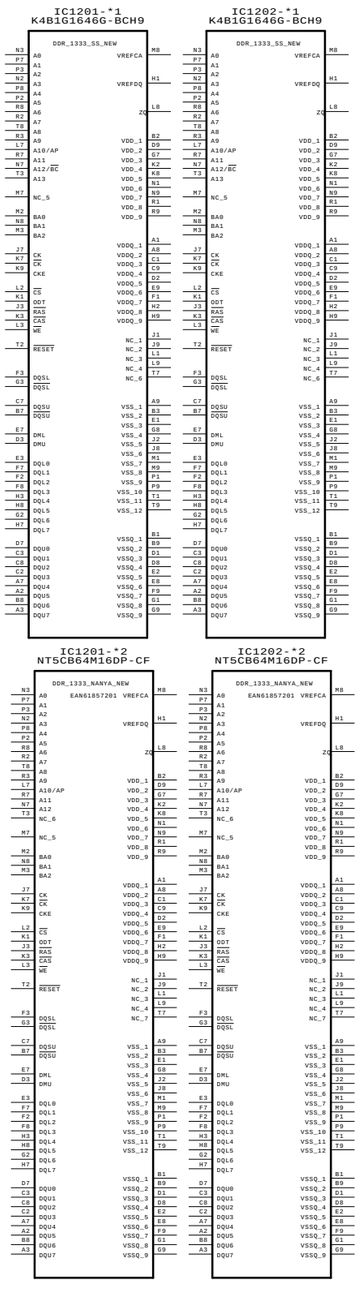












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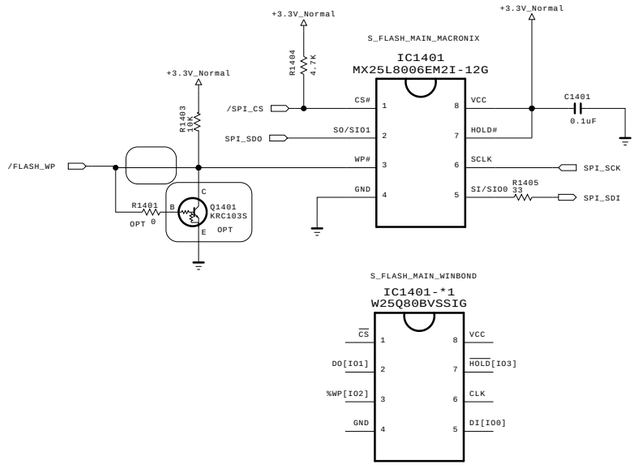
SECRET

LGElectronics

LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	DDR_256	SHEET	12 /



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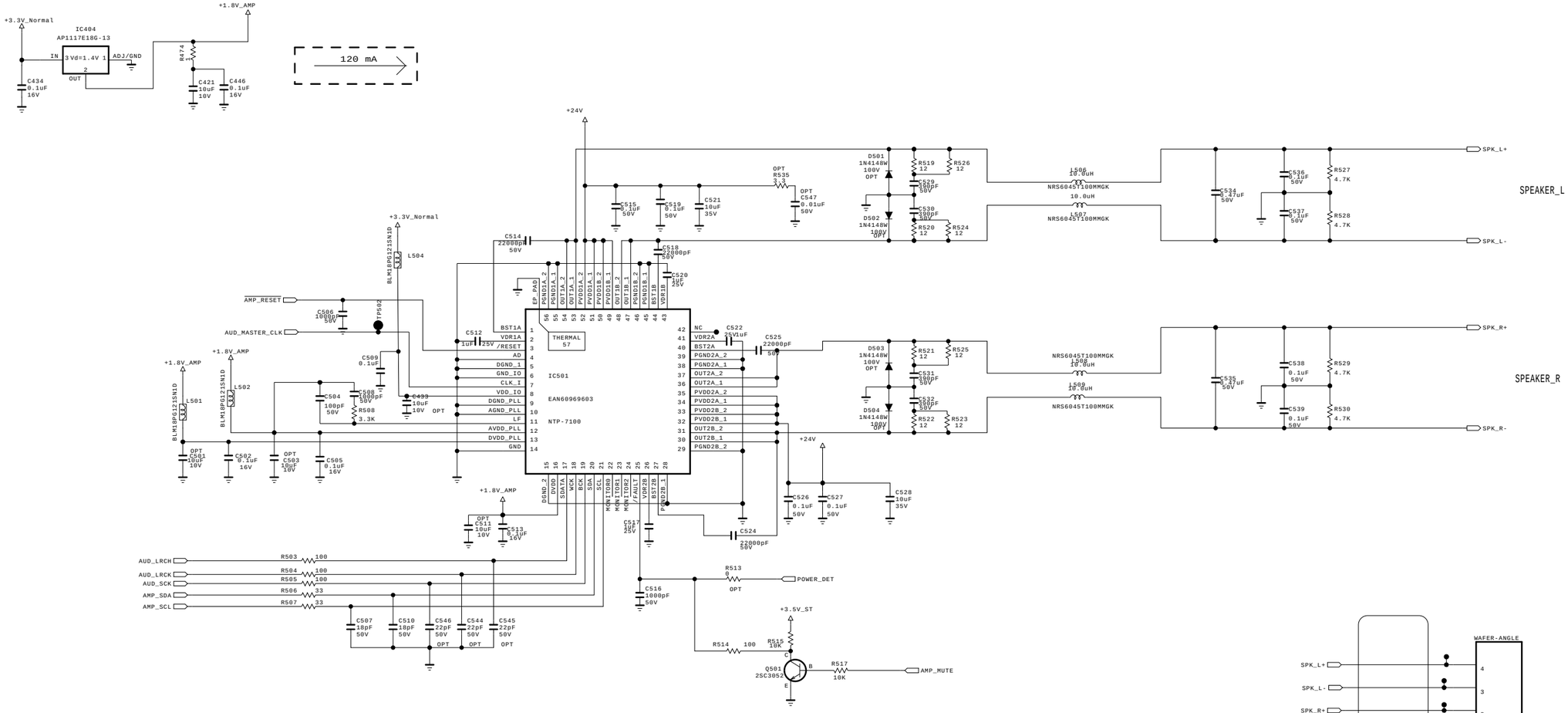
SECRET
LGElectronics

LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	SFLASH_1MB	SHEET	13 /



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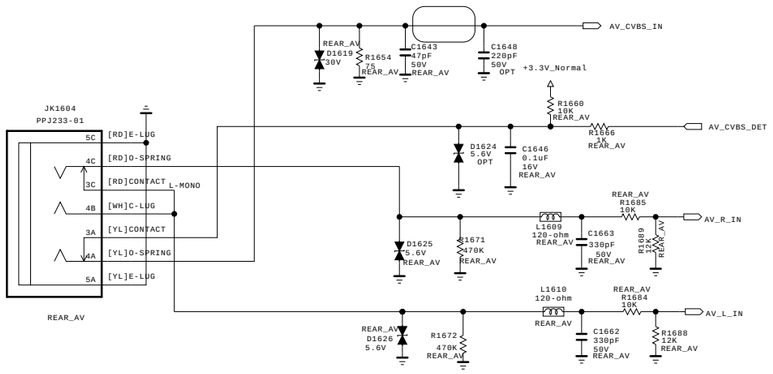
SECRET

LGElectronics

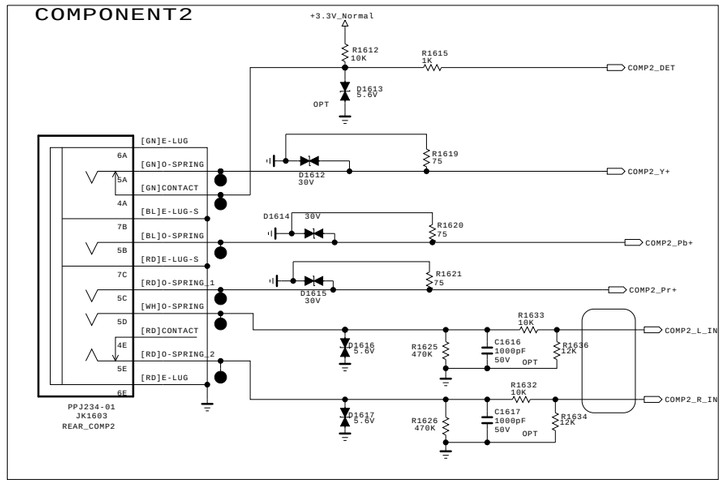
LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	AMP_NTP	SHEET	16 /

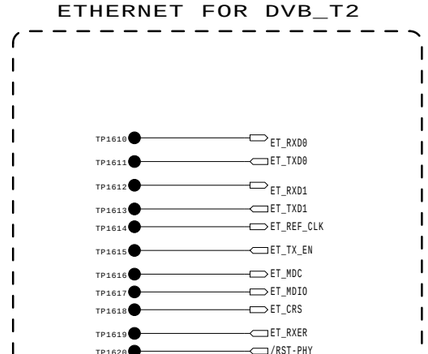
Rear AV



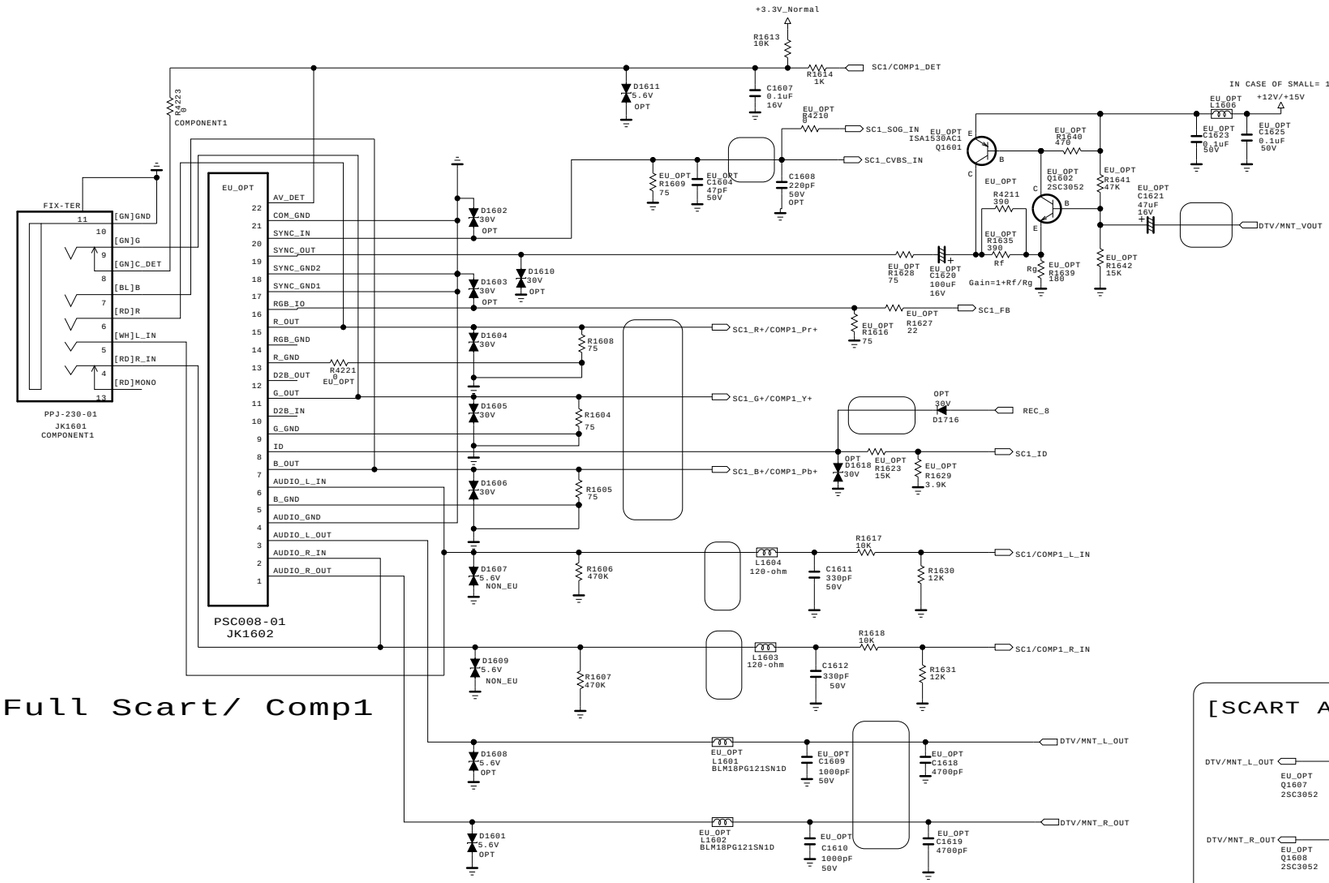
COMPONENT2



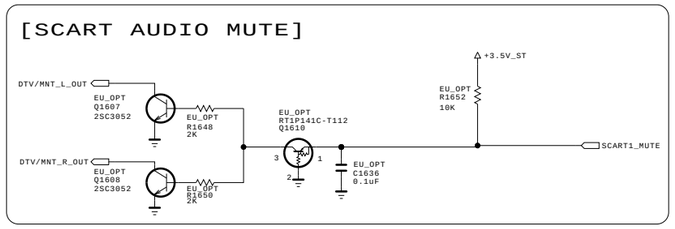
ETHERNET FOR DVB_T2

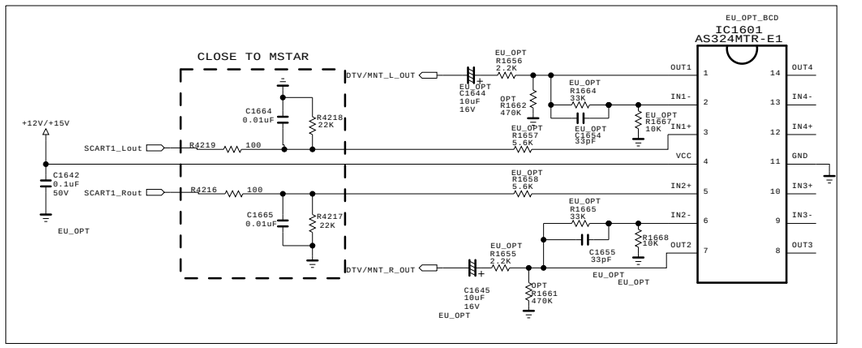


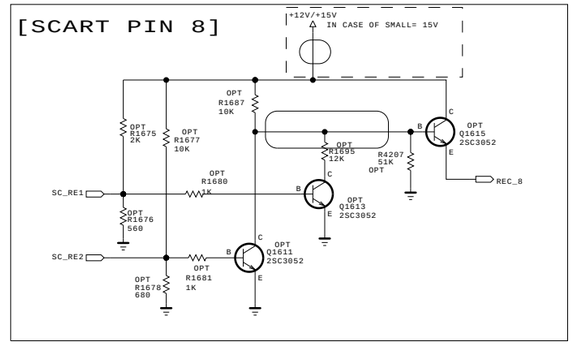
Full Scart/ Comp1



[SCART AUDIO MUTE]







SECRET
LGElectronics

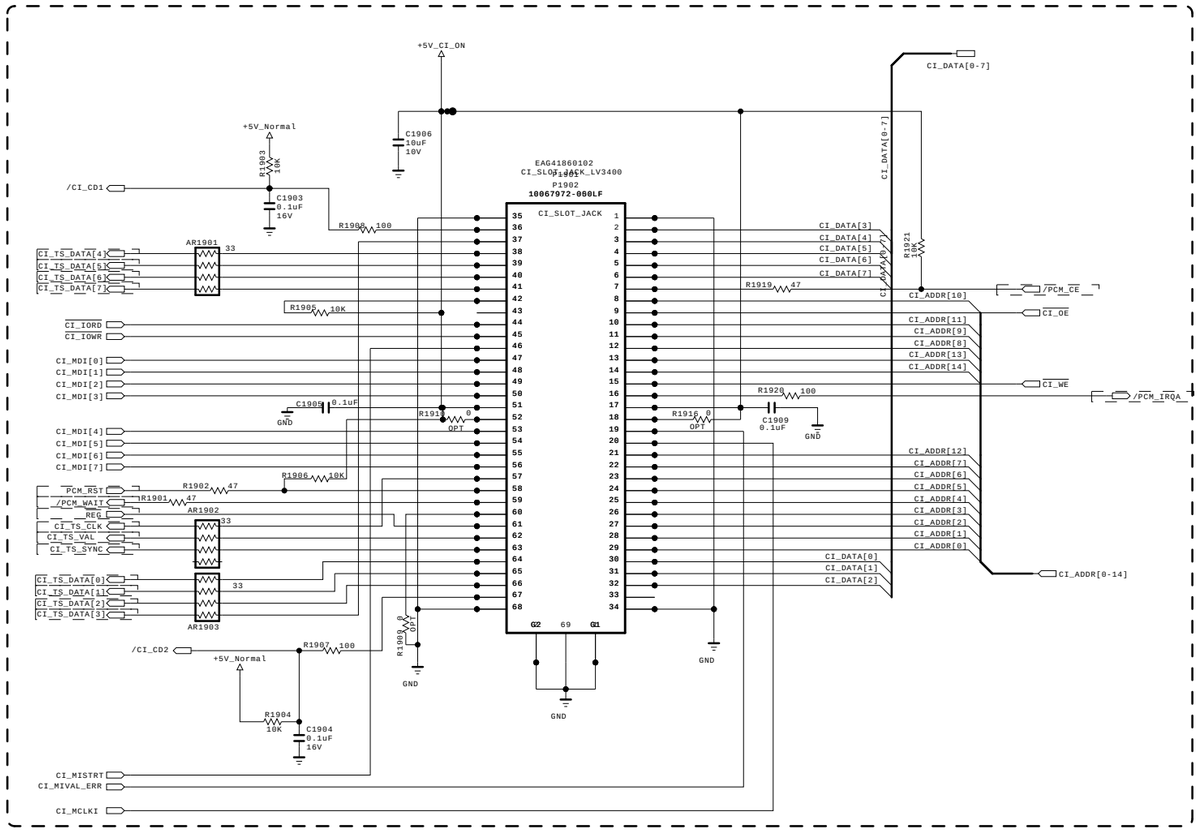
LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	REAR_JACK	SHEET	17 /

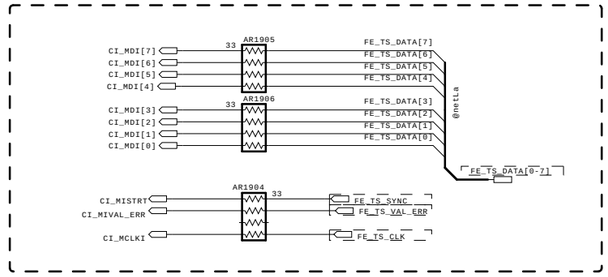
CI Region

* Option name of this page : CI_SLOT
(because of Hong Kong)

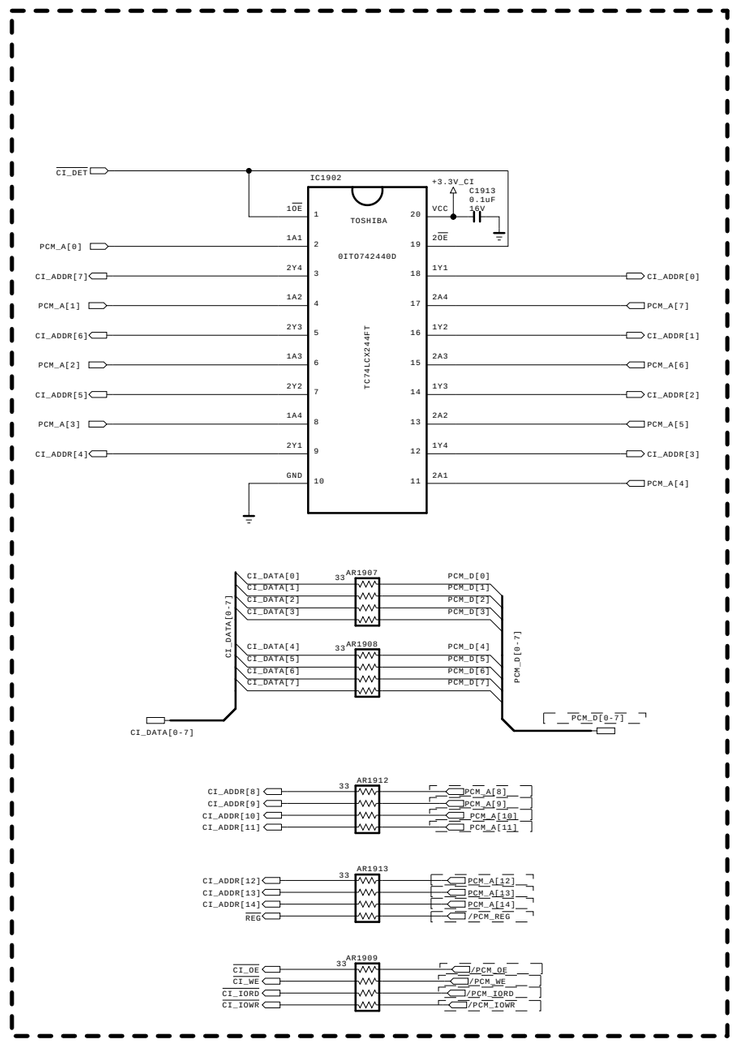
CI SLOT

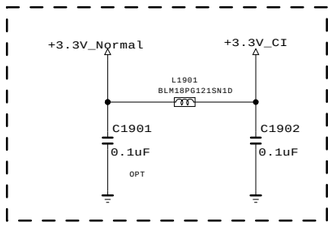


CI TS INPUT

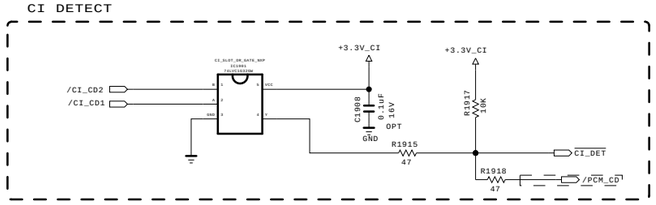


CI HOST I/F

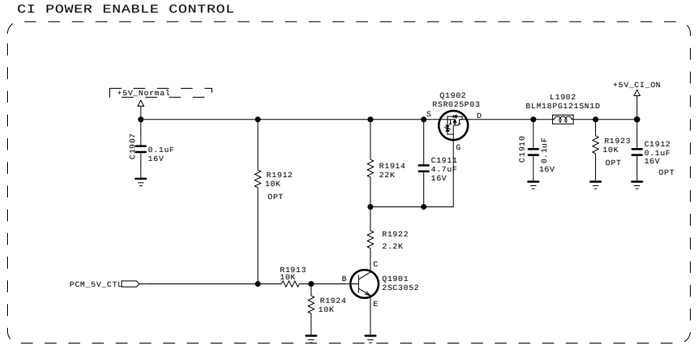




CI DETECT



CI POWER ENABLE CONTROL



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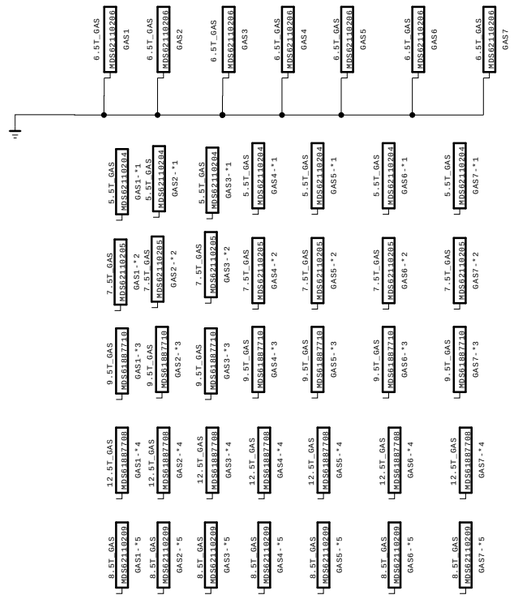
SECRET
LGElectronics

LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	PCMCI	SHEET	20 /

SMD GASKET



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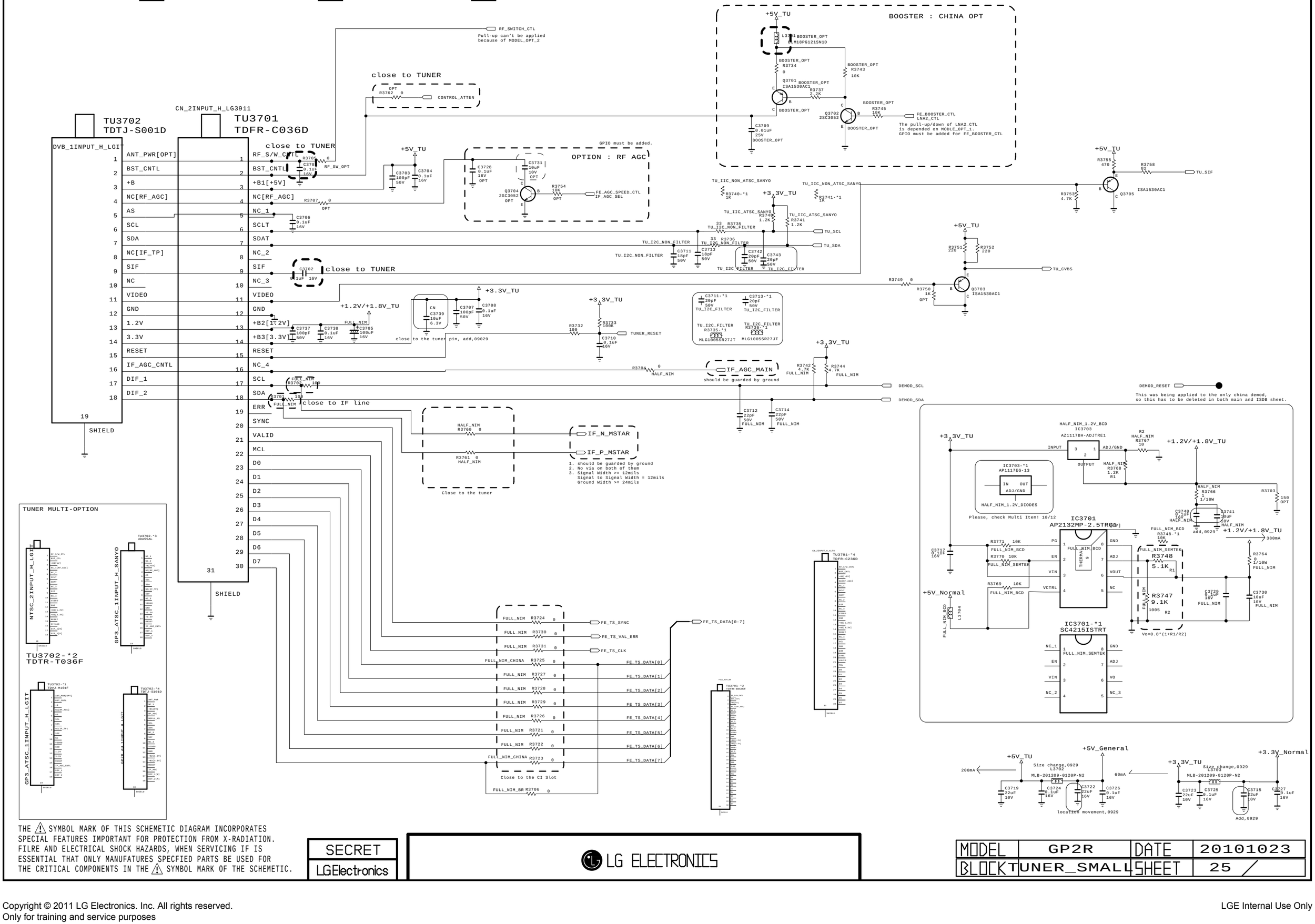
SECRET
LGElectronics

LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	SMD_GAS	SHEET	20 /

GP2R_GLOBAL_TUNER_BLOCK for Small Model



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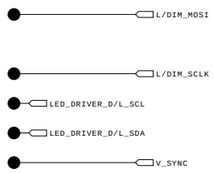
SECRET
LGElectronics

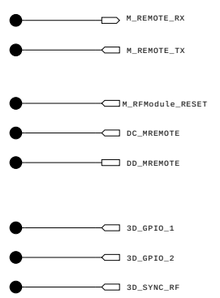
LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	TUNER_SMALL	SHEET	25

NON_L/DIM_LED/DRIVER



NON_3D_SG



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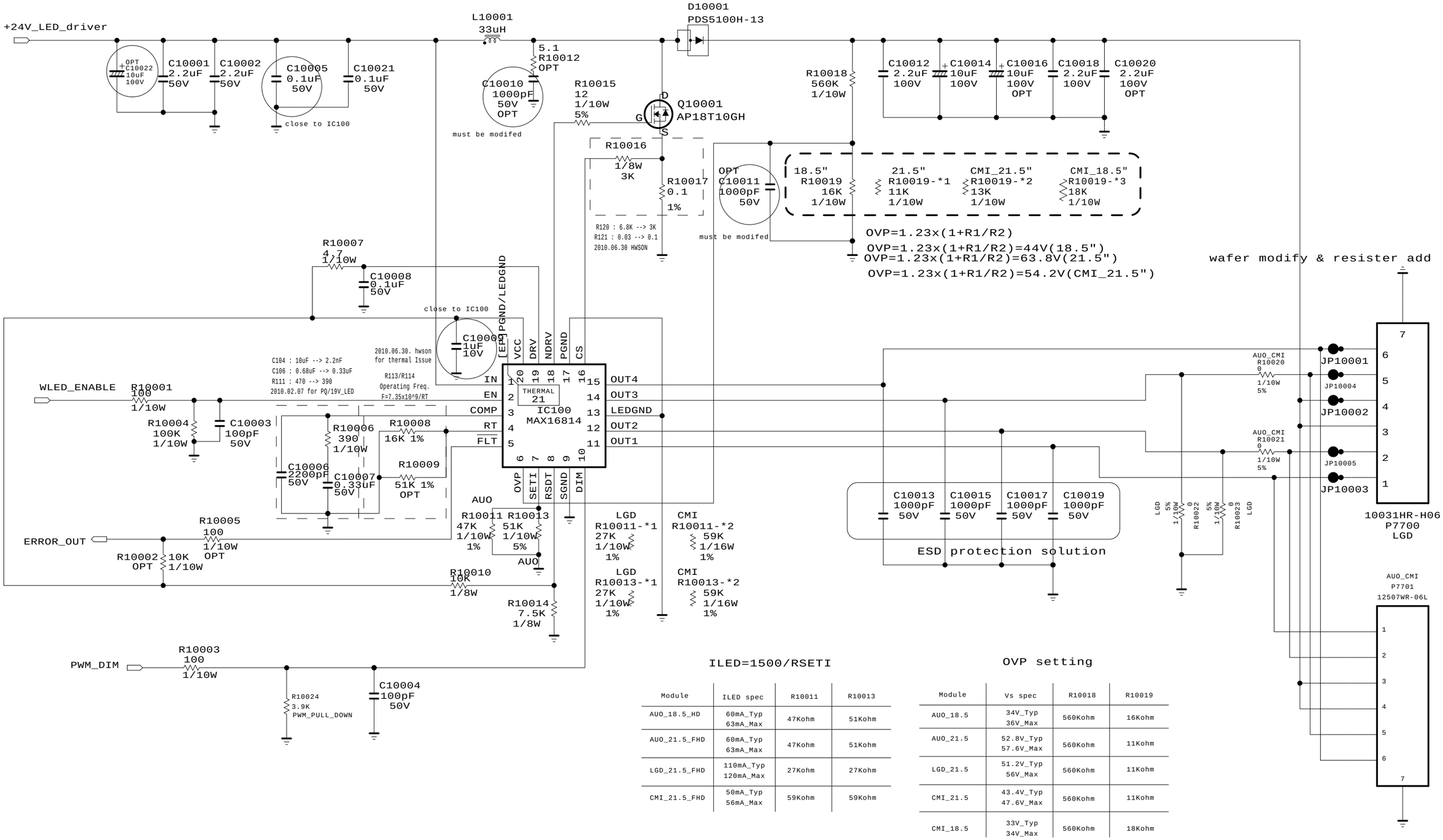
SECRET
LGElectronics

LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	NON_L/DIM	SHEET	26 /

TN_module_LED_DRIVER_CIRCUIT



ILED=1500/RSETI

Module	ILED spec	R10011	R10013
AUO_18.5_HD	60mA_Typ 63mA_Max	47Kohm	51Kohm
AUO_21.5_FHD	60mA_Typ 63mA_Max	47Kohm	51Kohm
LGD_21.5_FHD	110mA_Typ 120mA_Max	27Kohm	27Kohm
CMI_21.5_FHD	50mA_Typ 56mA_Max	59Kohm	59Kohm

OVP setting

Module	Vs spec	R10018	R10019
AUO_18.5	34V_Typ 36V_Max	560Kohm	16Kohm
AUO_21.5	52.8V_Typ 57.6V_Max	560Kohm	11Kohm
LGD_21.5	51.2V_Typ 56V_Max	560Kohm	11Kohm
CMI_21.5	43.4V_Typ 47.6V_Max	560Kohm	11Kohm
CMI_18.5	33V_Typ 34V_Max	560Kohm	18Kohm

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SECRET

LGElectronics

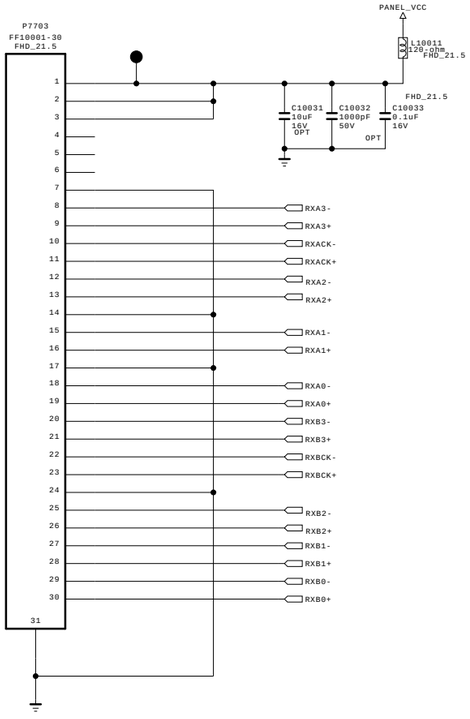
LG ELECTRONICS

MODEL	GP2R	DATE	20101023
BLOCK	TN_DRIVER	SHEET	29

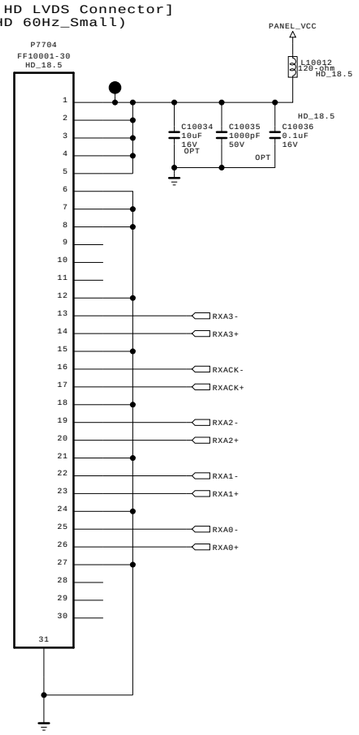
ALL_TN_LED_DRIVER

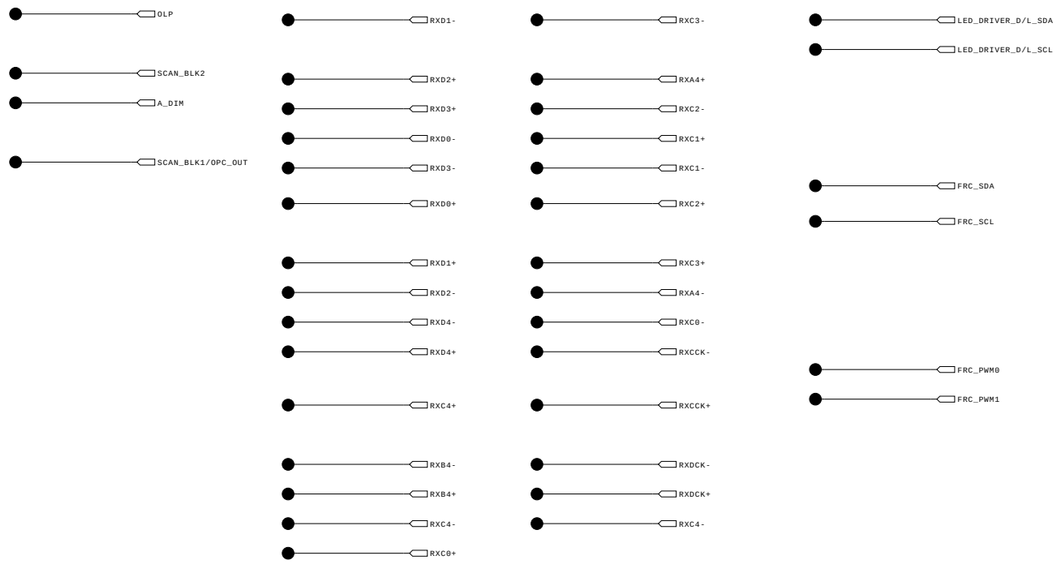
LVDS_TN

[30Pin FHD LVDS Connector]
(For FHD 60Hz_Small)

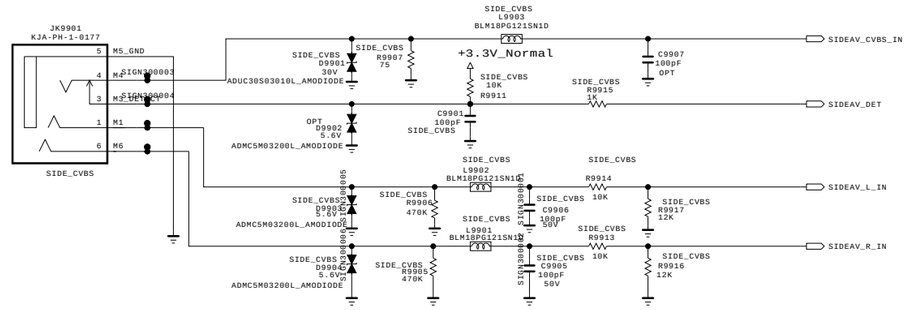


[30Pin HD LVDS Connector]
(For HD 60Hz_Small)





SIDE CVBS PHONE JACK
(New Item Development)



THE ⚠ SYMBOL MARK OF THIS SCHEMETIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFATURES SPECFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE ⚠ SYMBOL MARK OF THE SCHEMETIC.

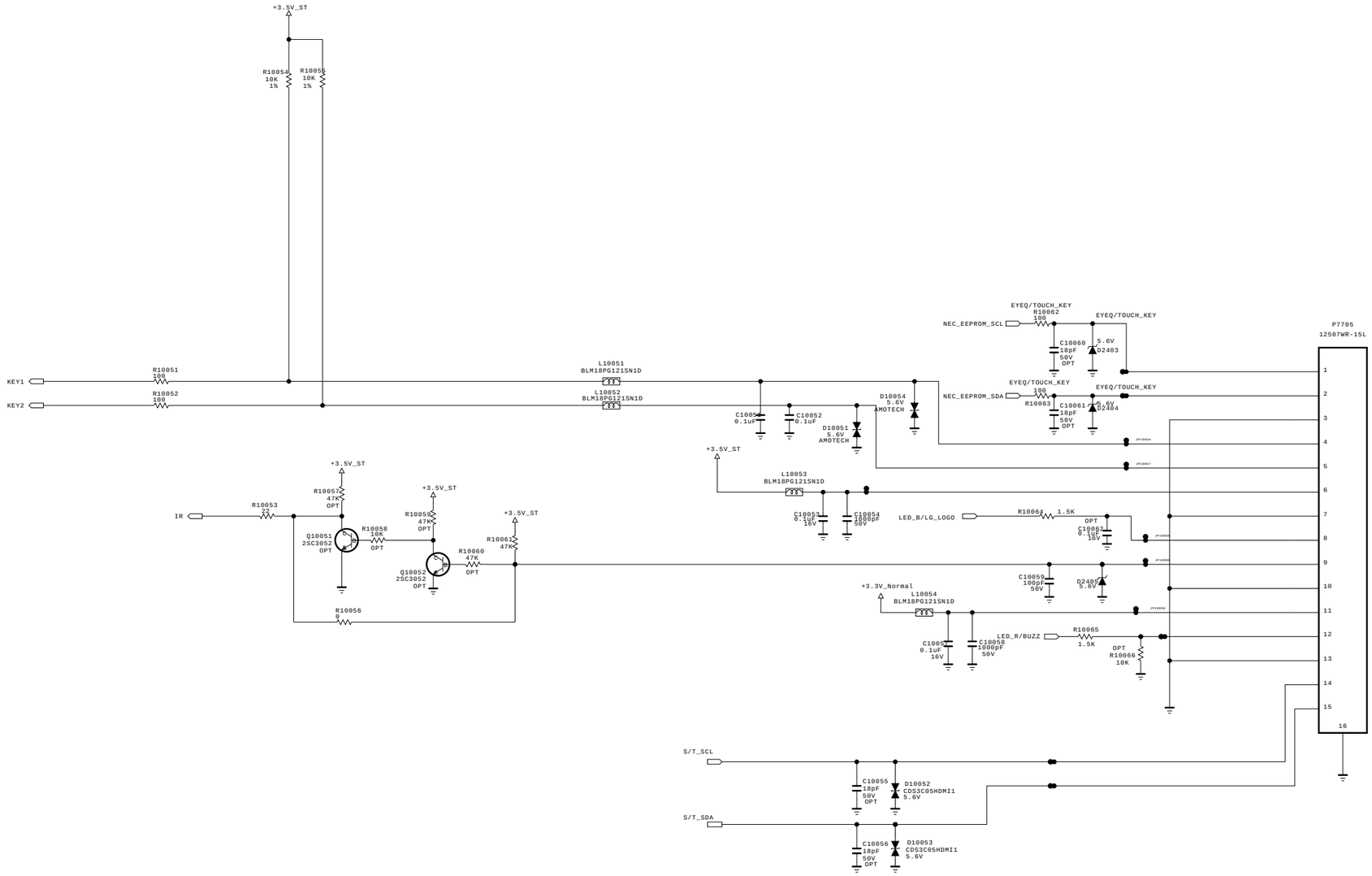
SECRET
LGElectronics

LG ELECTRONICS

MODEL	GP2R	DATE	20101220
BLOCK	TN_LVDS_SIDEAV	SHEET	30 /

SMALL_TN_LVDS

TN CONTROL IR & LED



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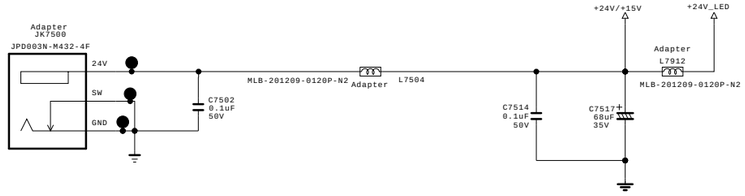
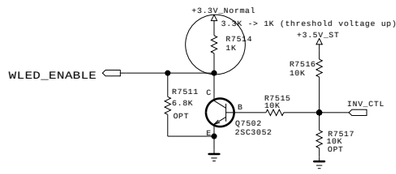
SECRET
LGElectronics

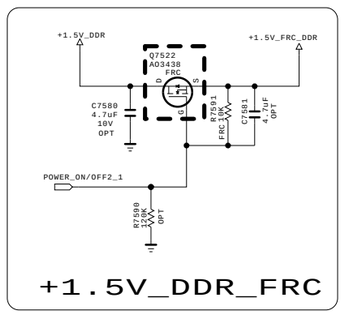
LG ELECTRONICS

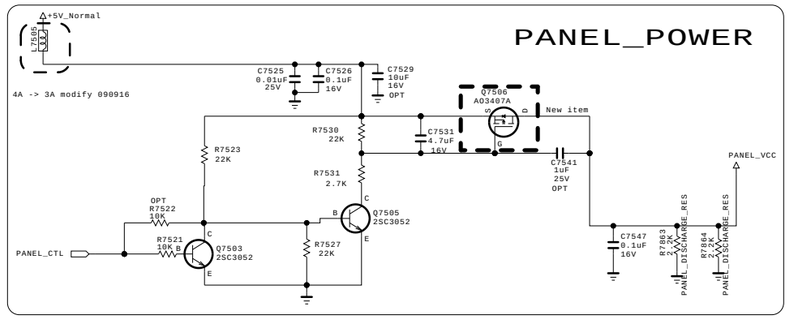
MODEL	GP2R	DATE	20101023
BLOCK	TN_IR/CONT	SHEET	31 /

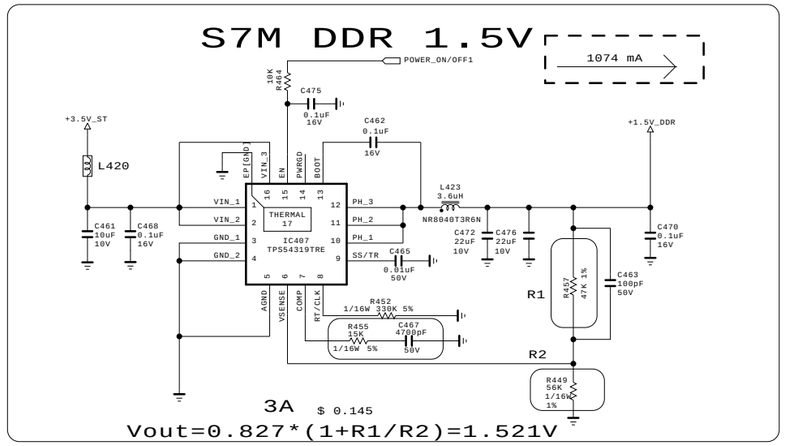
SMALL_TN_IR/CONTROL

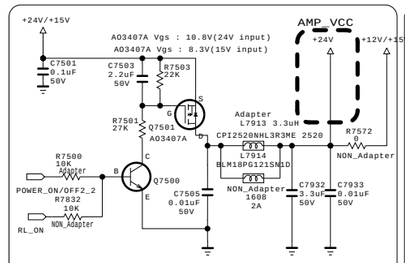


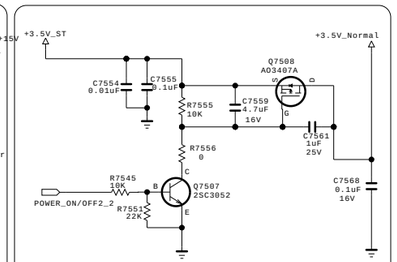


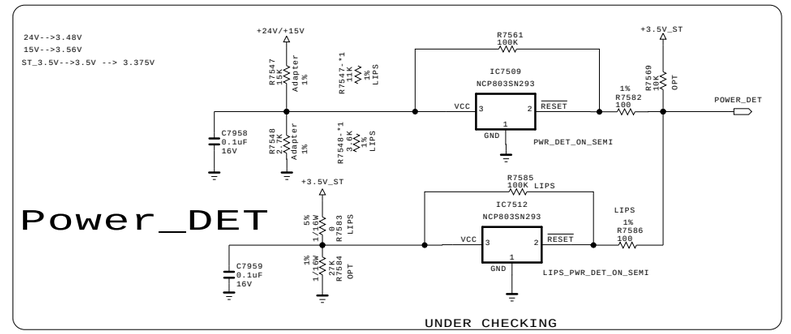


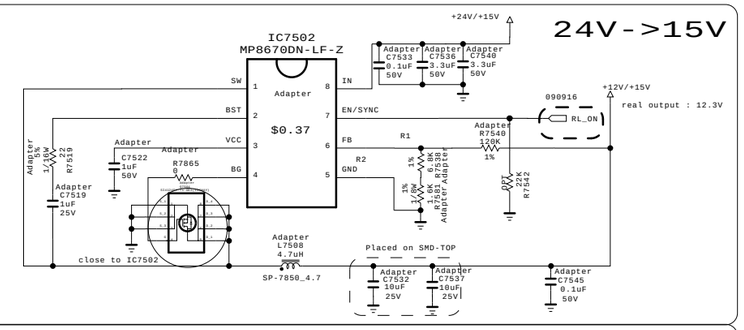


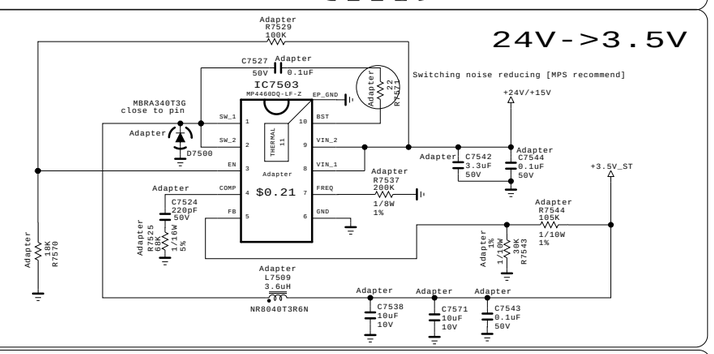


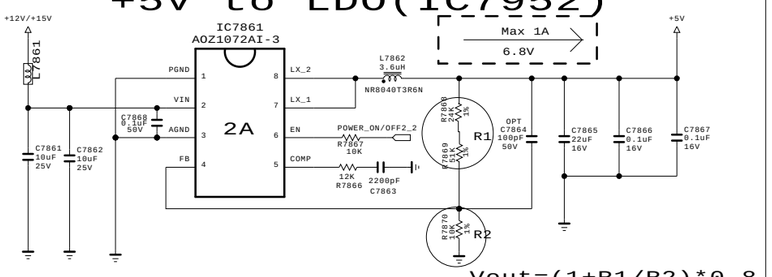


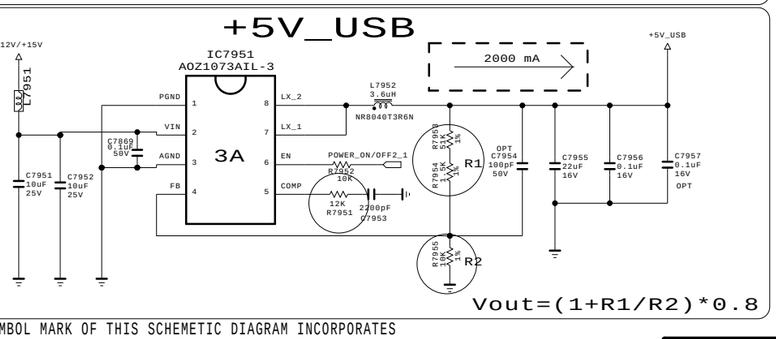


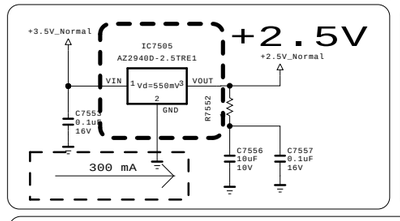


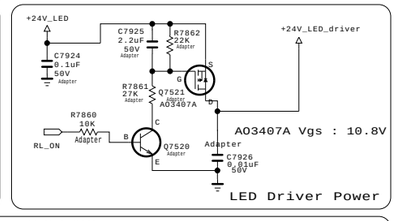


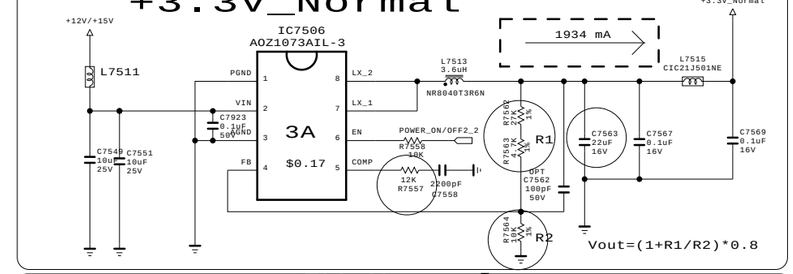


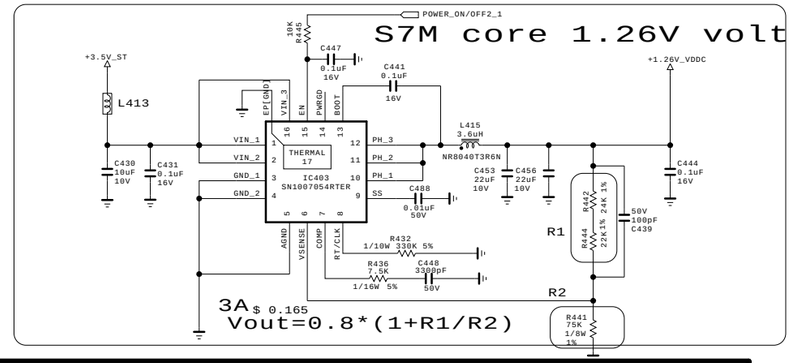


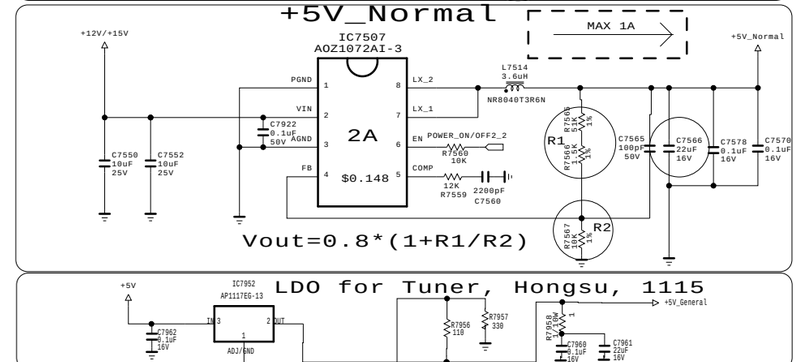












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SECRET
LGElectronics

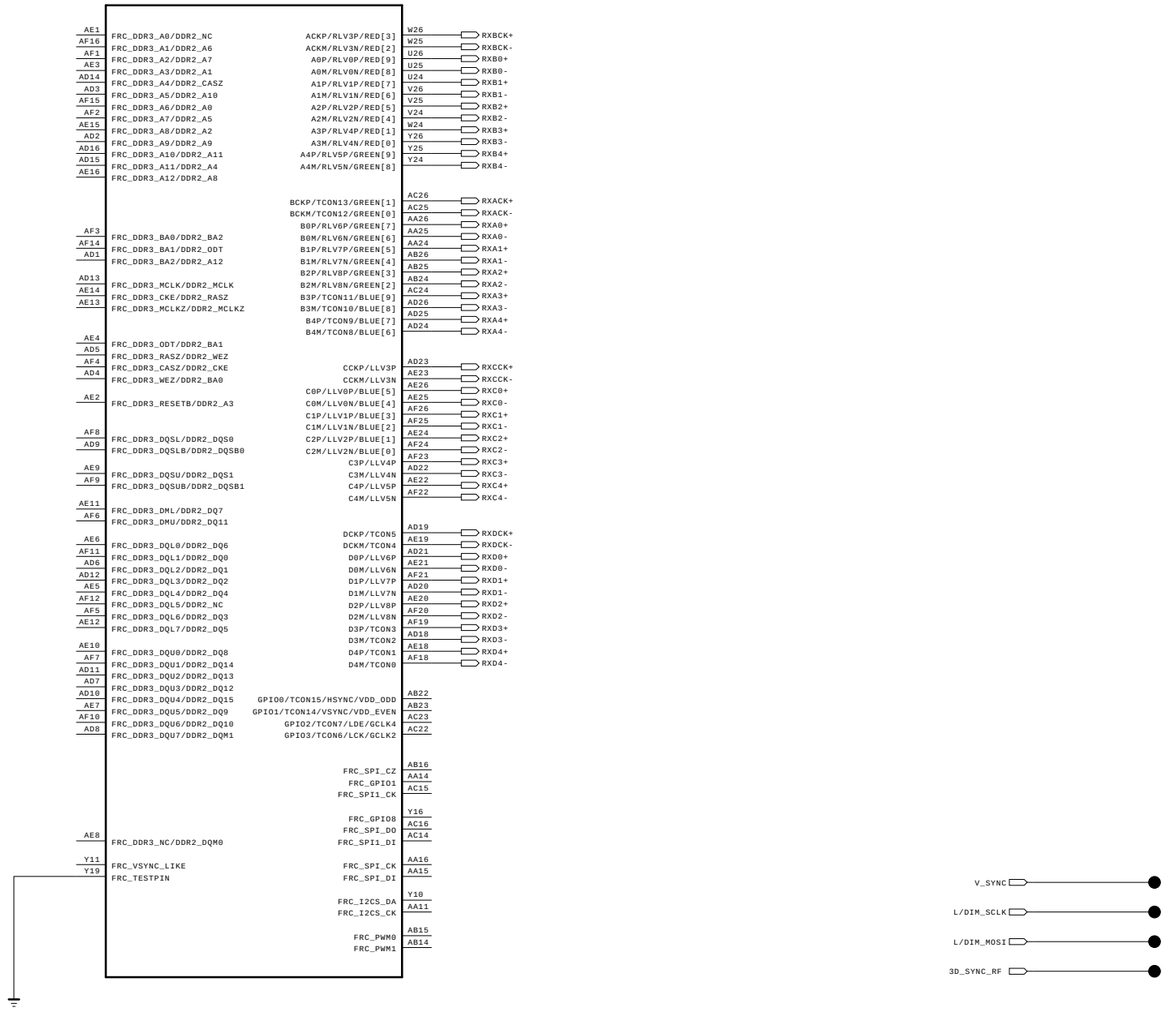
LG ELECTRONICS

MODEL	GP2R	DATE	20101115
BLOCK	POWER_TN	SHEET	32

SMALL_TN_POWER

S7M-PLUS

IC101
LGE107DC-RP [S7M+ DIVX/MS10]



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SECRET
LGElectronics

LG ELECTRONICS

MODEL		DATE	
BLOCK		SHEET	/


LG Electronics Inc.

LCD TV Repair Guide
`11 years New Basic Models

Contents

LCD TV EU Group

LCD TV Research Department

JAN. 28th, 2011

LCD TV Repair Guide

`11 years New Models

< Applicable Basic Model >

xxLK330, xxLK430, xxLK450, xxLK530, xxLK550
xxLV2300, xxLV2500, xxLV2540, xxLV3400
xxLV3500, xxLV3550, xxLV5500, xxLV4500
xxLW4500 (3D)

Product Roadmap

2011

Lamp

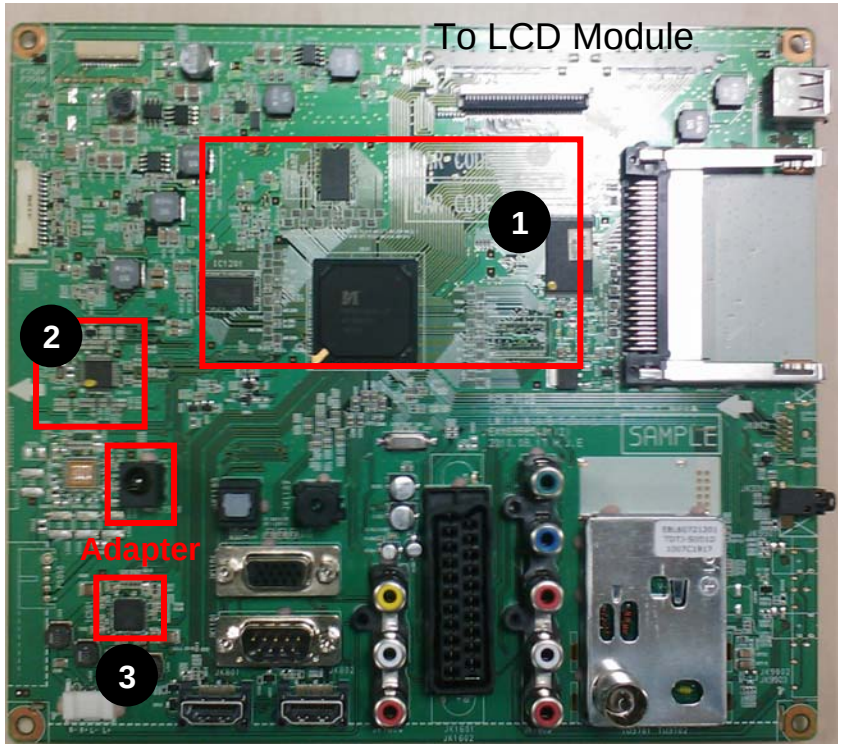
Tool	Pan EU	UK(U)	Nordic(N)	Italy(A)
LK33	22LK330-ZB	22LK330U-ZB	22LK330N-ZB	22LK330A-ZB
	26LK330-ZB	26LK330U-ZB	26LK330N-ZB	26LK330A-ZB
	32LK330-ZB	32LK330U-ZB	32LK330N-ZB	32LK330A-ZB
	22LK331-ZA			
	26LK331-ZA			
	32LK331-ZA			
LK43	32LK430-ZA	32LK430U-ZA	32LK430N-ZA	32LK430A-ZA
	37LK430-ZA	37LK430U-ZA	37LK430N-ZA	37LK430A-ZA
	42LK430-ZA	42LK430U-ZA	42LK430N-ZA	42LK430A-ZA
LK45	32LK450-ZB	32LK450U-ZB	32LK450N-ZB	32LK450A-ZB
	37LK450-ZB	37LK450U-ZB	37LK450N-ZB	37LK450A-ZB
	42LK450-ZB	42LK450U-ZB	42LK450N-ZB	42LK450A-ZB
	32LK451-ZA			
	37LK451-ZA			
	42LK451-ZA			
LK53	32LK530-ZC	32LK530U-ZC	32LK530N-ZC	32LK530A-ZC
	42LK530-ZC	42LK530U-ZC	42LK530N-ZC	42LK530A-ZC
	47LK530-ZC	47LK530U-ZC	47LK530N-ZC	47LK530A-ZC
LK55	32LK550-ZA	32LK550U-ZA	32LK550N-ZA	32LK550A-ZA
	42LK550-ZA	42LK550U-ZA	42LK550N-ZA	42LK550A-ZA
	32LK551-ZB			
	42LK551-ZB			

Edge LED

Tool	Pan EU	UK(U)	Nordic(N)	Italy(A)
LV23	19LV2300-ZA			
	22LV2300-ZA			
LV25	19LV2500-ZA	19LV250U-ZA	19LV250N-ZA	19LV250A-ZA
	22LV2500-ZA	22LV250U-ZA	22LV250N-ZA	22LV250A-ZA
	26LV2500-ZA	26LV250U-ZA	26LV250N-ZA	26LV250A-ZA
	32LV2500-ZA	32LV250U-ZA	32LV250N-ZA	32LV250A-ZA
	26LV2540-ZE	26LV254U-ZE	26LV254N-ZE	26LV254A-ZE
	32LV2540-ZE	32LV254U-ZE	32LV254N-ZE	32LV254A-ZE
LV34	32LV3400-ZA	32LV340U-ZA	32LV340N-ZA	32LV340A-ZA
	42LV3400-ZA	42LV340U-ZA	42LV340N-ZA	42LV340A-ZA
	32LW3450-ZB	32LW345U-ZB	32LW345N-ZB	32LW345A-ZB
	42LW3450-ZB	42LW345U-ZB	42LW345N-ZB	42LW345A-ZB
LV35	32LV3500-ZA	32LV350U-ZA	32LV350N-ZA	32LV350A-ZA
	37LV3500-ZA	37LV350U-ZA	37LV350N-ZA	37LV350A-ZA
	42LV3500-ZA	42LV350U-ZA	42LV350N-ZA	42LV350A-ZA
	47LV3500-ZA	47LV350U-ZA	47LV350N-ZA	47LV350A-ZA
	32LV3550-ZB	32LV355U-ZB	32LV355N-ZB	32LV355A-ZB
	37LV3550-ZB	37LV355U-ZB	37LV355N-ZB	37LV355A-ZB
	42LV3550-ZB	42LV355U-ZB	42LV355N-ZB	42LV355A-ZB
	47LV3550-ZB	47LV355U-ZB	47LV355N-ZB	47LV355A-ZB
	32LV3551-ZD			
	37LV3551-ZD			
	42LV3551-ZD			
	47LV3551-ZD			
LV45	32LV4500-ZC	32LV450U-ZC	32LV450N-ZC	32LV450A-ZC
	37LV4500-ZC	37LV450U-ZC	37LV450N-ZC	37LV450A-ZC
	42LV4500-ZC	42LV450U-ZC	42LV450N-ZC	42LV450A-ZC
	47LV4500-ZC	47LV450U-ZC	47LV450N-ZC	47LV450A-ZC
LW45 3D	32LW4500-ZB	32LW450U-ZB	32LW450N-ZB	32LW450A-ZB
	37LW4500-ZB	37LW450U-ZB	37LW450N-ZB	37LW450A-ZB
	42LW4500-ZB	42LW450U-ZB	42LW450N-ZB	42LW450A-ZB
	47LW4500-ZB	47LW450U-ZB	47LW450N-ZB	47LW450A-ZB
	55LW4500-ZB	55LW450U-ZB	55LW450N-ZB	55LW450A-ZB
LV55	22LV5500-ZC	22LV550U-ZC	22LV550N-ZC	22LV550A-ZC
	26LV5500-ZC	26LV550U-ZC	26LV550N-ZC	26LV550A-ZC

Main PCB

19/22/26LV2500 (50HZ)

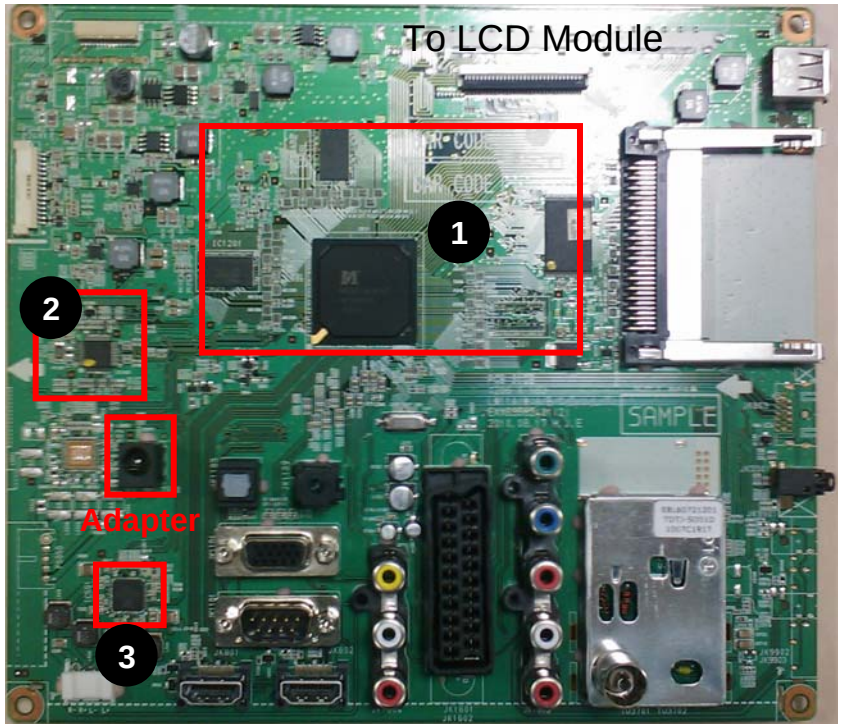


1. Main processor, DDR Memory Flash Memory
2. Micom for Key/IR sensing
3. Audio AMP (5W+5W)

*** 19/22/26LV2500_S7 Reused ('11)**
Main IC : LGE101_Mstar
Tuner Type : TDTJ-S001D (DVB-T/C)
Display Type (Resolution) : LED TV (1366 x 768)
Interface : HDMI 2EA , Component 1EA, AV 1EA, USB 1EA
Difference : Without FRC, HDMI Position , Resolution , Interface, Wafer Position (Sub)

Main PCB

22/26LK330 (50HZ)

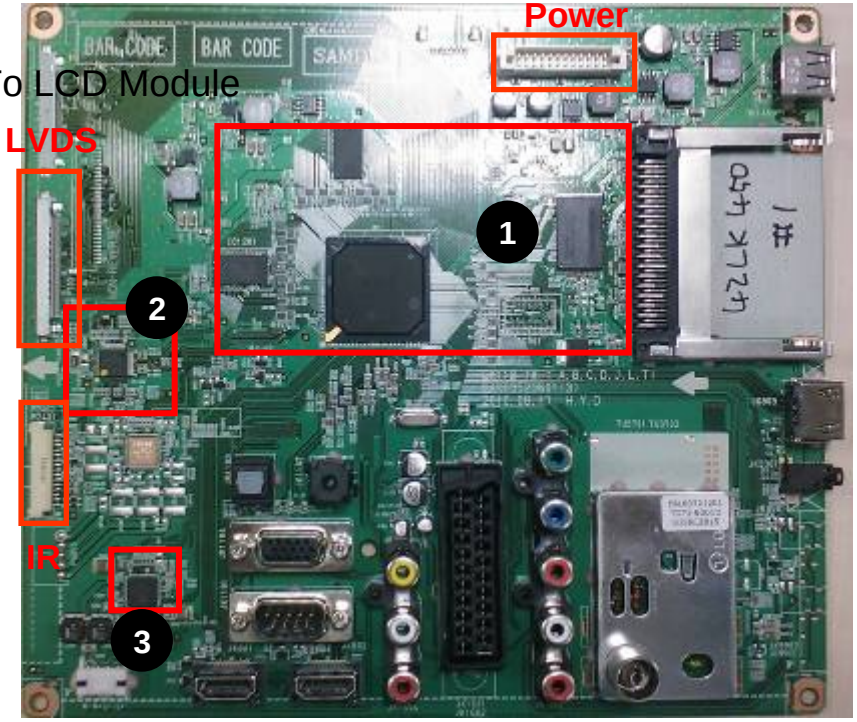


1. Main processor, DDR Memory Flash Memory
2. Micom for Key/IR sensing
3. Audio AMP (5W+5W)

* 22/26LK330_S7 Reused ('11)
Main IC : LGE101_Mstar
Tuner Type : TDTJ-S001D (DVB-T/C)
Display Type (Resolution) : LCD TV (1366 x 768)
Interface : HDMI 2EA , Component 1EA, AV 1EA, USB 1EA
Difference : Without FRC, HDMI Position , Resolution , Interface, Wafer Position (Sub)

Main PCB

32/37/42/47LK450 (50HZ)

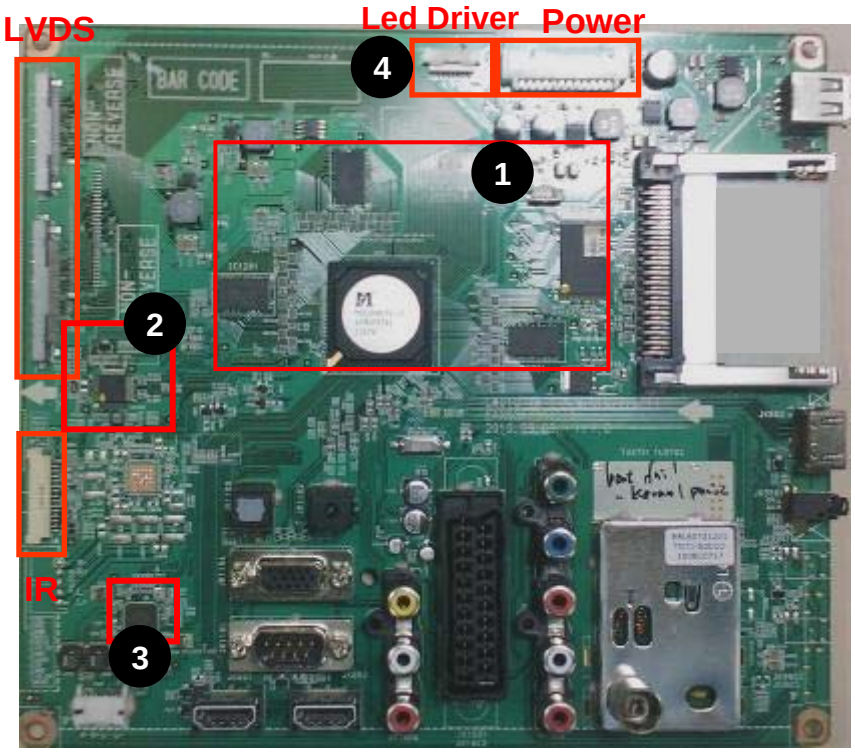


1. Main processor, DDR Memory Flash Memory
2. Micom for Key/IR sensing
3. Audio AMP (10W+10W)

*** 32/37/42/47LK450_S7 Reused ('11)**
Main IC : LGE101_Mstar
Tuner Type : TDTJ-S001D (DVB-T/C)
Display Type (Resolution) : LCD TV (1920 x 1080)
Interface : HDMI 3EA , Component 1EA, AV 1EA, USB 1EA
Difference : Without FRC, HDMI Position , Resolution , Interface, Wafer Position (Sub)

Main PCB

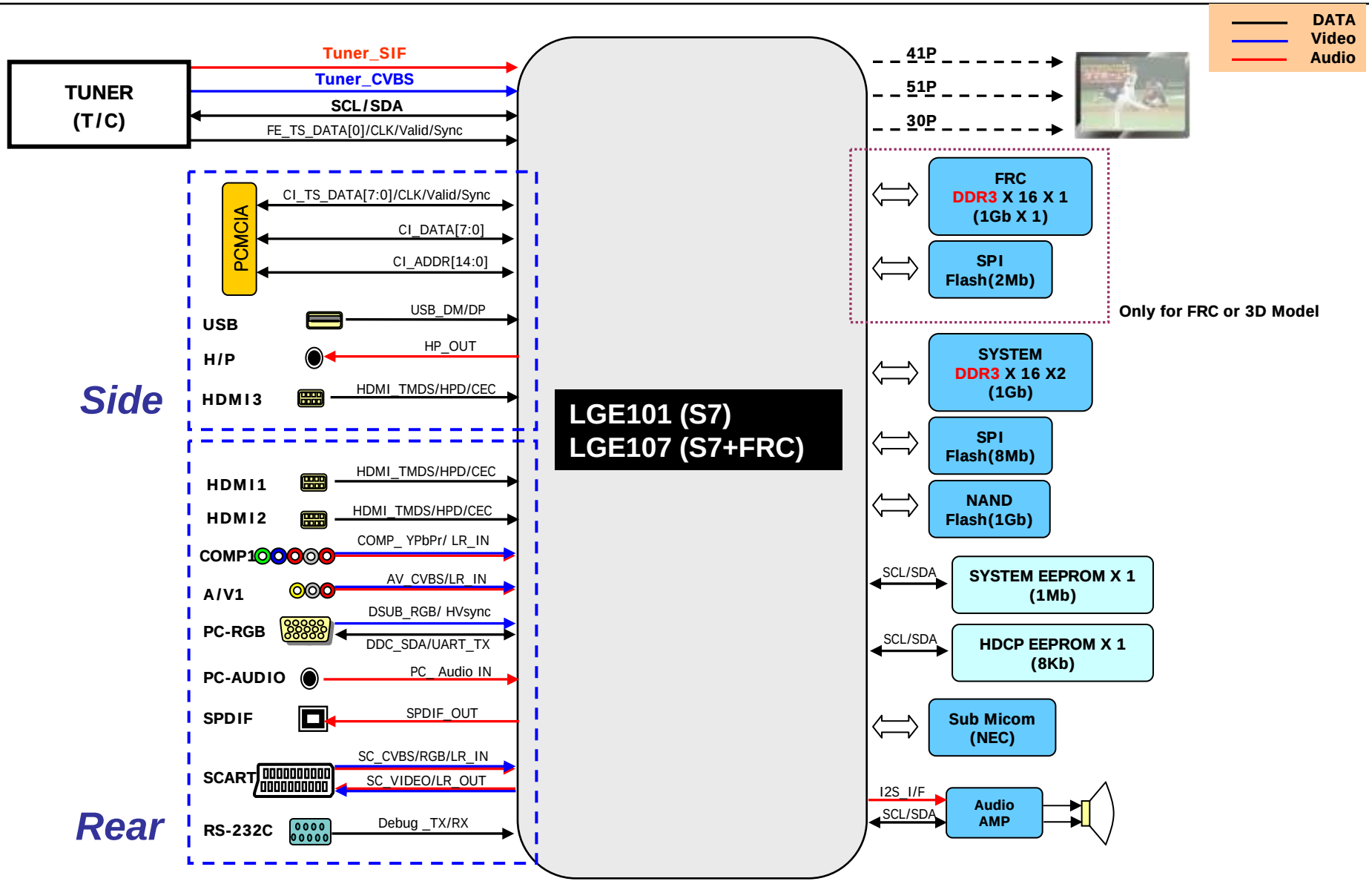
32/37/42/47/55LW4500 (100HZ)



1. Main processor, DDR Memory
   Flash Memory
2. Micom for Key/IR sensing
3. Audio AMP (10W+10W)
4. LED Driver connection (with local dimming)

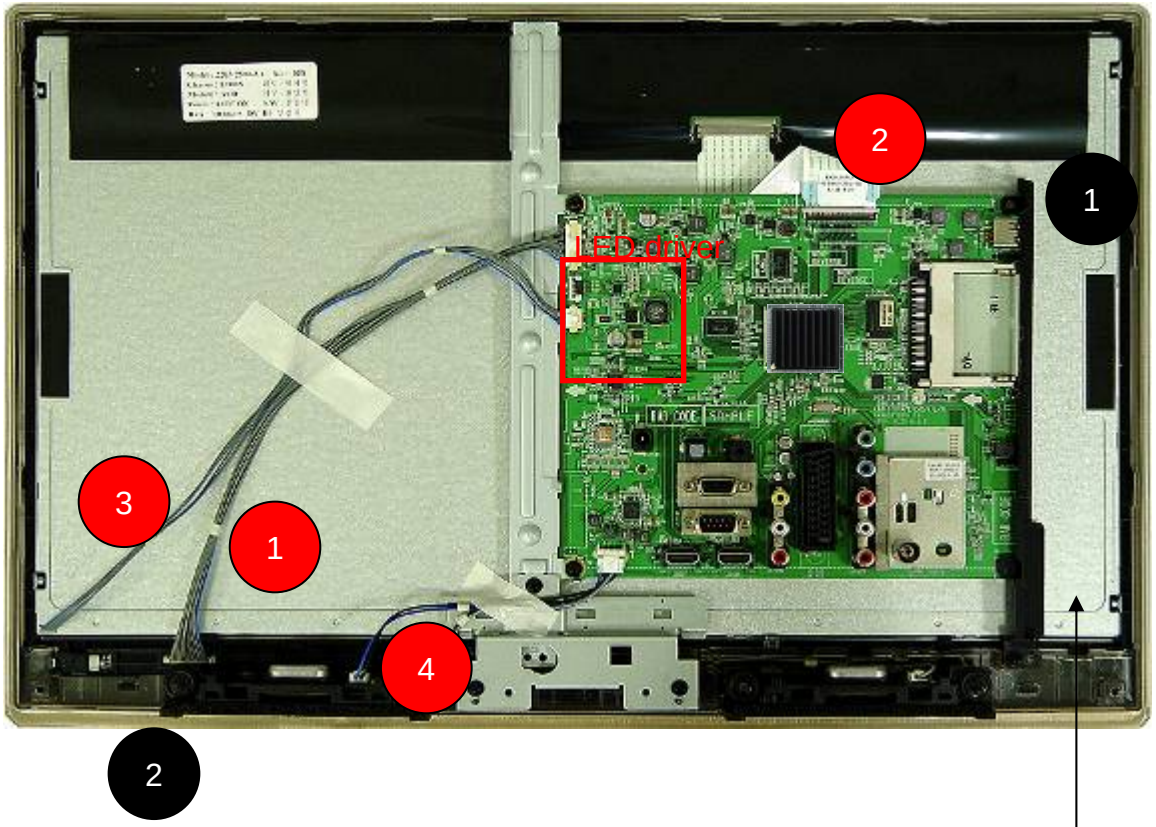
*** 37LW4500_S7 Reused ('11)**
Main IC : LGE107_Mstar
Tuner Type : TDTJ-S001D (DVB-T/C)
Display Type (Resolution) : 3D, LED TV (1920 x 1080)
Interface : HDMI 3EA , Component 1EA, AV 1EA, USB 1EA
Difference : HDMI Position , Interface, Wafer Position (LVDS, Power, Sub)

Block diagram



Interconnection - 1

19/22LV2500



[PCBs]

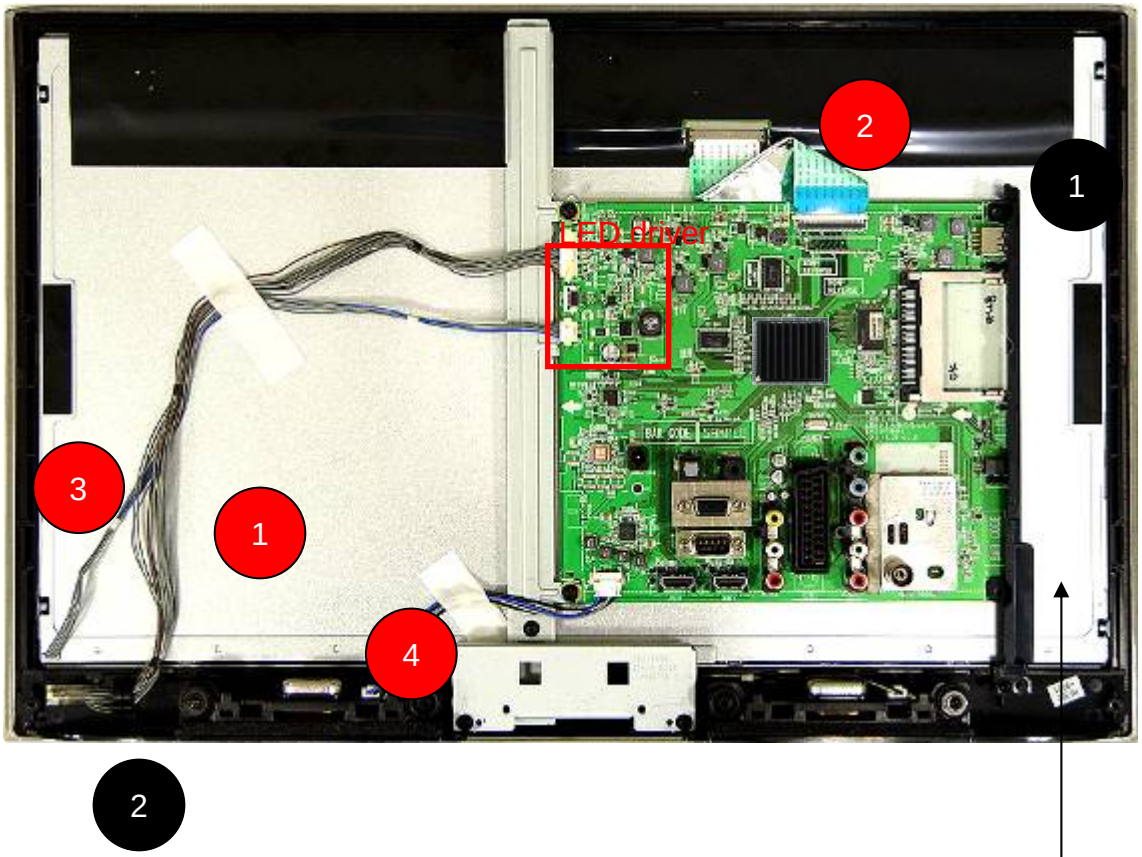
1. Main PCB
2. Soft Touch Key/IR PCB

[Cables]

1. Soft Touch key/IR cable
2. Main / Module LVDS cable
3. LED driver / Module cable
4. SPK cable

Interconnection - 2

19/22LV5500



[PCBs]

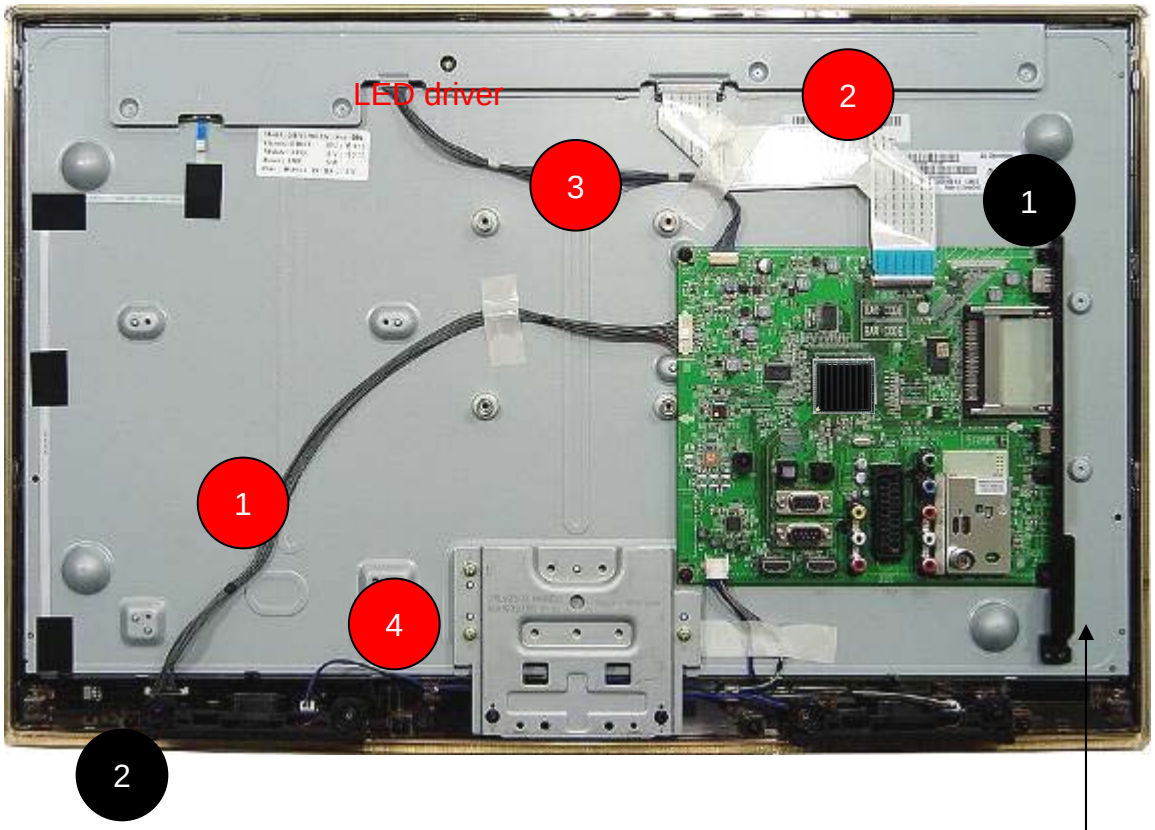
1. Main PCB
2. Soft Touch Key/IR PCB

[Cables]

1. Soft Touch key/IR cable
2. Main / Module LVDS cable
3. LED driver / Module cable
4. SPK cable

Interconnection - 3

26LV2500



[PCBs]

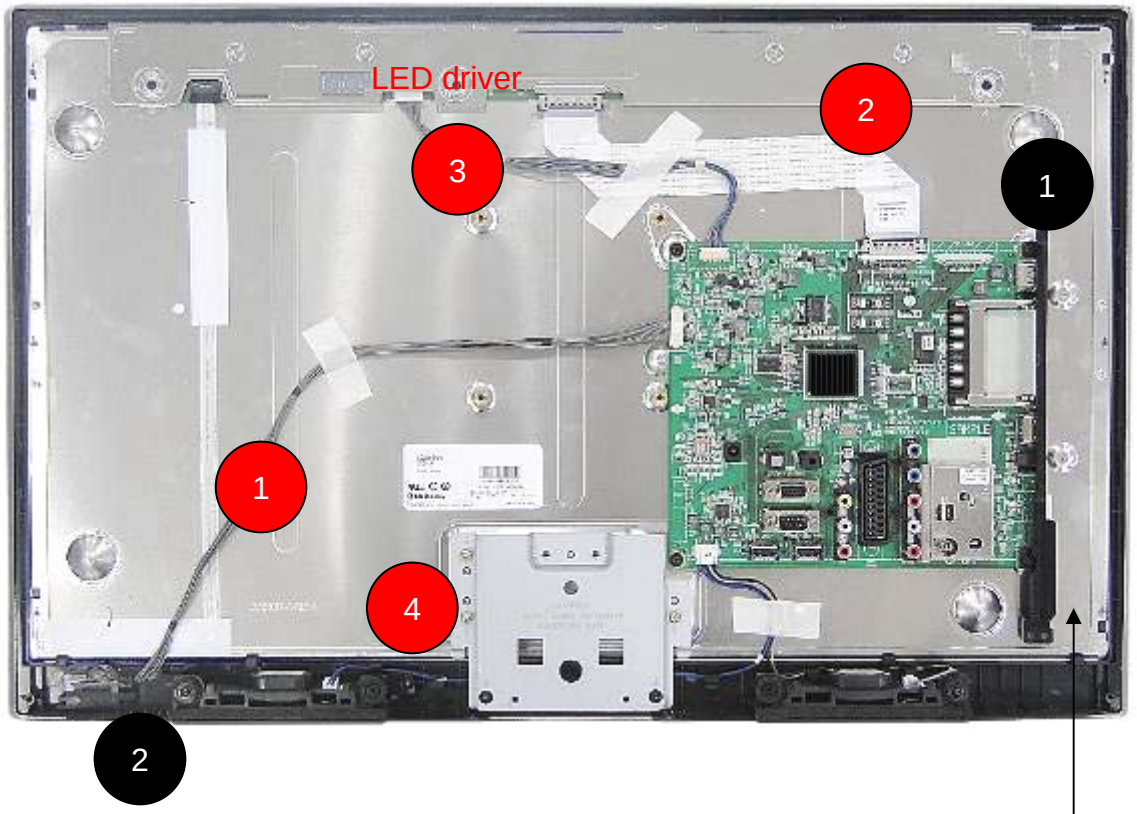
1. **Main PCB**
2. **Soft Touch Key/IR PCB**

[Cables]

1. **Soft Touch key/IR cable**
2. **Main / Module LVDS cable**
3. **LED driver / Module cable**
4. **SPK cable**

Interconnection - 4

26LV5500



[PCBs]

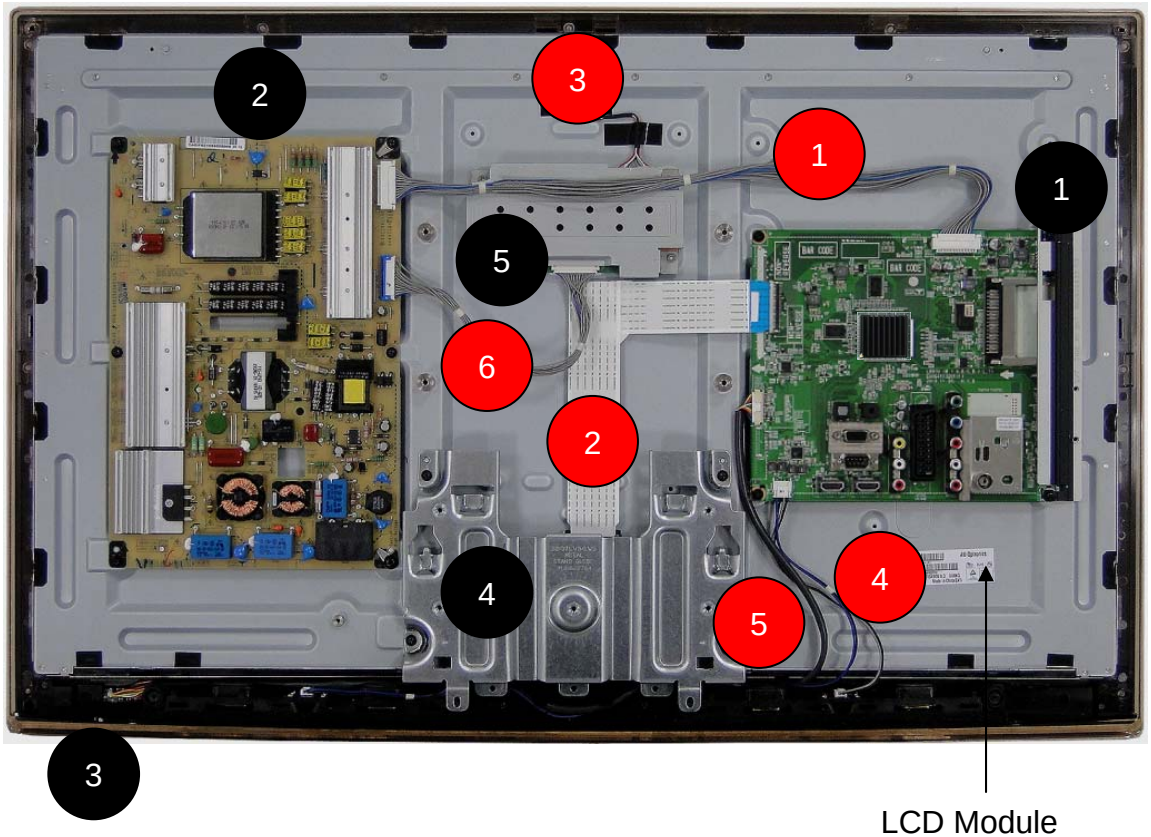
1. Main PCB
2. Soft Touch Key/IR PCB

[Cables]

1. Soft Touch key/IR cable
2. Main / Module LVDS cable
3. LED driver / Module cable
4. SPK cable

Interconnection - 5

32LV2500



[PCBs]

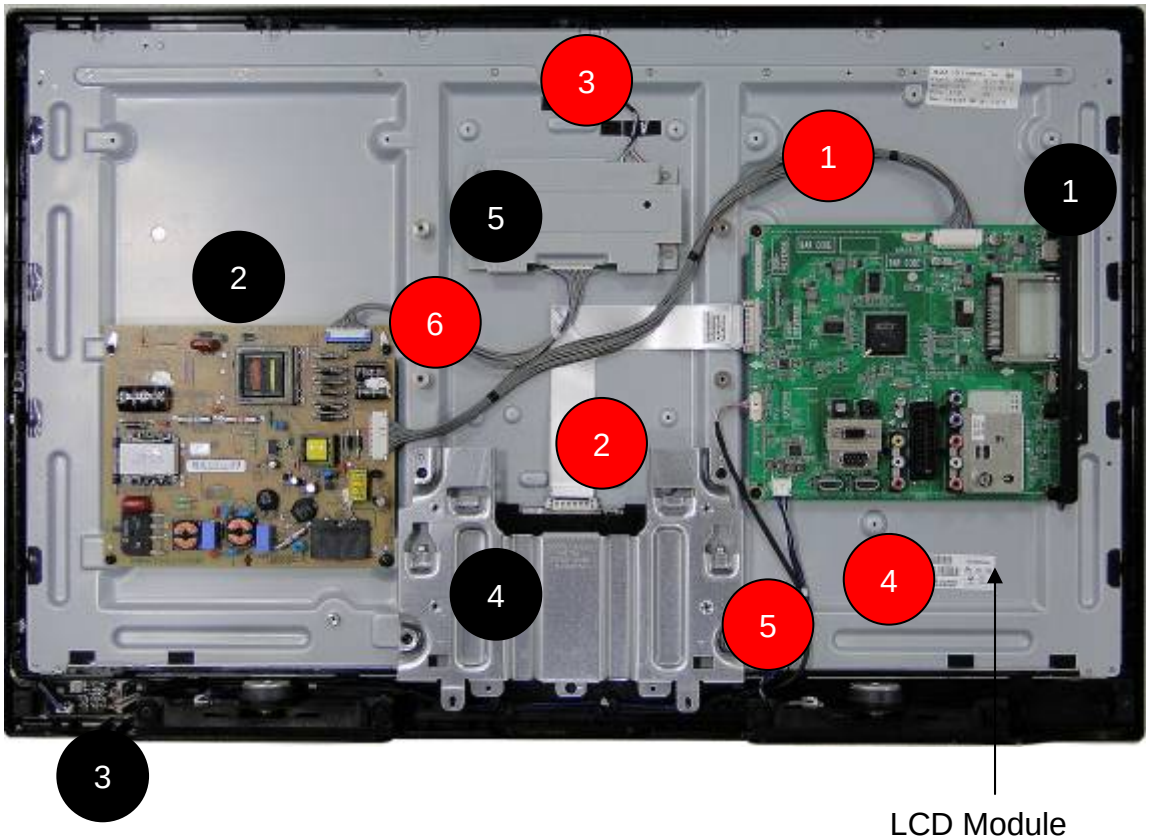
1. Main PCB
2. PSU
3. Soft Touch Key/IR PCB
4. Timing controller
5. LED Driver

[Cables]

1. Main / PSU cable
2. Main / Module LVDS cable
3. LED driver / Module cable
4. SPK cable
5. Soft Touch key/IR cable
6. LED driver / PSU cable 14P

Interconnection - 6

37LV3400



[PCBs]

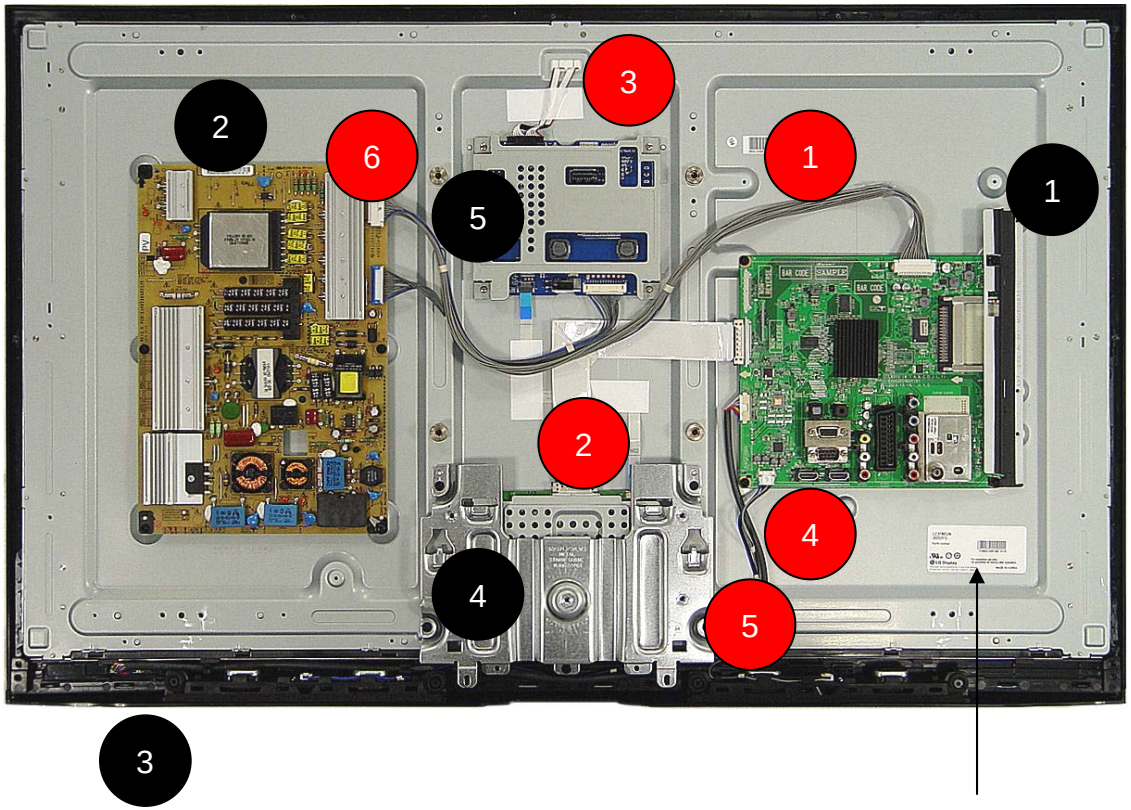
1. Main PCB
2. PSU
3. Soft Touch Key/IR PCB
4. Timing controller
5. LED Driver

[Cables]

1. Main / PSU cable
2. Main / Module LVDS cable
3. LED driver / Module cable
4. SPK cable
5. Soft Touch key/IR cable
6. LED driver / PSU cable 14P

Interconnection - 7

37LV3550



[PCBs]

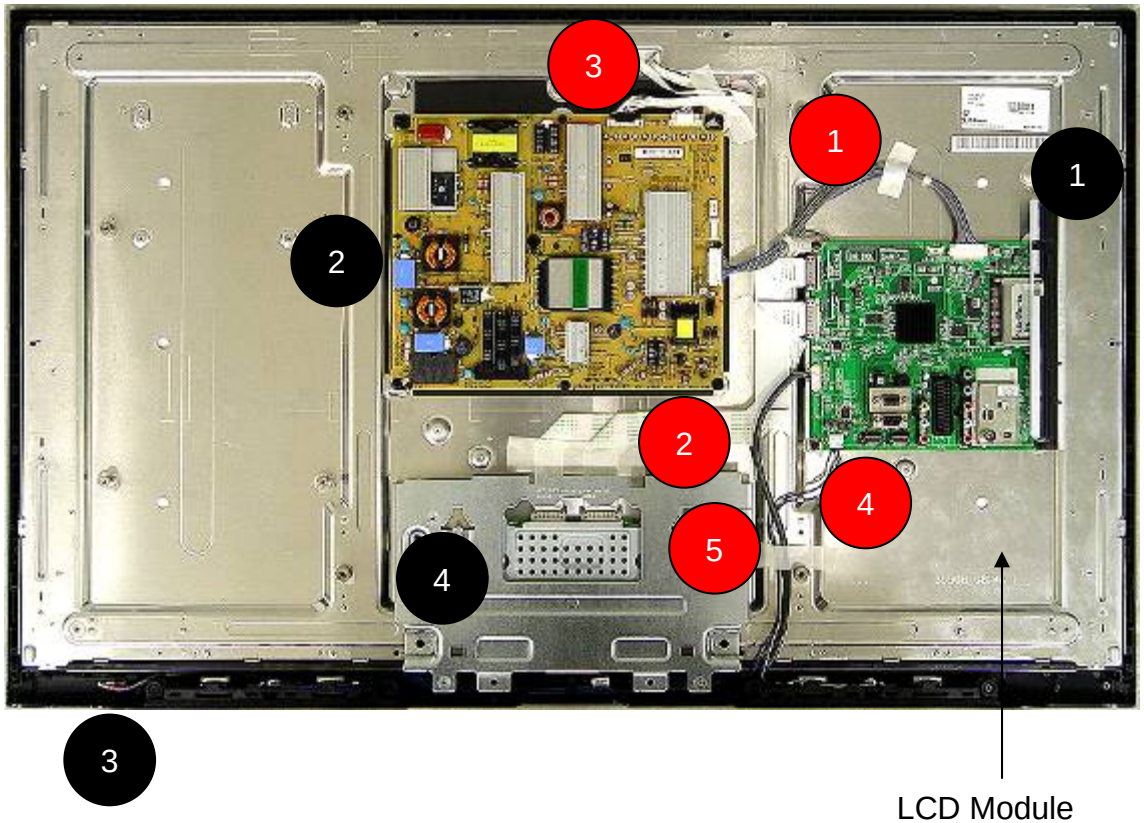
1. Main PCB
2. PSU
3. Soft Touch Key/IR PCB
4. Timing controller
5. LED Driver

[Cables]

1. Main / PSU cable
2. Main / Module LVDS cable
3. LED driver / Module cable
4. SPK cable
5. Soft Touch key/IR cable
6. LED driver / PSU cable 14P

Interconnection - 8

42LV4500



[PCBs]

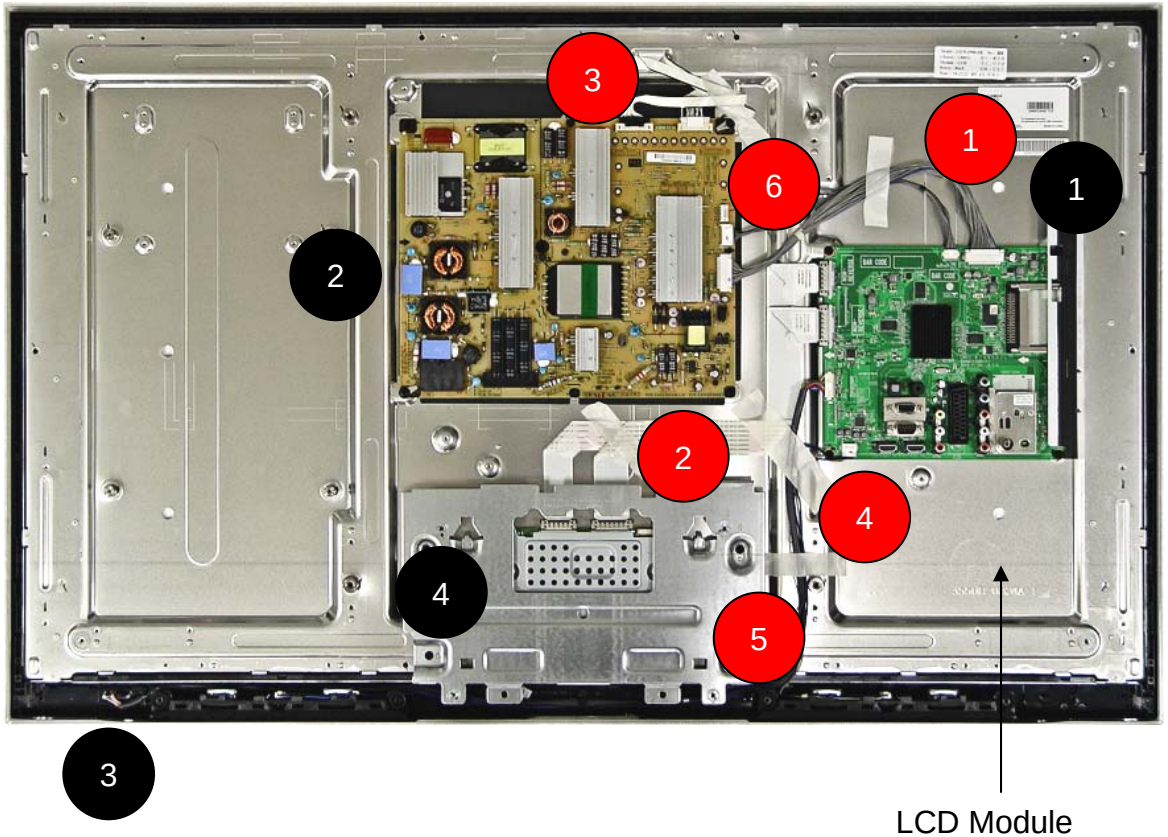
1. Main PCB
2. PSU + LED driver
3. Soft Touch Key/IR PCB
4. Timing controller

[Cables]

1. Main / PSU cable
2. Main / Module LVDS cable
3. LED driver / Module cable
4. SPK cable
5. Soft Touch key/IR cable

Interconnection - 9

42LW4500



[PCBs]

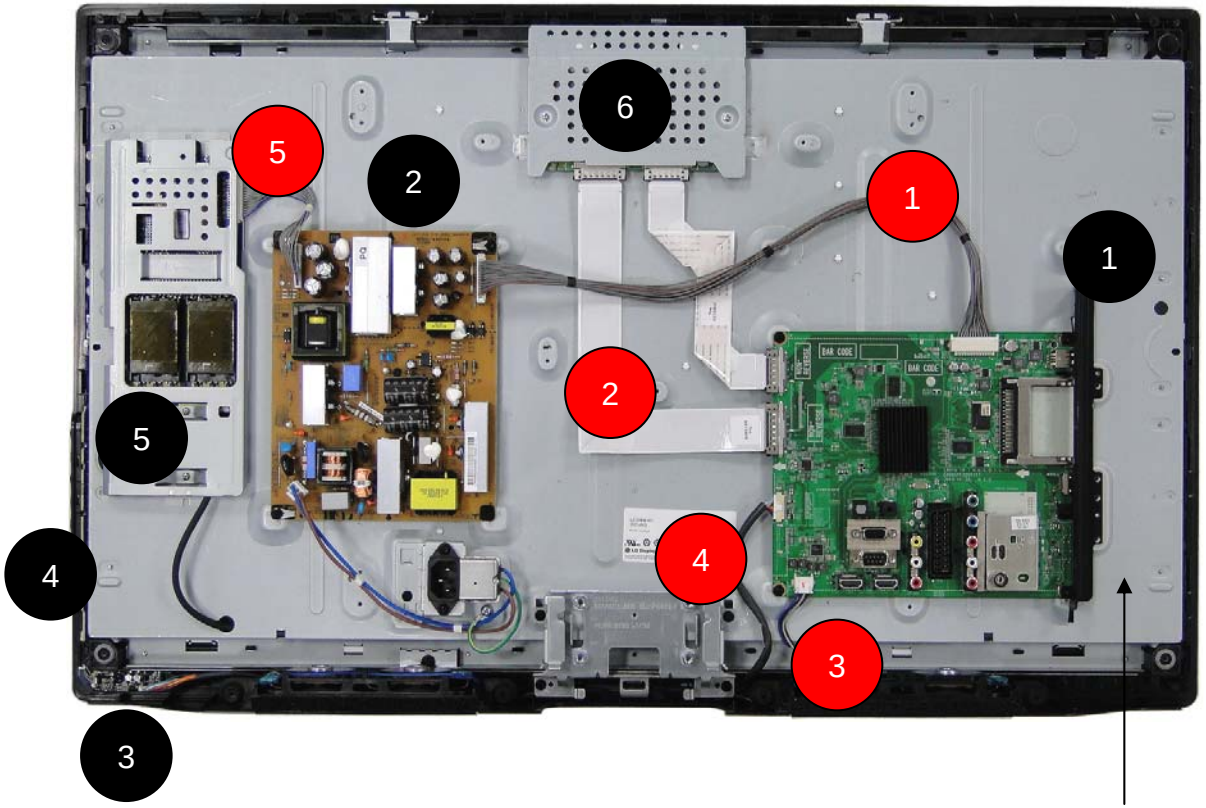
1. Main PCB
2. PSU + LED driver
3. Soft Touch Key/IR PCB
4. Timing controller

[Cables]

1. Main / PSU cable
2. Main / Module LVDS cable
3. LED driver / Module cable
4. SPK cable
5. Soft Touch key/IR cable
6. Local dimming signal cable (Main / LED driver 8pin)

Interconnection - 10

32LK530



[PCBs]

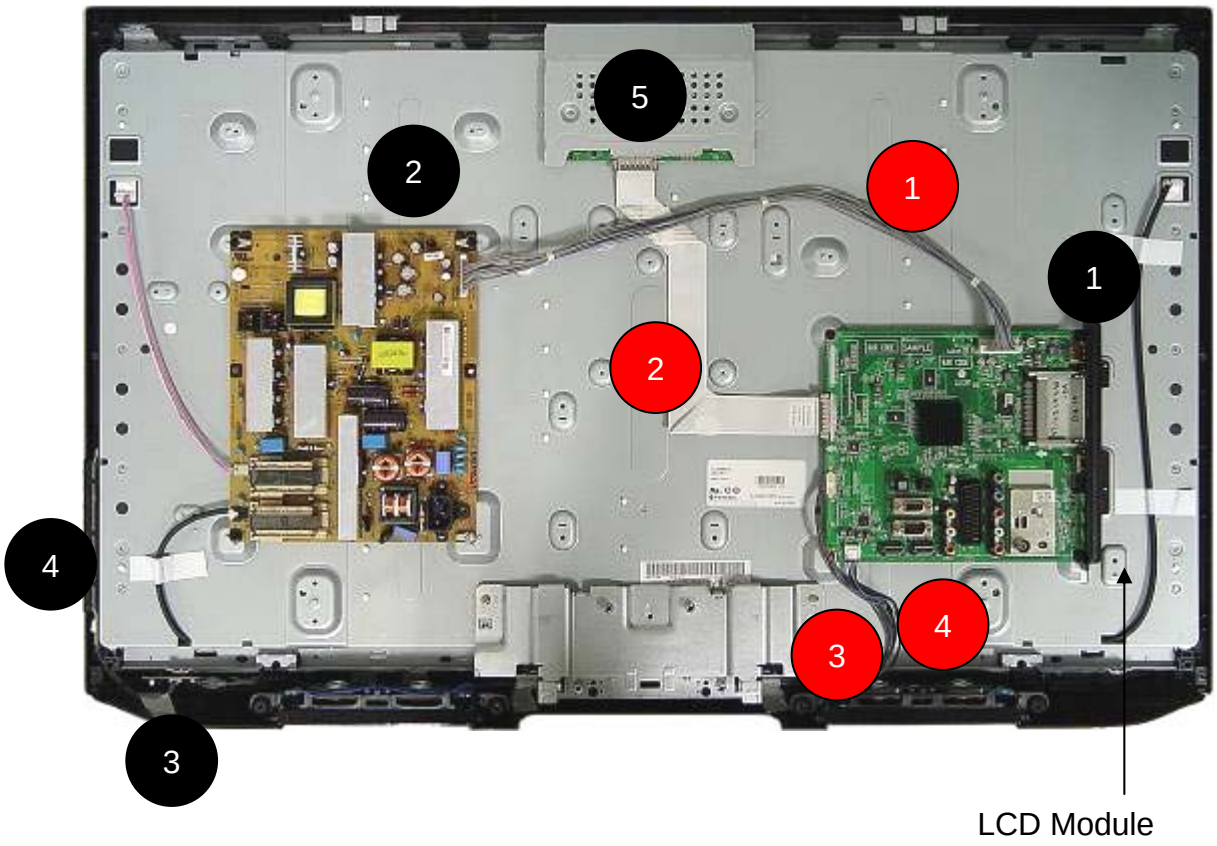
1. Main PCB
2. PSU (without inverter)
3. IR & Indicator PCB
4. Local Key PCB
5. Inverter
6. Timing controller

[Cables]

1. Main / PSU cable
2. Main / Module LVDS cable
3. SPK cable
4. IR/Local key cable
5. Inverter/PSU cable (14pin)

Interconnection - 11

37LK430



[PCBs]

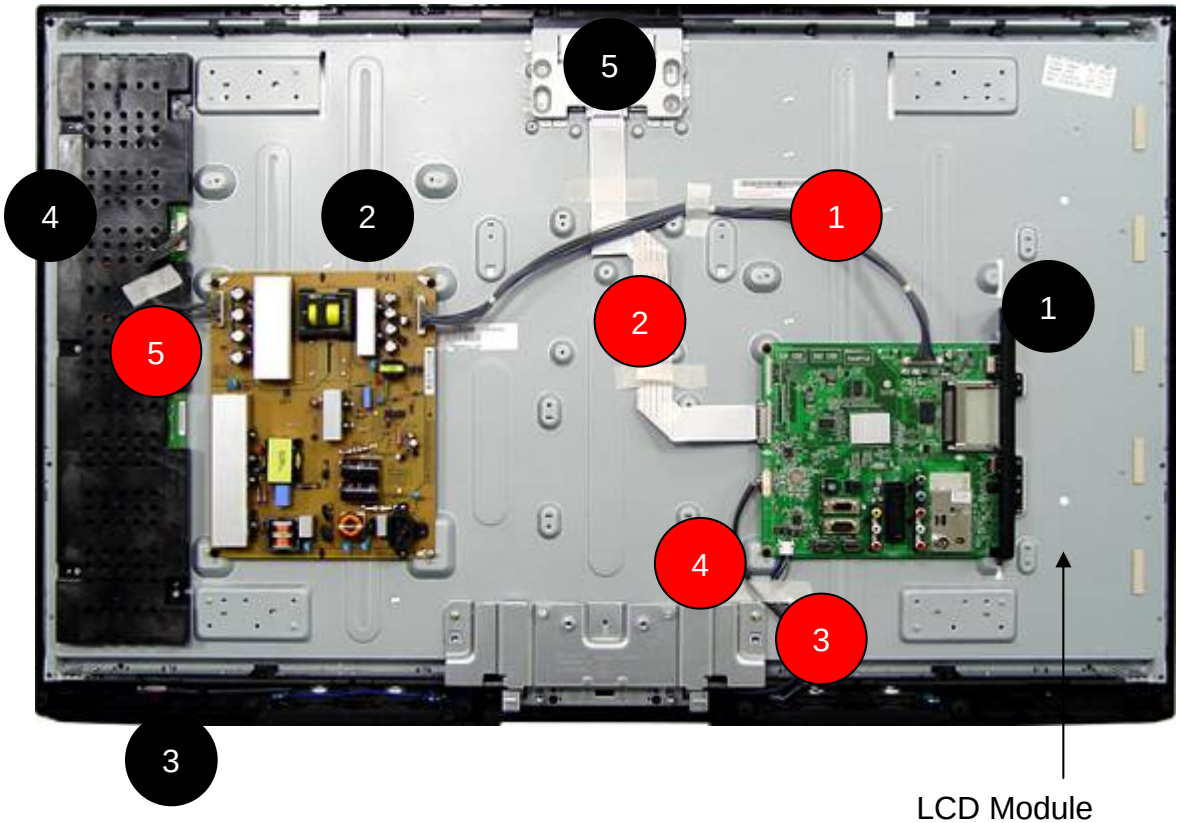
1. Main PCB
2. PSU (without inverter)
3. IR & Indicator PCB
4. Local Key PCB
5. Timing controller

[Cables]

1. Main / PSU cable
2. Main / Module LVDS cable
3. SPK cable
4. IR/Local key cable

Interconnection - 12

42LK450



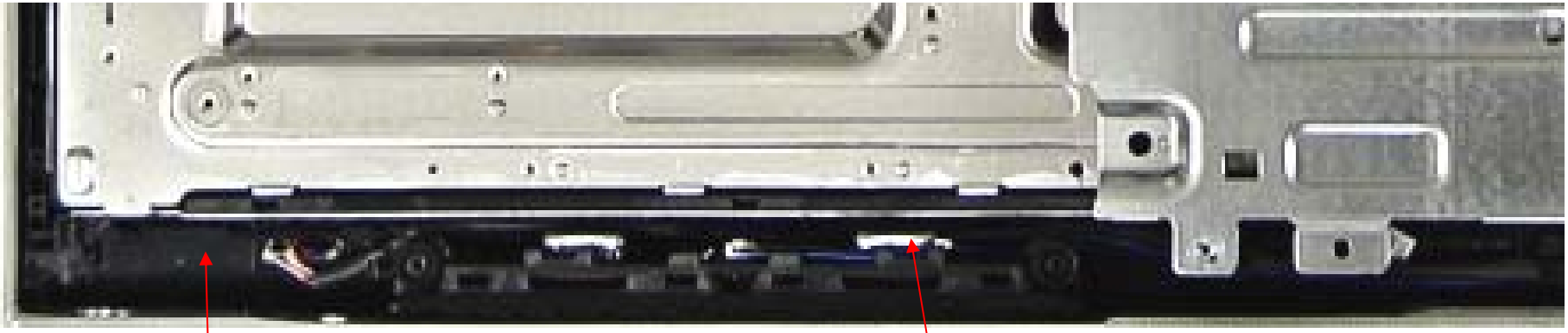
[PCBs]

1. **Main PCB**
2. **PSU (without inverter)**
3. **Soft Touch Key/IR PCB**
4. **Inverter**
5. **Timing controller**

[Cables]

1. **Main / PSU cable**
2. **Main / Module LVDS cable**
3. **SPK cable**
4. **Soft Touch key/IR cable**
5. **Inverter/PSU cable (14pin)**

Interconnection – sub PCB(LV**/LK450 Series)



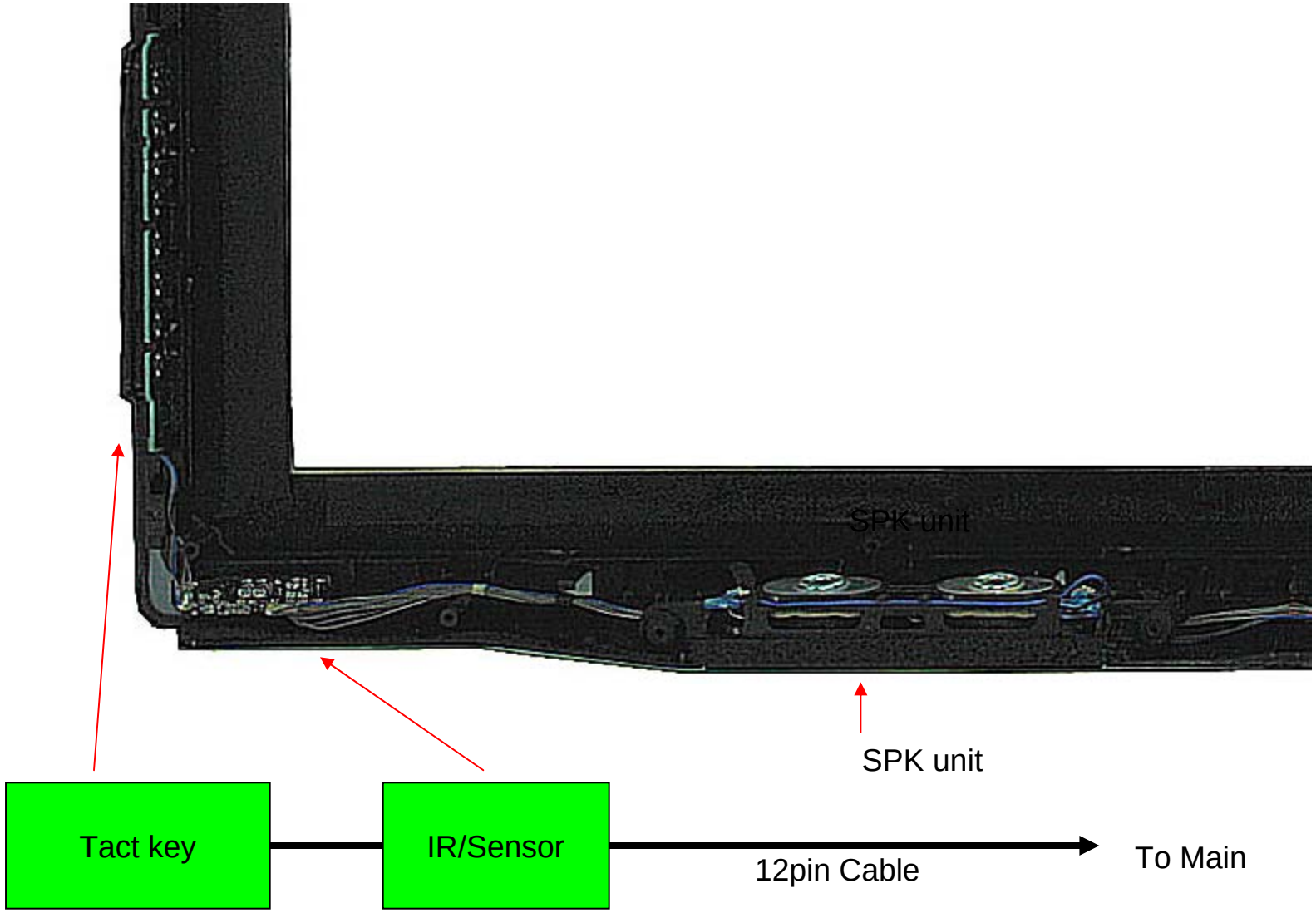
SPK unit

Soft Touch Key/IR PCB

15pin Cable

To Main

Interconnection – sub PCB(LK430/LK530 Series)

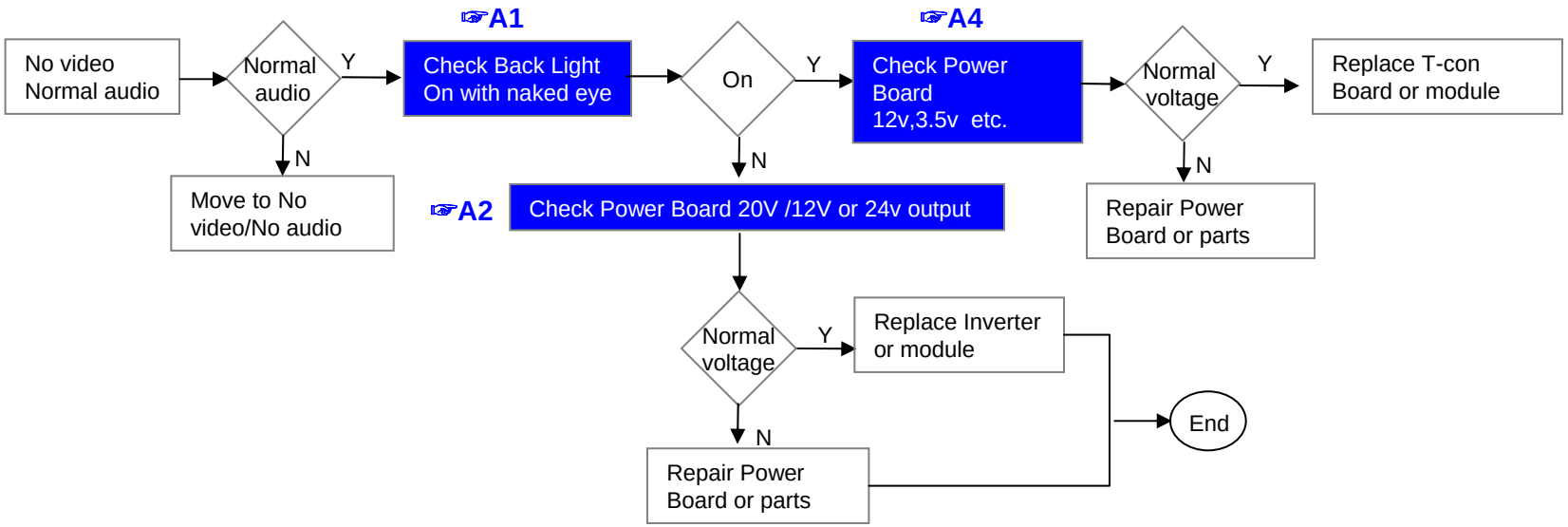


Contents of LCD TV Standard Repair Process

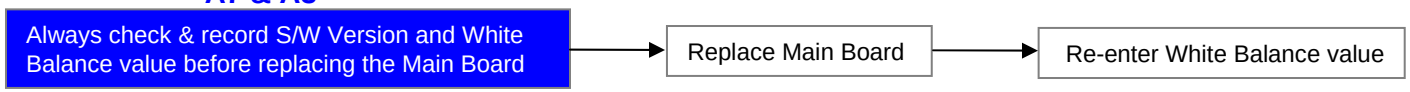
First of all, Check whether there is SVC Bulletin in GCSC System for these model.

LCD TV	Error symptom	A. Video error	Established date	2010. 2 .19	
		No video/ Normal audio	Revised date		1/13

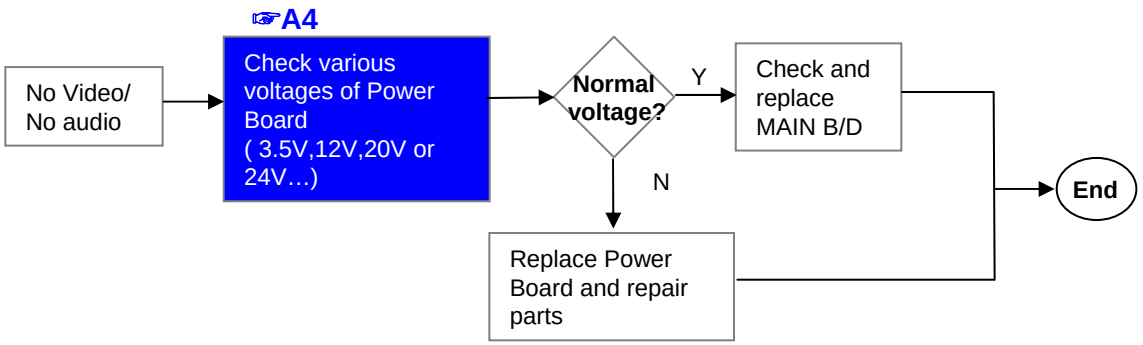
First of all, Check whether all of cables between board is inserted properly or not.
(Main B/D↔ Power B/D, LVDS Cable,Speaker Cable,IR B/D Cable,,,)



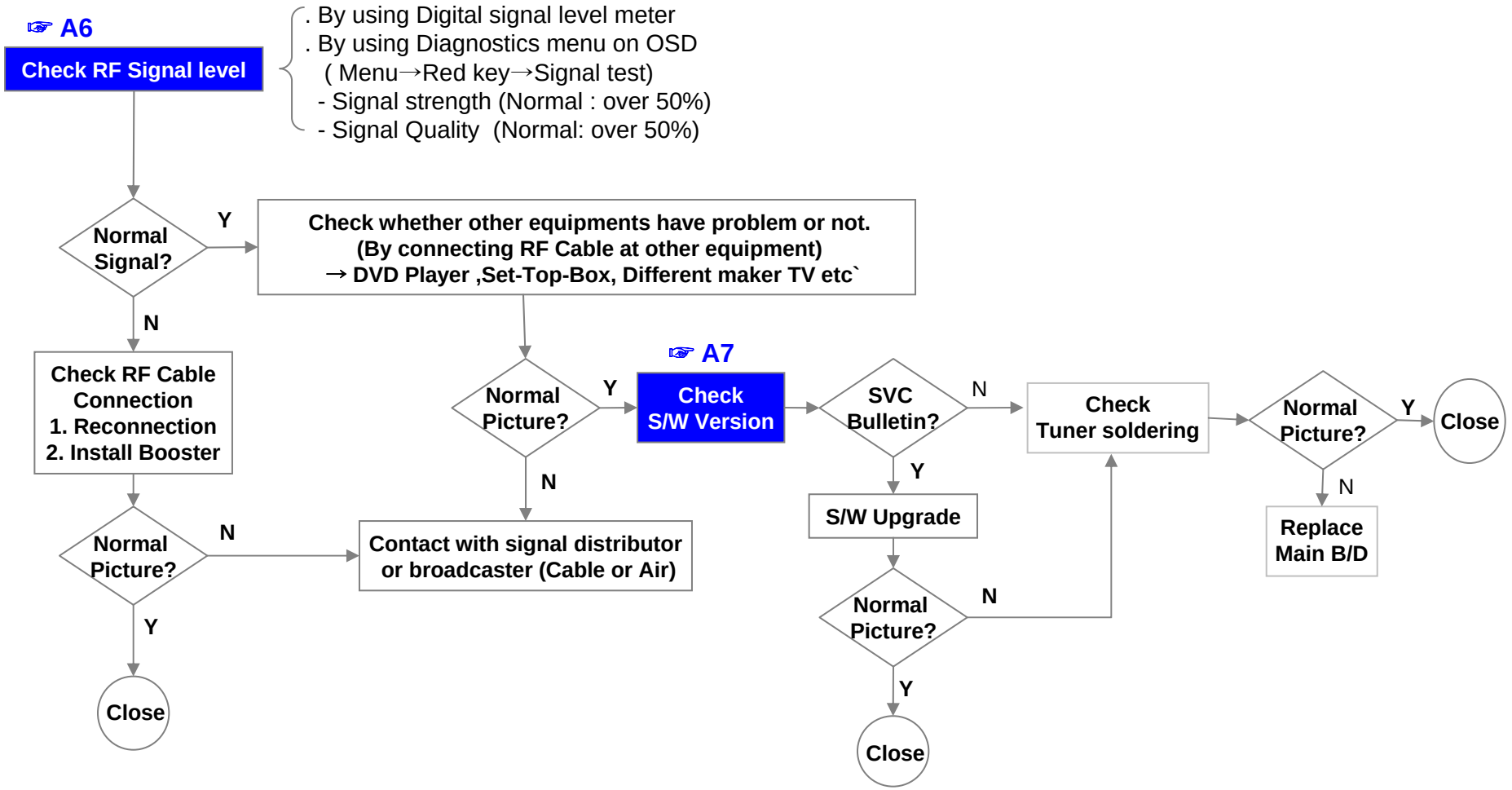
※Precaution ☞A7 & A3



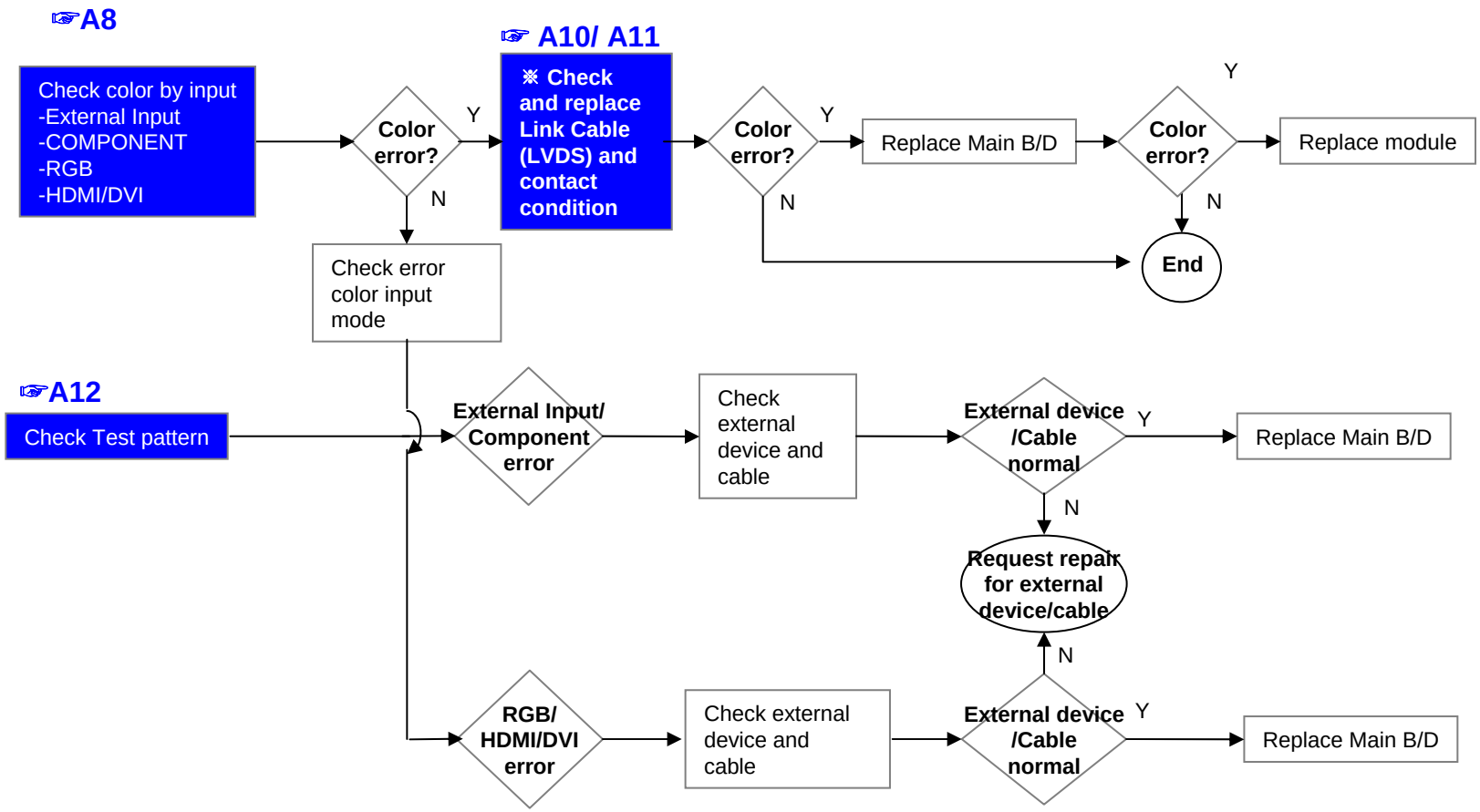
LCD TV	Error symptom	A. Video error	Established date	2010. 2 .19	
		No video/ No audio	Revised date		2/13



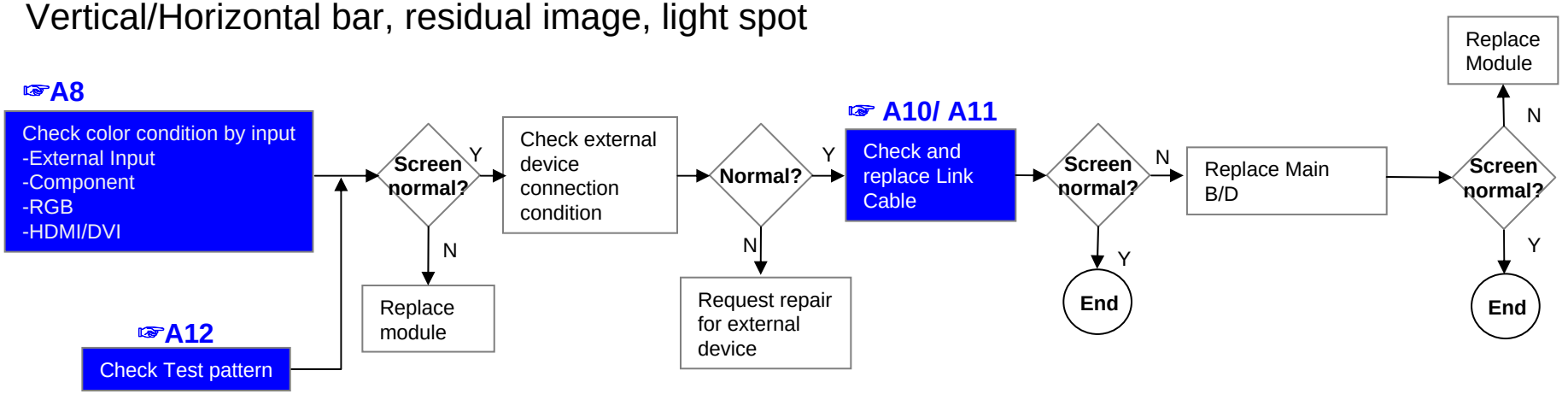
LCD TV	Error symptom	A. Picture Problem	Established date	2010. 2 .19	
		Picture broken/ Freezing	Revised date		3/13



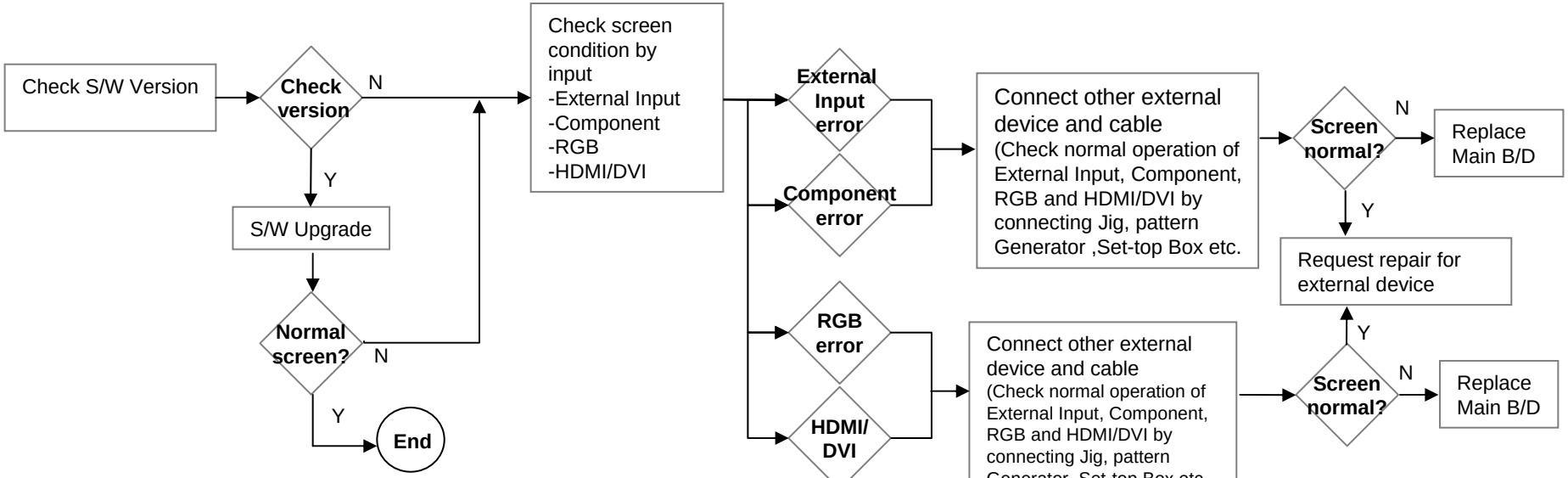
LCD TV	Error symptom	A. Video error	Established date	2010. 2 .19	
		Color error	Revised date		4/13



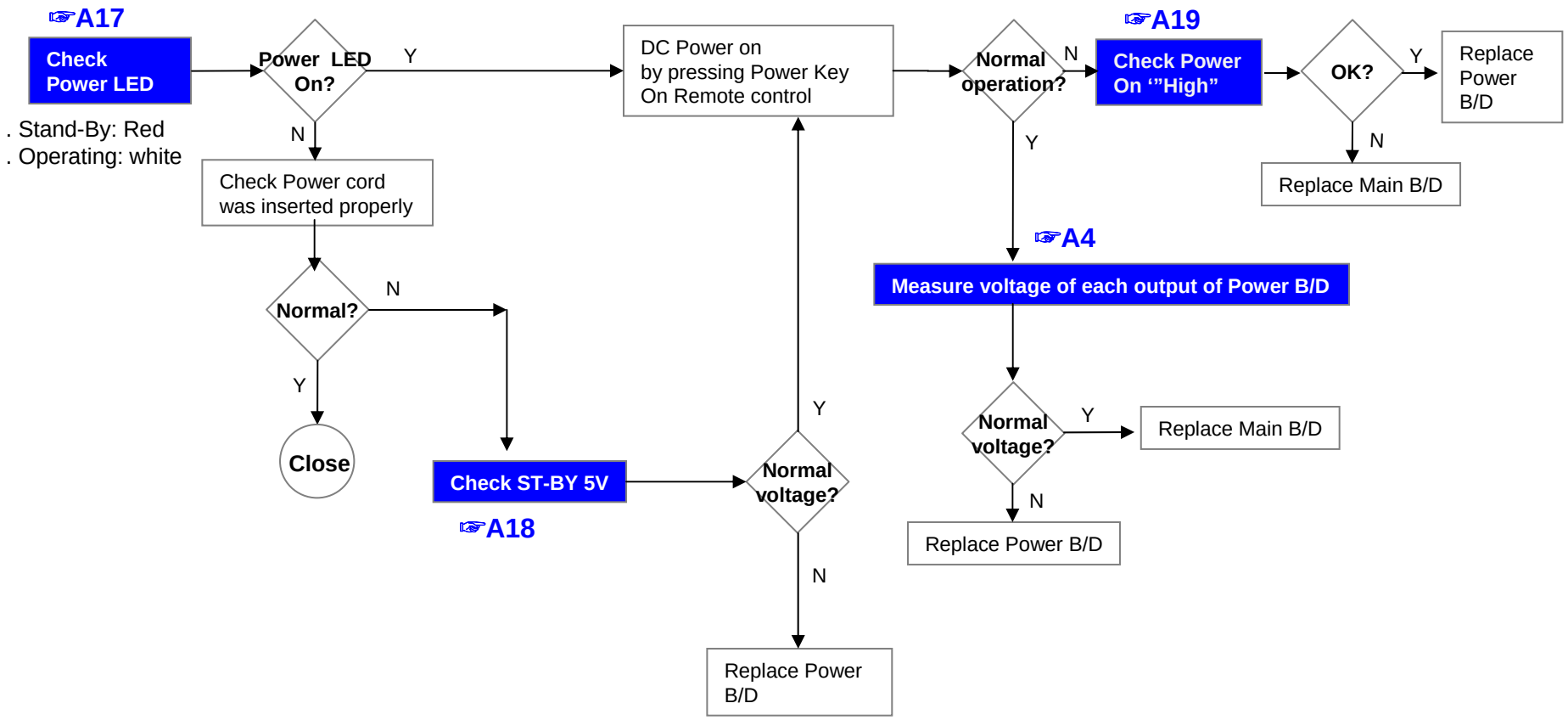
Vertical/Horizontal bar, residual image, light spot



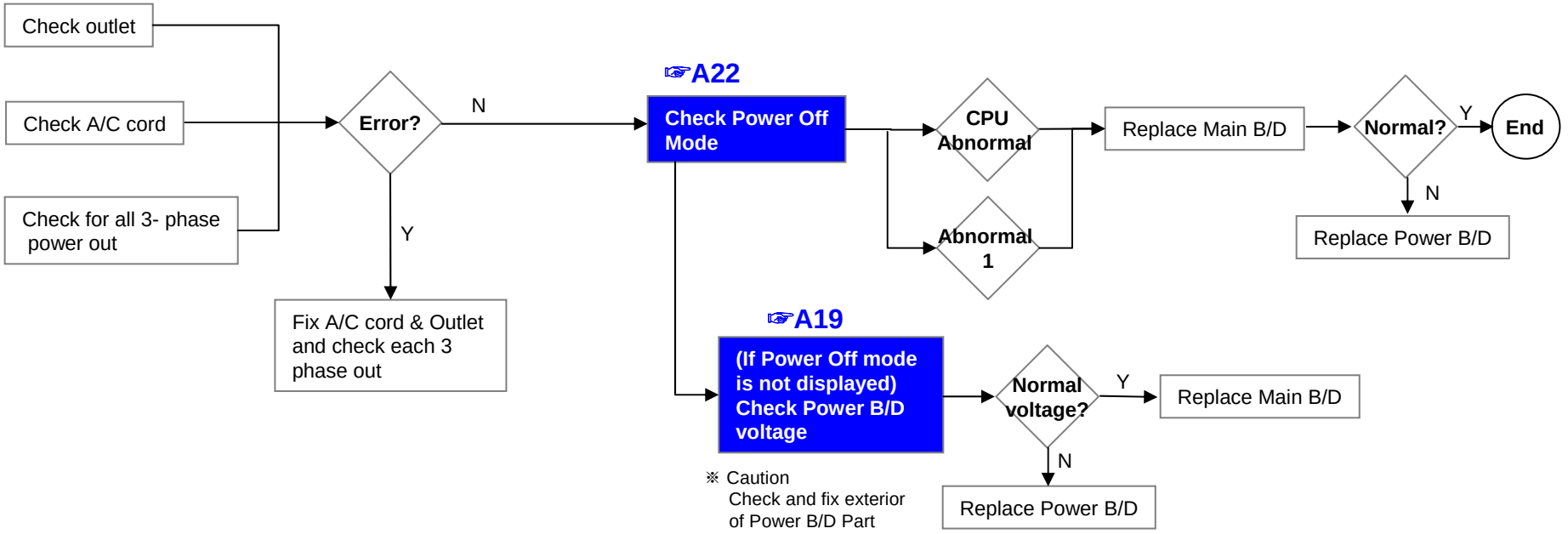
External device screen error-Color error



LCD TV	Error symptom	B. Power error	Established date	2010. 2 .19	
		No power	Revised date		6/13



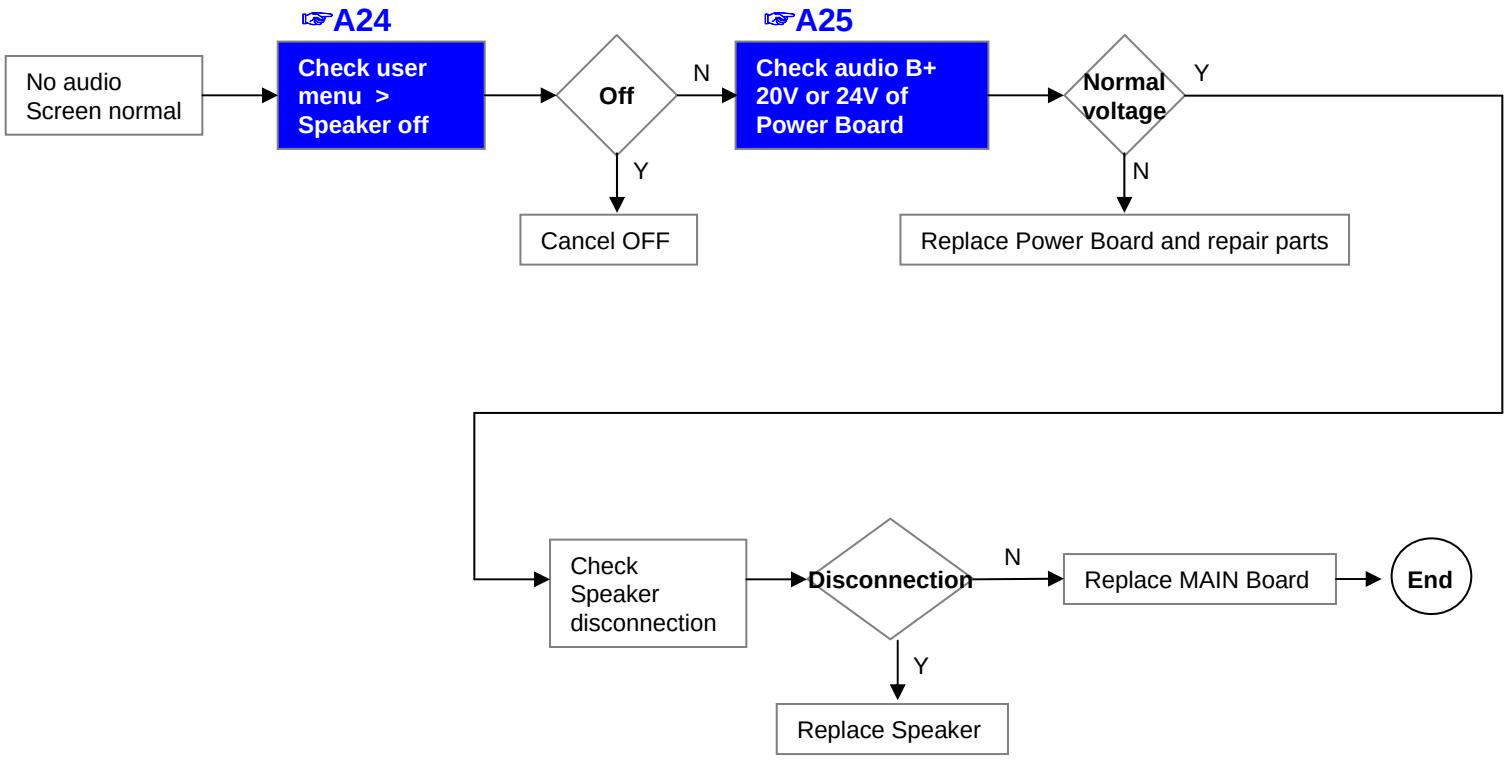
LCD TV	Error symptom	B. Power error	Established date	2010. 2 .19	
		Off when on, off while viewing, power auto on/off	Revised date		7/13



*** Please refer to the all cases which can be displayed on power off mode.**

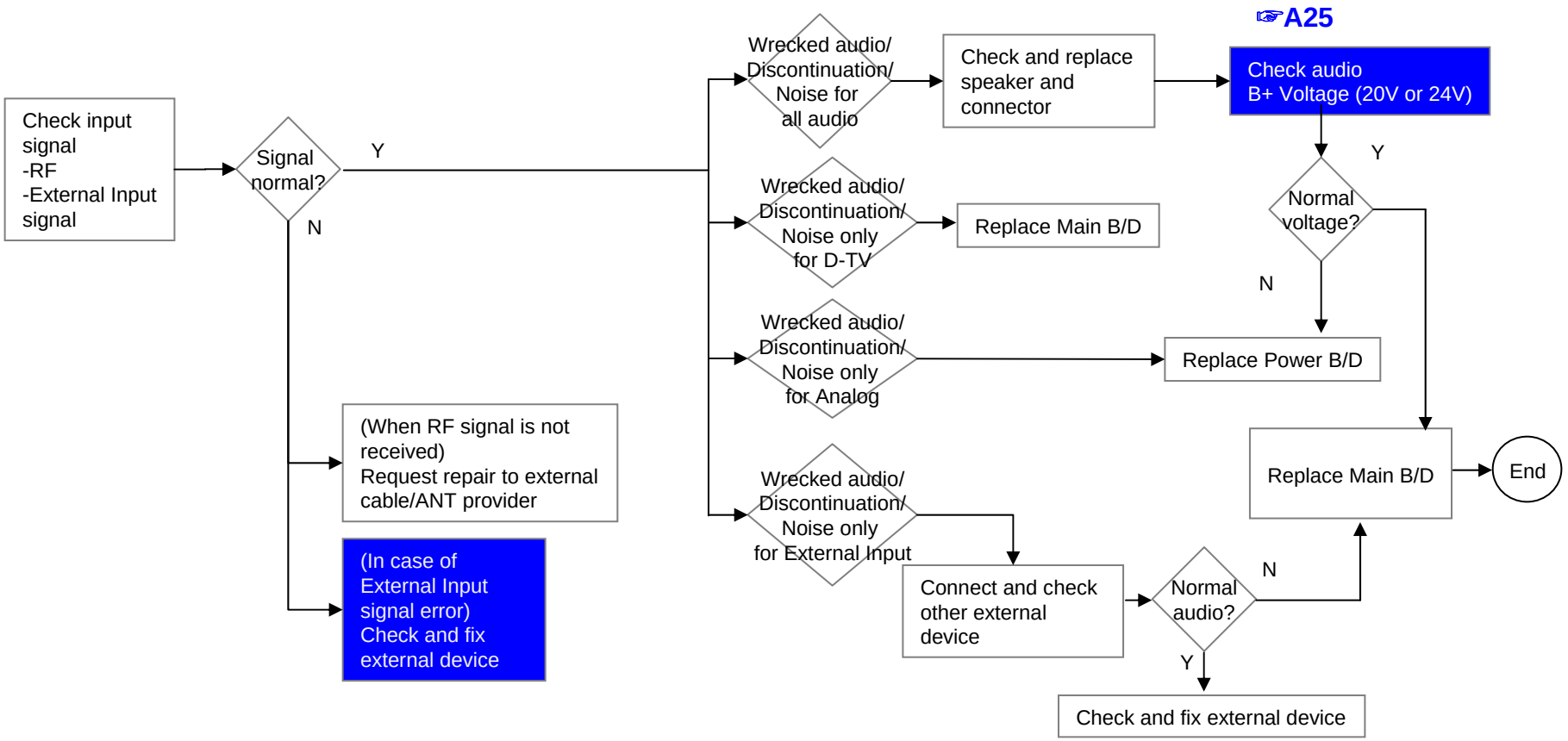
Status	Power off List	Explanation
Normal	"POWEROFF_REMOTEKEY"	Power off by REMOTE CONTROL
	"POWEROFF_OFFTIMER"	Power off by OFF TIMER
	"POWEROFF_SLEEPTIMER"	Power off by SLEEP TIMER
	"POWEROFF_INSTOP"	Power off by INSTOP KEY
	"POWEROFF_AUTOOFF"	Power off by AUTO OFF
	"POWEROFF_ONTIMER"	Power off by ON TIMER
	"POWEROFF_RS232C"	Power off by RS232C
	"POWEROFF_RESREC"	Power off by Reservated Record
	"POWEROFF_RECEND"	Power off by End of Recording
	"POWEROFF_SWDOWN"	Power off by S/W Download
	"POWEROFF_UNKNOWN"	Power off by unknown status except listed case
Abnormal	"POWEROFF_ABNORMAL1"	Power off by abnormal status except CPU trouble
	"POWEROFF_CPUABNORMAL"	Power off by CPU Abnormal

LCD TV	Error symptom	C. Audio error	Established date	2010. 2 .19	
		No audio/ Normal video	Revised date		8/13



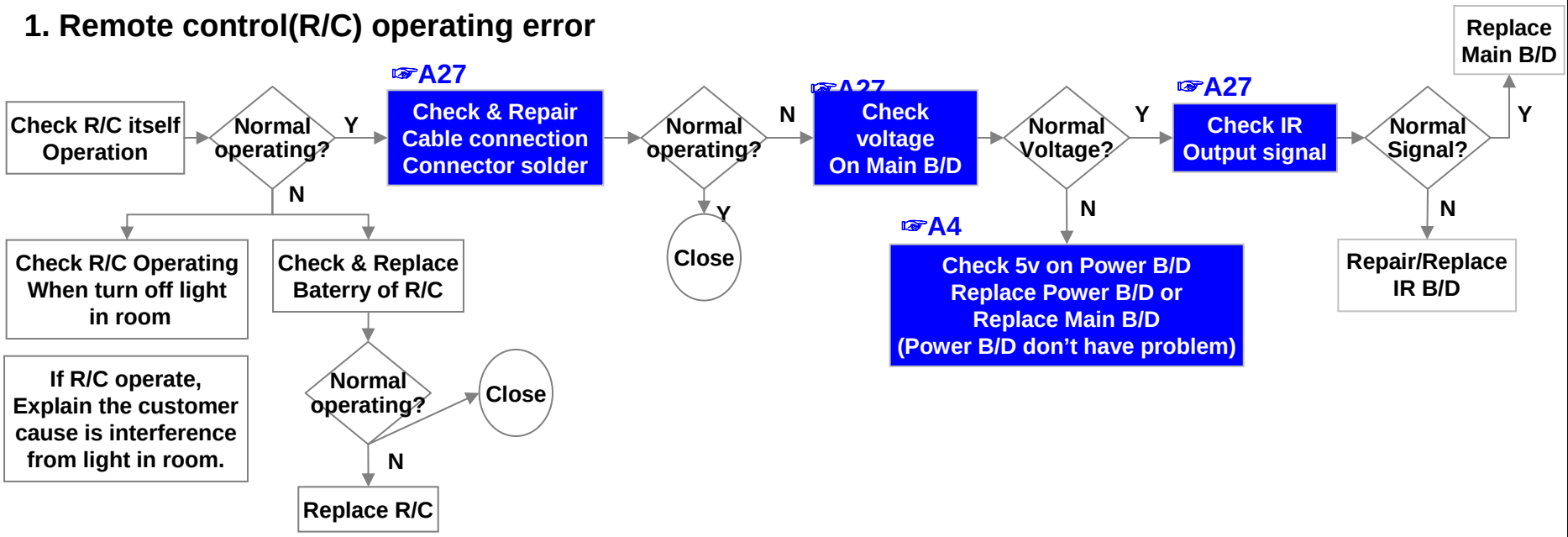
LCD TV	Error symptom	C. Audio error	Established date	2010. 2 .19	
		Wrecked audio/ discontinuation/noise	Revised date		9/13

→ abnormal audio/discontinuation/noise is same after "Check input signal" compared to No audio

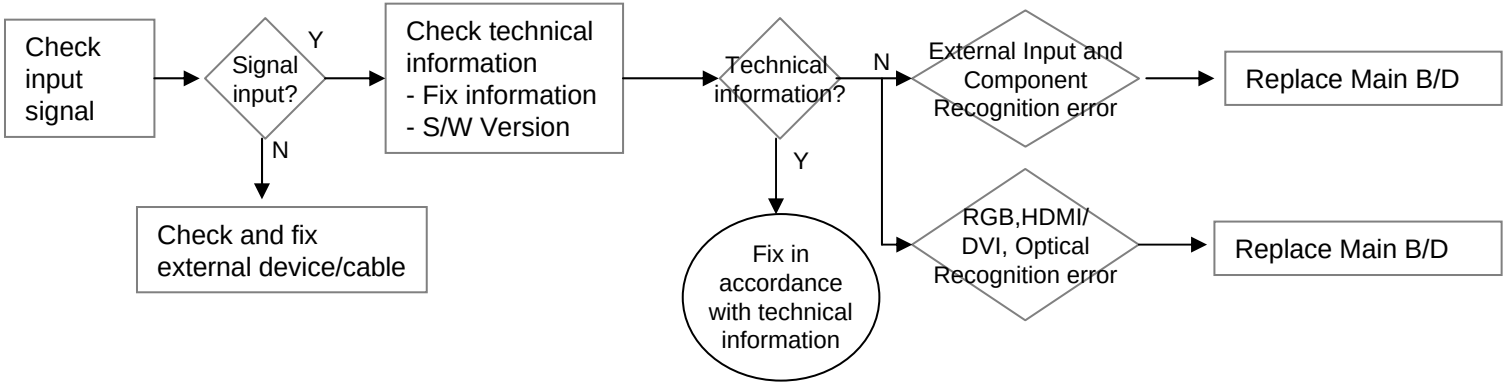


LCD TV	Error symptom	D. General Function Problem	Established date	2010. 2 .19	
		Remote control & Local switch checking	Revised date		10/13

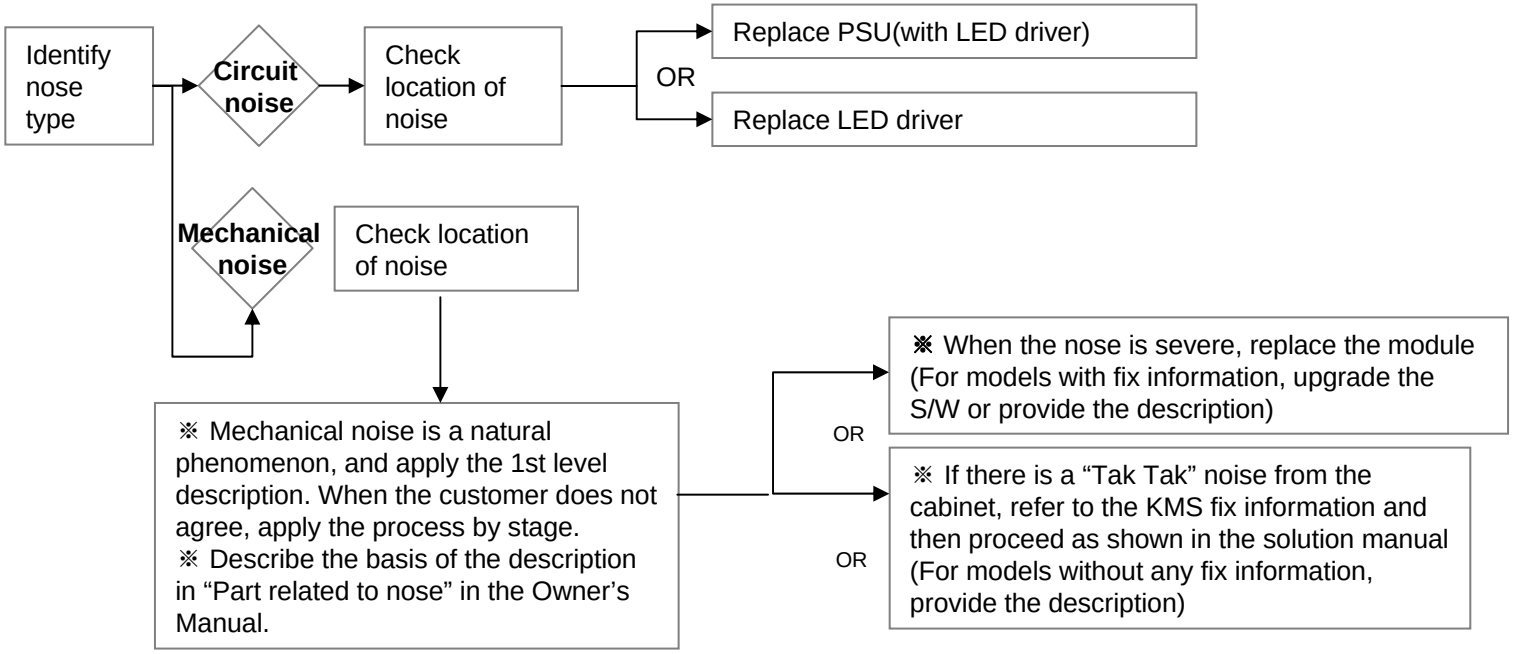
1. Remote control(R/C) operating error



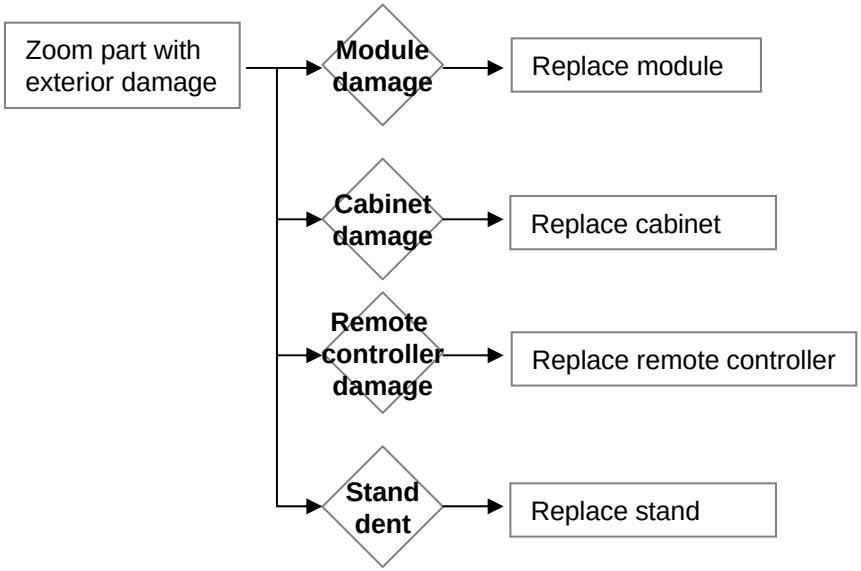
LCD TV	Error symptom	D. Function error	Established date	2010. 2 .19	
		External device recognition error	Revised date		11/13



LCD TV	Error symptom	E. Noise	Established date	2010. 2 .19	
		Circuit noise, mechanical noise	Revised date		12/13



LCD TV	Error symptom	F. Exterior defect	Established date	2010. 2 .19	
		Exterior defect	Revised date		13/13



Contents of LCD TV Standard Repair Process Detail Technical Manual

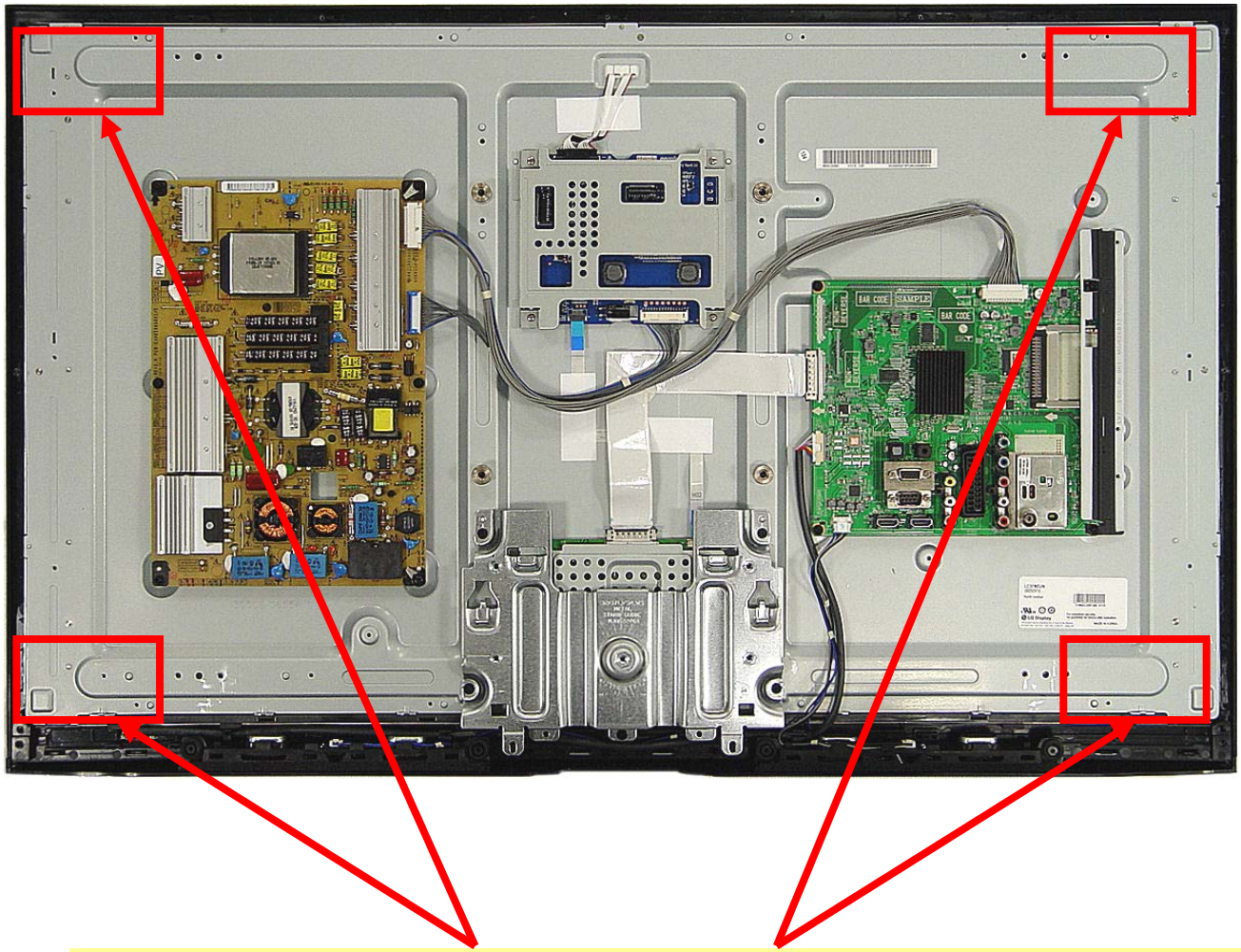
Continue to the next page

Contents of LCD TV Standard Repair Process Detail Technical Manual

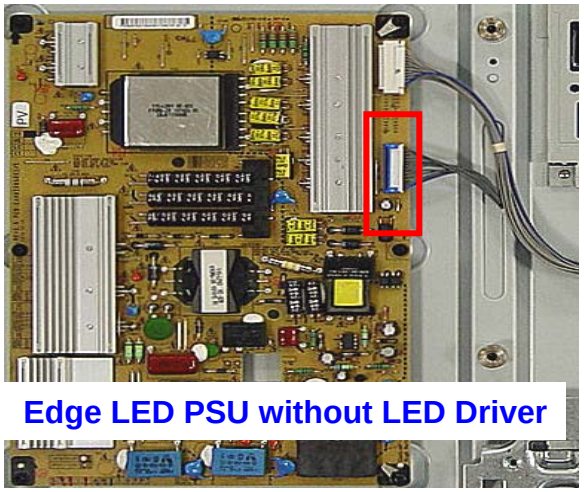
Continued from previous page

LCD TV	Error symptom	A. Video error_No video/Normal audio	Established date	2011. 2 .07	
	Content	Check LCD back light with naked eye	Revised date		A1

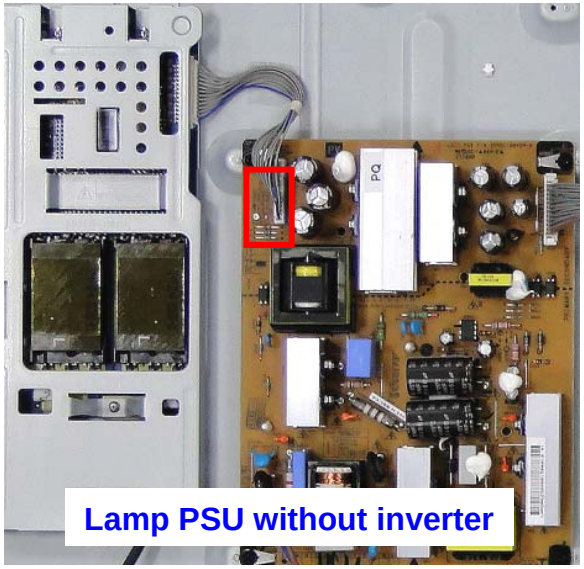
<ALL MODELS>



After turning on the power and disassembling the case, check with the naked eye, whether you can see light from 4 locations.



Edge LED PSU without LED Driver



Lamp PSU without inverter

Check the DC 24V, 12V, 3.5V and Inverter on

* ALEF/OS 42/47/55”/60”

Power Board ↔ Drive Board – PSU		
	14 pin	14 pin
1 ~ 5	24V	24V
6 ~ 10	GND	GND
11	Detect	Detect
12	Inverter On/Off	Inverter On/Off
13	Int. PWM	Int. PWM
14	Ext. PWM (PDIM)	Ext. PWM (PDIM)

* ALL 32”/37”

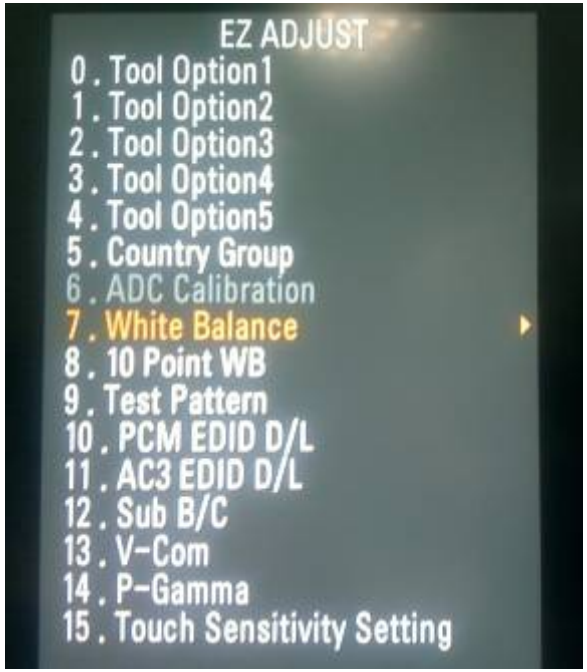
14 Pin (Power Board ↔ Driver) PSU	
1 ~ 5	24V
6 ~ 10	GND
11	Detect
12	Inverter On/Off
13	Int. PWM
14	Ext. PWM (PDIM)

*** 26” ~ 47” : `11 Pin map**

Lamp (Power Board ↔ Inverter) - PSU	
	14 Pin
1 ~ 5	24V
6 ~ 10	GND
11	Detect
12	Inverter On/Off
13	Int. PWM
14	Ext. PWM (PDIM)

LCD TV	Error symptom	A. Video error_No video/Normal audio	Established date	2011. 2 .07	
	Content	Check White Balance value	Revised date		A3

<ALL MODELS>



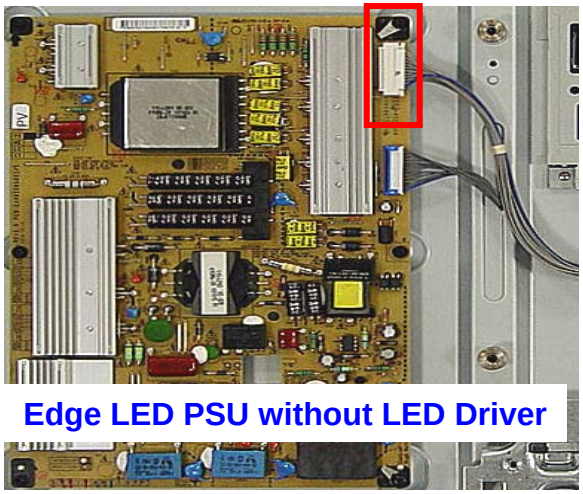
Entry method

1. Press the ADJ button on the remote controller for adjustment.

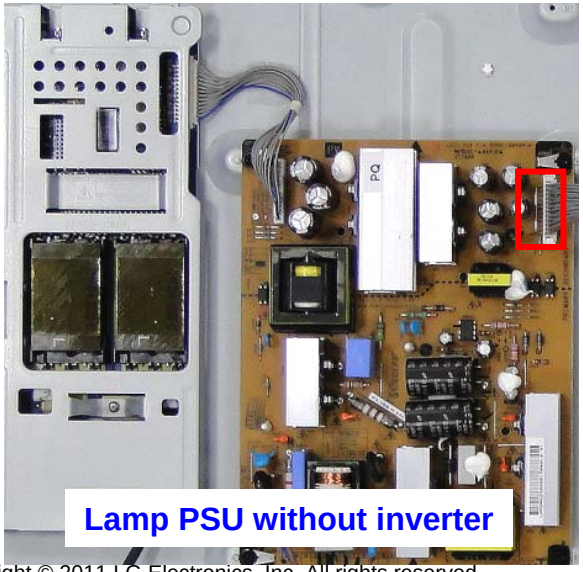
2. Enter into White Balance of item 7.

3. After recording the R, G, B (GAIN, Cut) value of Color Temp (Cool/Medium/Warm), re-enter the value after replacing the MAIN BOARD.

LCD TV	Error symptom	A. Video error_No video/ Audio	Established date	2011. 2 .07	
	Content	Power Board voltage measuring method	Revised date		A4



Edge LED PSU without LED Driver



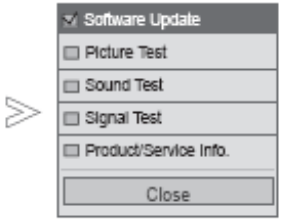
Lamp PSU without inverter

Check the DC 20Vor24V, 12V, 3.5V.

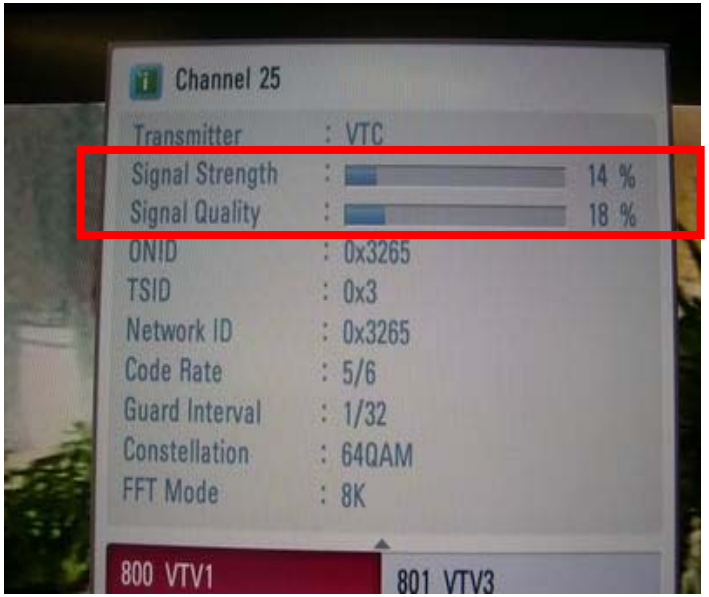
24 Pin (Power Board ↔ Main Board) - 공통			
SMAW200-H24S (YEONHO)			
1	Power on	2	20V (24V)
3	20V (24V)	4	20V (24V)
5	GND	6	GND
7	GND	8	GND
9	3.5V	10	3.5V
11	3.5V	12	3.5V
13	GND	14	GND
15	GND	16	N.C
17	12V	18	Inverter On/off
19	12V	20	Lamp : A-Dim LED : N.C
21	12V	22	PWM Dim #1
23	N.C • Lamp SCANNING Model : PWM Dim #2	24	Error-out

24 Pin (Power Board ↔ Main Board)			
FW20020-24SB (FOOSUNG)			
1	Power on	2	20V (24V)
3	20V (24V)	4	20V (24V)
5	GND	6	GND
7	GND	8	GND
9	3.5V	10	3.5V
11	3.5V	12	3.5V
13	GND	14	GND
15	GND	16	GND
17	12V	18	Inverter On/off
19	12V	20	Lamp : A-Dim
21	12V	22	PWM Dim #1
23	N.C	24	Error-out

<ALL MODELS>



MENU -→ red key(customer support -→ signal test -→ select channel



When the signal is strong, use the attenuator (-10dB, -15dB, -20dB etc.)

<ALL MODELS>

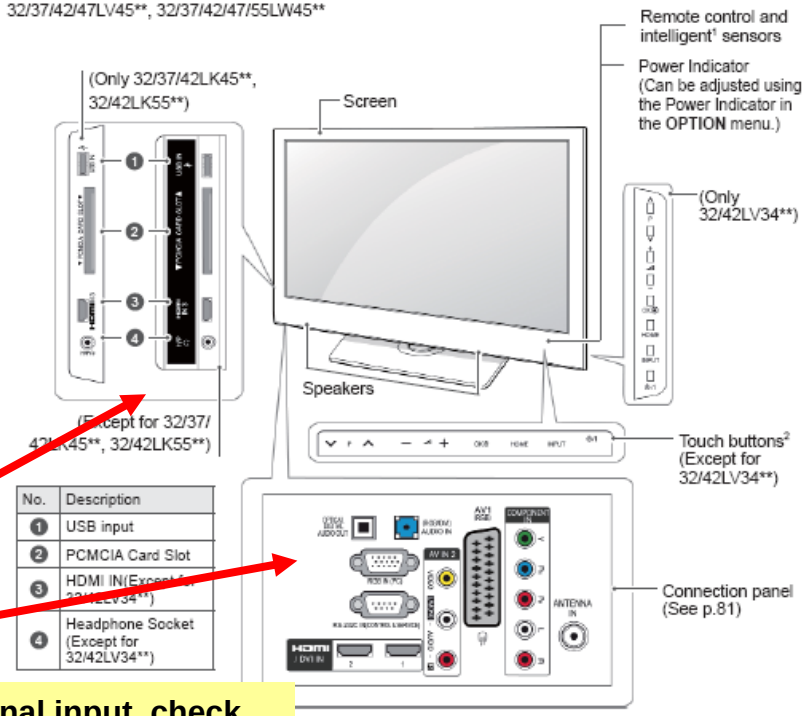
1. Checking method for remote controller for adjustment

Version

Press the IN-START with the remote controller for adjustment

• Image shown may differ from your TV.

Only 32/37/42LK45**, 32/42LK55**, 32LV25**, 32/42LV34**, 32/37/40/42/47LV35**, 32/37/42/47LV45**, 32/37/42/47/55LW45**



No.	Description
1	USB input
2	PCMCIA Card Slot
3	HDMI IN(Except for 32/42LV34**)
4	Headphone Socket (Except for 32/42LV34**)

As the part connecting to the external input, check the screen condition by signal

Button	Description
∨ P ∧	Scrolls through the saved programmes
− ◁ +	Adjusts the volume level
OK	Selects the highlighted menu option or confirms an input
HOME	Accesses the main menus, or saves your input and exits the menus
INPUT	Changes the input source
⏻/I	Turns the power on or off

1 Intelligent sensor - Adjusts the image quality corresponding to the surrounding environment.
2. Touch Button - You can use the desired button function by touching.

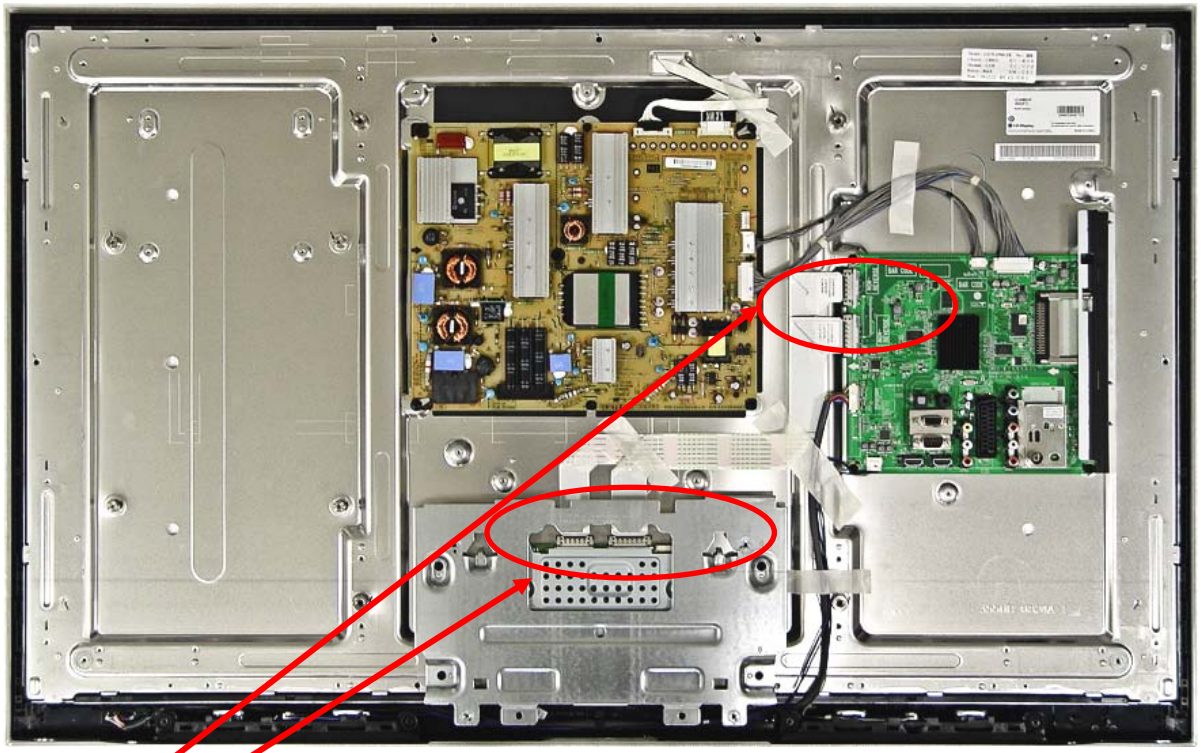
LCD TV	Error symptom	A. Video error_Video error, video lag/stop	Established date	2011. 2 .07	
	Content	TUNER checking part	Revised date		A9

<ALL MODELS>

Checking method:
1. Check the signal strength or check whether the screen is normal when the external device is connected.
2. After measuring each voltage from power supply, finally replace the MAIN BOARD.

LCD TV	Error symptom	A. Video error_Color error	Established date	2011. 2 .07	
	Content	Check Link Cable (LVDS) reconnection condition	Revised date		A10

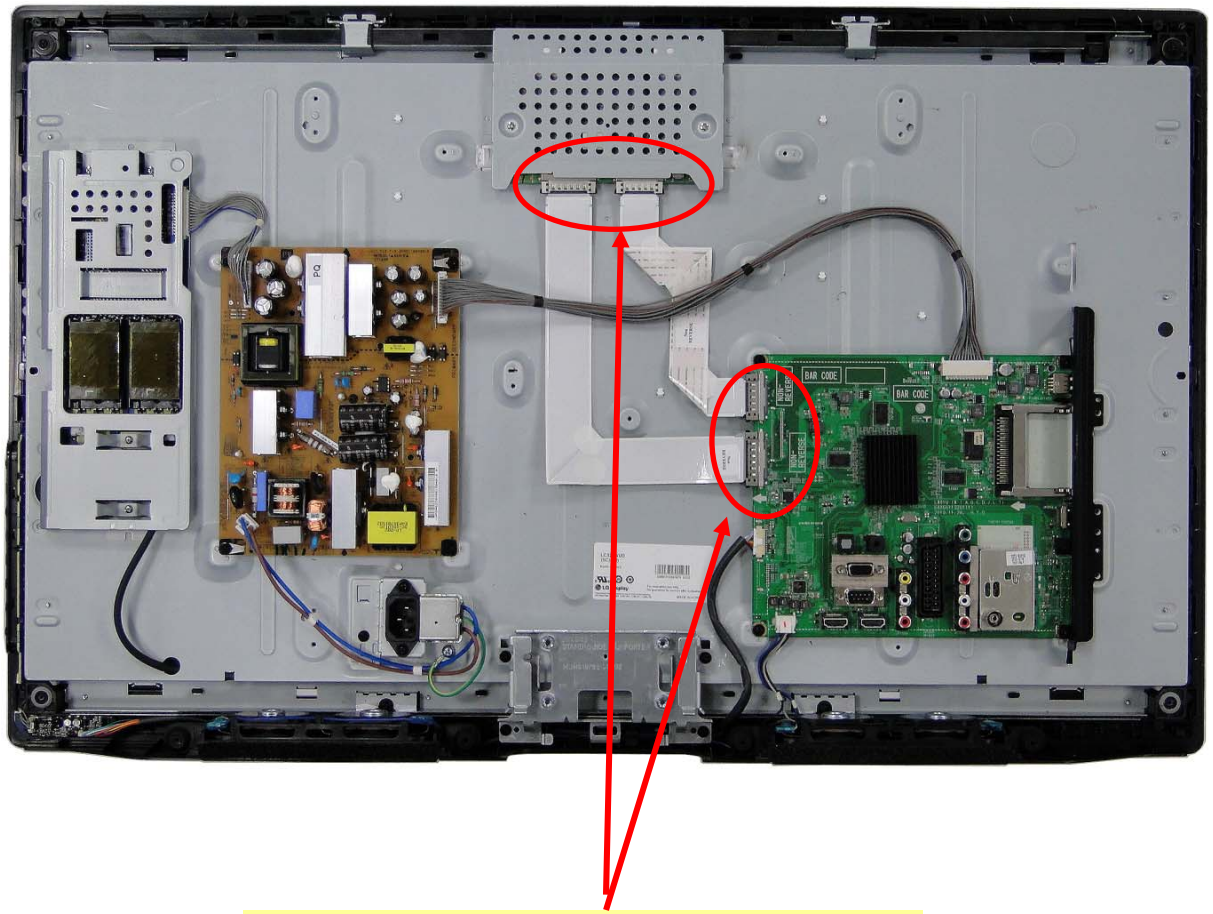
<LV** : Edge LED Series Models>



Check the contact condition of the Link Cable, especially dust or mis insertion.

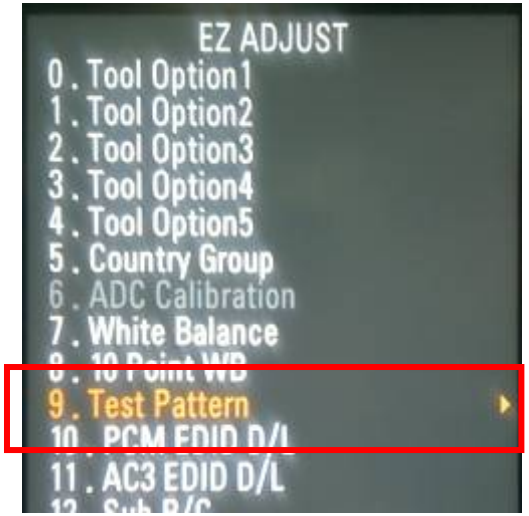
LCD TV	Error symptom	A. Video error_Color error	Established date	2011. 2 .07	
	Content	Check Link Cable (LVDS) reconnection condition	Revised date		A11

<LK** : Lamp series Models>



Standard Repair Process Detail Technical Manual

LCD TV	Error symptom	A. Video error_Color error	Established date	2011. 2 .07	
	Content	Adjustment Test pattern - ADJ Key	Revised date		A12



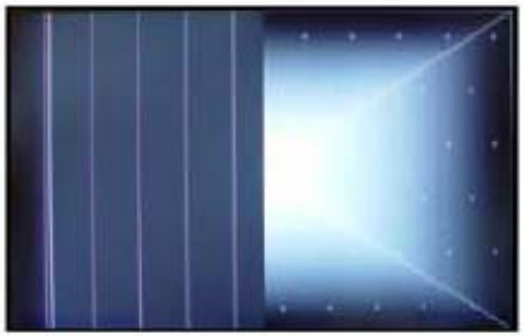
You can view 6 types of patterns using the ADJ Key

Checking item : 1. Defective pixel 2. Residual image 3. MODULE error (ADD-BAR,SCAN BAR..)
4.Video error (Classification of MODULE or Main-B/D!)

Appendix : Exchange T-Con Board (1)

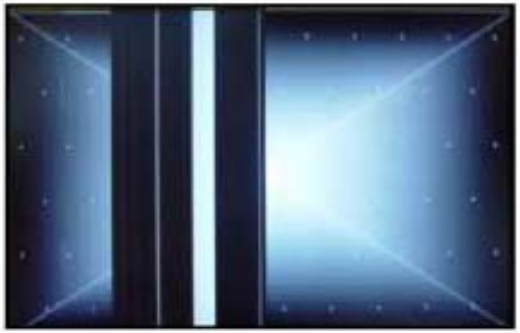
Solder defect, CNT Broken

Solder defect, CNT Broken

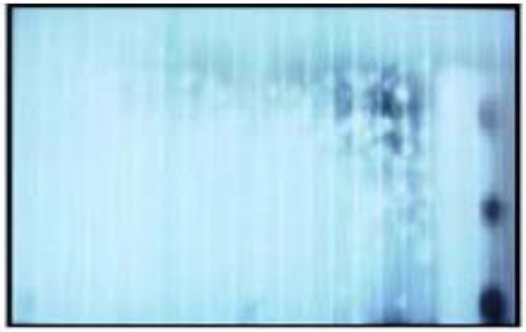


Solder defect, CNT Broken

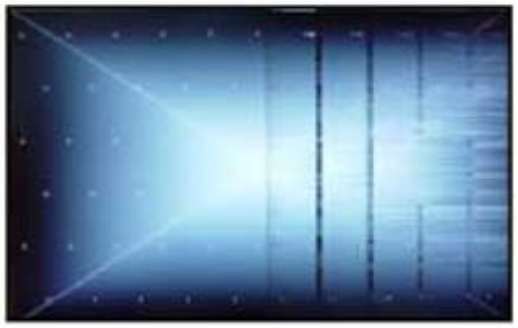
Solder defect, CNT Broken



Solder defect, CNT Broken



Abnormal Power Section

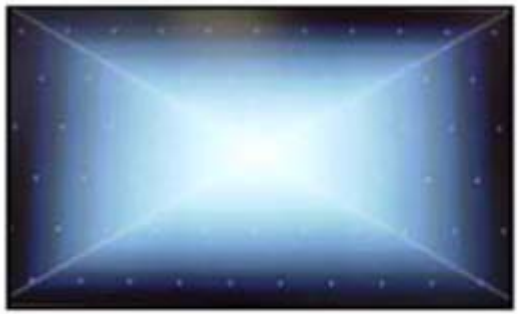


Solder defect, Short/Crack

Abnormal Power Section

Solder defect, Short/Crack

Appendix : Exchange T-Con Board (2)



Abnormal Power Section

Abnormal Power Section

Solder defect, Short/Crack

Solder defect, Short/Crack

Fuse Open, Abnormal power section

Abnormal Display

GRADATION

Noise

GRADATION

Appendix : Exchange PSU(LED driver)

No Light

Dim Light

Dim Light

Dim Light

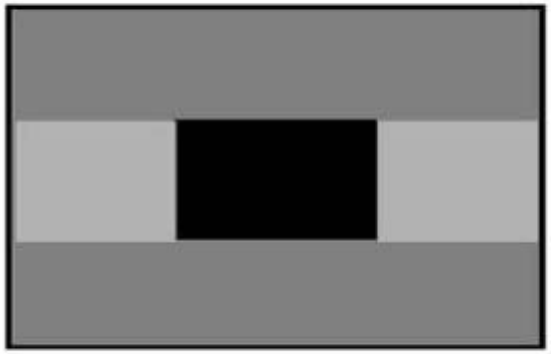
No picture/Sound Ok

Appendix : Exchange the Module (1)

Panel Mura, Light leakage

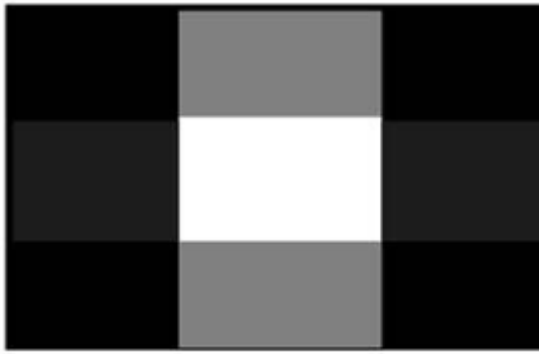
Panel Mura, Light leakage

Press damage



Crosstalk

Press damage



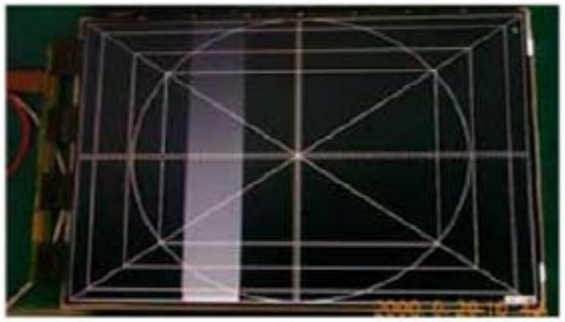
Crosstalk

Press damage

Un-repairable Cases

In this case please exchange the module.

Appendix : Exchange the Module (2)



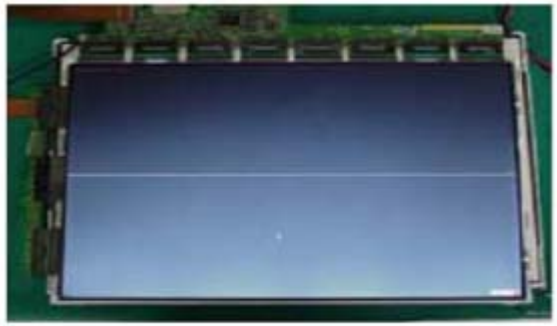
Vertical Block
Source TAB IC Defect

Vertical Line
Source TAB IC Defect

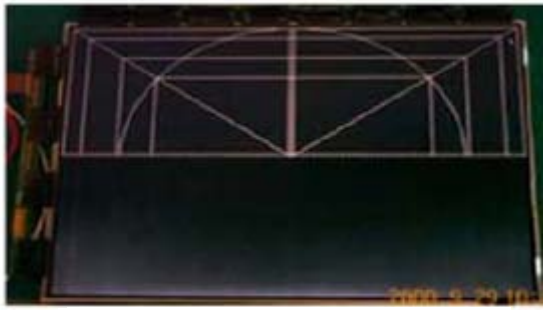
Vertical Block
Source TAB IC Defect

Horizontal Block
Gate TAB IC Defect

Horizontal Block
Gate TAB IC Defect



Horizontal line
Gate TAB IC Defect



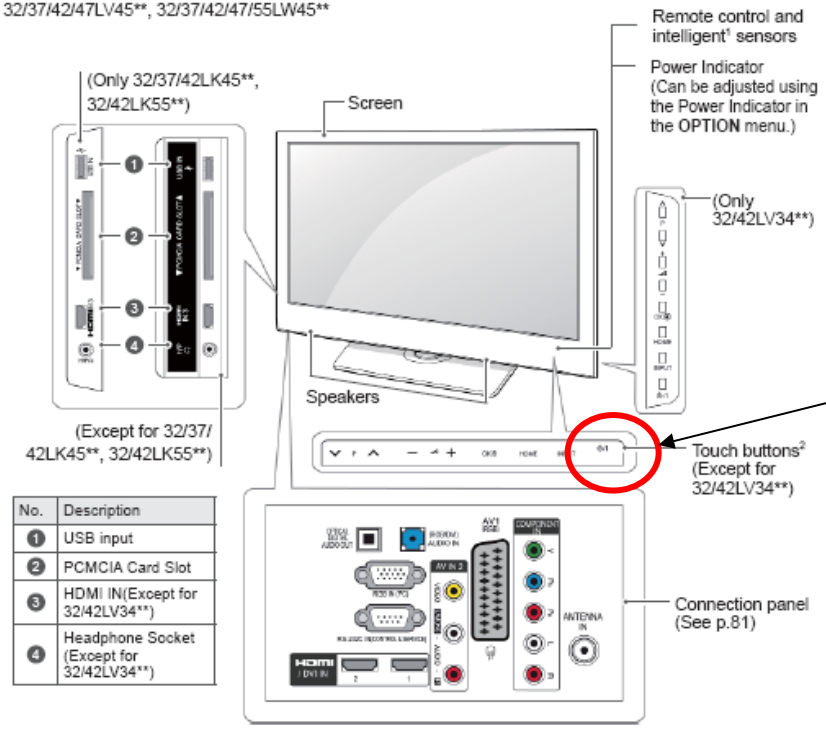
Horizontal Block
Gate TAB IC Defect

Un-repairable Cases

In this case please exchange the module.

• Image shown may differ from your TV.

Only 32/37/42LK45**, 32/42LK55**, 32LV25**, 32/42LV34**, 32/37/40/42/47LV35**, 32/37/42/47LV45**, 32/37/42/47/55LW45**



No.	Description
1	USB input
2	PCMCIA Card Slot
3	HDMI IN(Except for 32/42LV34**)
4	Headphone Socket (Except for 32/42LV34**)

Front LED control :
Menu → Option → Power Indicator → Standby light ON

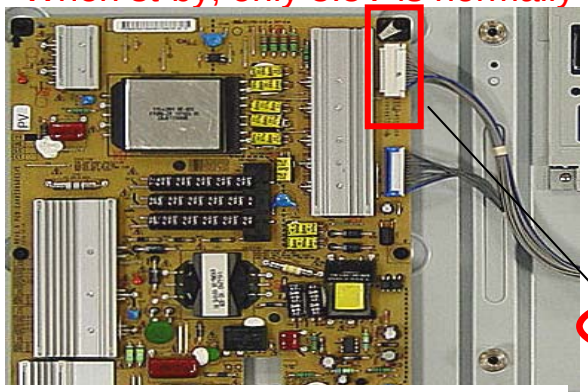
ST-BY condition: Red
Power ON condition: white

Button	Description
∨ P ∧	Scrolls through the saved programmes
− ◁ +	Adjusts the volume level
OK⊙	Selects the highlighted menu option or confirms an input
HOME	Accesses the main menus, or saves your input and exits the menus
INPUT	Changes the input source
⏻/I	Turns the power on or off

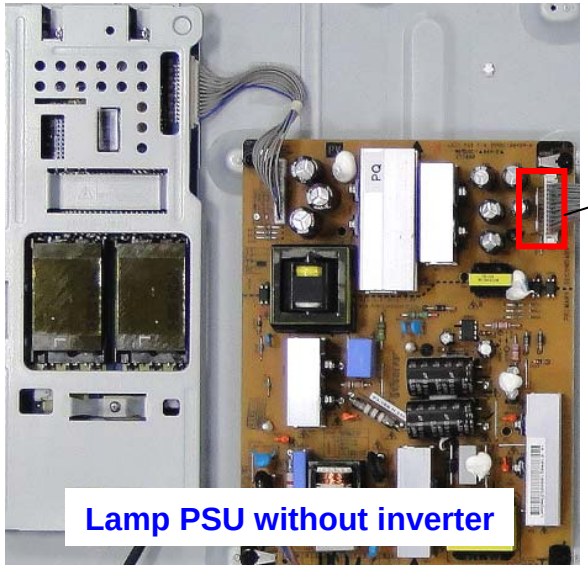
1 Intelligent sensor - Adjusts the image quality corresponding to the surrounding environment.
2. Touch Button - You can use the desired button function by touching.

LCD TV	Error symptom	B. Power error _No power	Established date	2011. 2 .07	
	Content	Check power input voltage and ST-BY 3.5V	Revised date		A18

For ’11 models, there is no voltage out for st-by purpose.
When st-by, only 3.5V is normally on.



Edge LED PSU without LED Driver

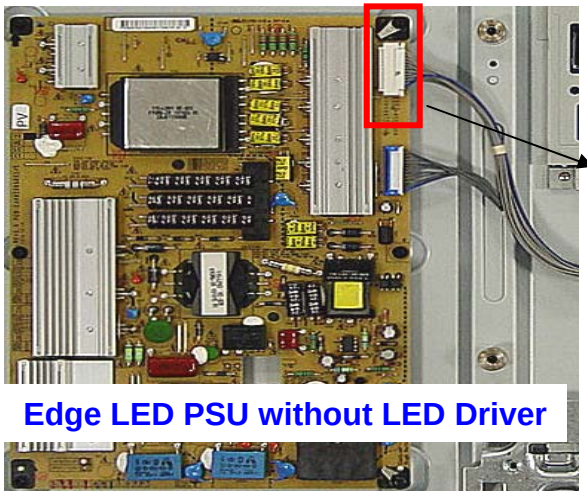


Lamp PSU without inverter

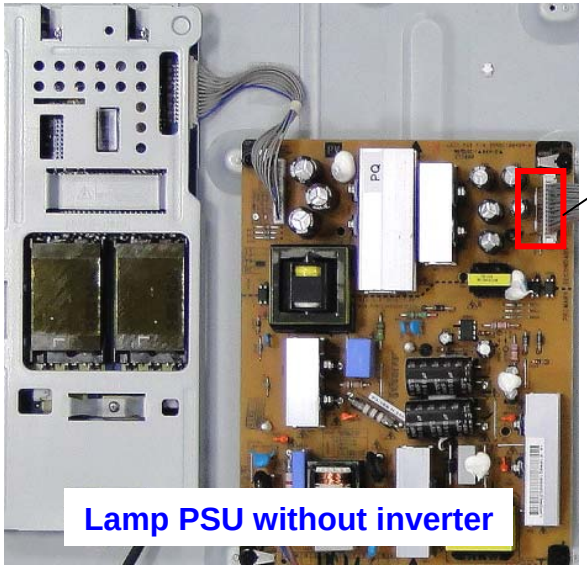
Check the 3.5V when st-by

24 Pin (Power Board ↔ Main Board) - 공통 SMAW200-H24S (YEONHO)			
1	Power on	2	20V (24V)
3	20V (24V)	4	20V (24V)
5	GND	6	GND
7	GND	8	GND
9	3.5V	10	3.5V
11	3.5V	12	3.5V
13	GND	14	GND
15	GND	16	N.C
17	12V	18	Inverter On/off
19	12V	20	Lamp : A-Dim LED : N.C
21	12V	22	PWM Dim #1
23	N.C • Lamp SCANNING Model : PWM Dim #2	24	Error-out

24 Pin (Power Board ↔ Main Board) FW20020-24SB (FOOSUNG)			
1	Power on	2	20V (24V)
3	20V (24V)	4	20V (24V)
5	GND	6	GND
7	GND	8	GND
9	3.5V	10	3.5V
11	3.5V	12	3.5V
13	GND	14	GND
15	GND	16	GND
17	12V	18	Inverter On/off
19	12V	20	Lamp : A-Dim
21	12V	22	PWM Dim #1
23	N.C	24	Error-out



Edge LED PSU without LED Driver



Lamp PSU without inverter

Check “power on” pin is high

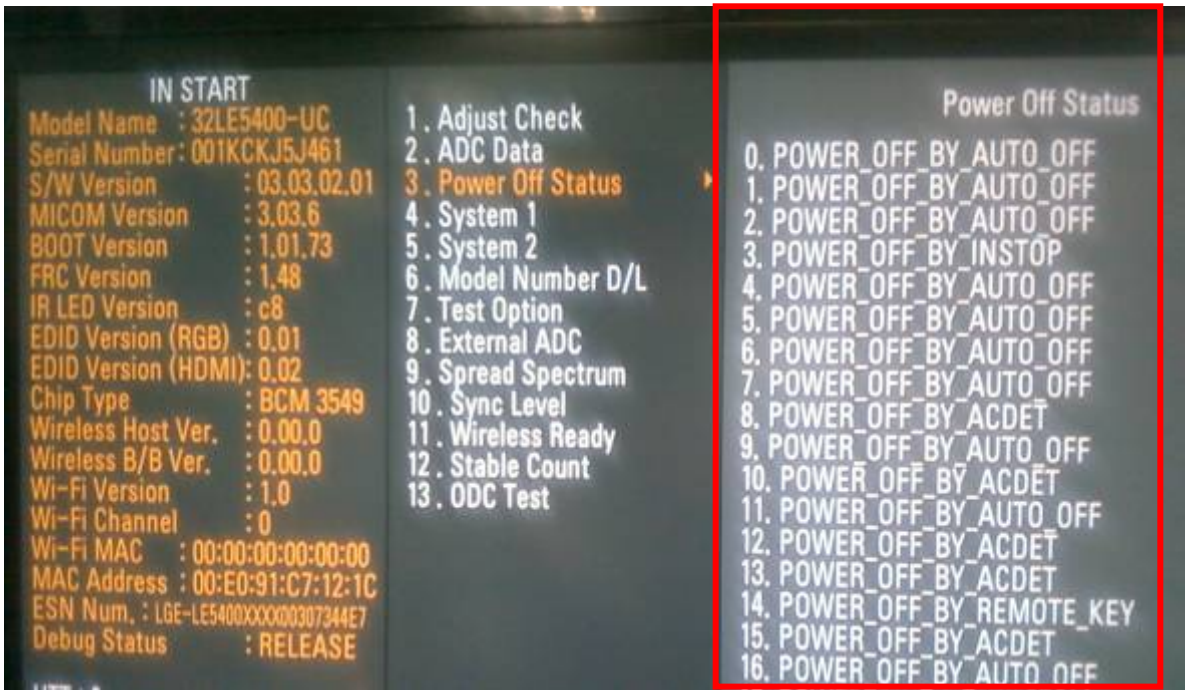
24 Pin (Power Board ↔ Main Board) - 공통 SMAW200-H24S (YEONHO)			
1	Power on	2	20V (24V)
3	20V (24V)	4	20V (24V)
5	GND	6	GND
7	GND	8	GND
9	3.5V	10	3.5V
11	3.5V	12	3.5V
13	GND	14	GND
15	GND	16	N.C
17	12V	18	Inverter On/off
19	12V	20	Lamp : A-Dim LED : N.C
21	12V	22	PWM Dim #1
23	N.C • Lamp SCANNING Model : PWM Dim #2	24	Error-out

24 Pin (Power Board ↔ Main Board) FW20020-24SB (FOOSUNG)			
1	Power on	2	20V (24V)
3	20V (24V)	4	20V (24V)
5	GND	6	GND
7	GND	8	GND
9	3.5V	10	3.5V
11	3.5V	12	3.5V
13	GND	14	GND
15	GND	16	GND
17	12V	18	Inverter On/off
19	12V	20	Lamp : A-Dim
21	12V	22	PWM Dim #1
23	N.C	24	Error-out

Standard Repair Process Detail Technical Manual

LCD TV	Error symptom	B. Power error _Off when on, off whiling viewing	Established date	2011. 2 .07	
	Content	POWER OFF MODE checking method	Revised date		A22

<ALL MODELS>



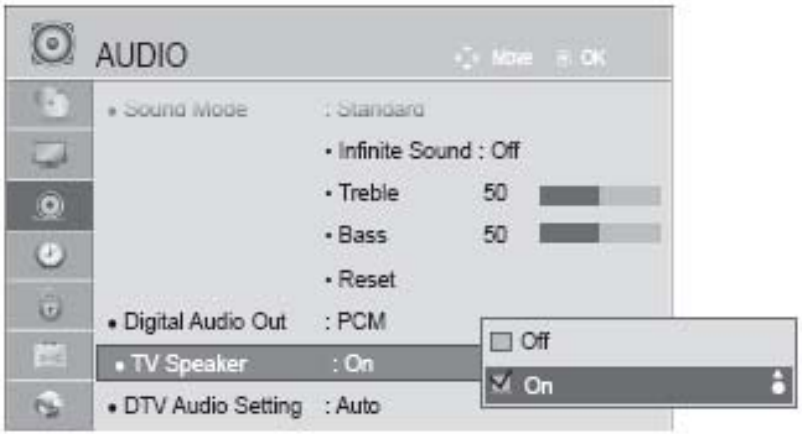
Entry method

1. Press the IN-START button of the remote controller for adjustment

2. Check the entry into adjustment item 3

LCD TV	Error symptom	C. Audio error_No audio/Normal video	Established date	2011. 2 .07	
	Content	Checking method in menu when there is no audio	Revised date		A24

<ALL MODELS>



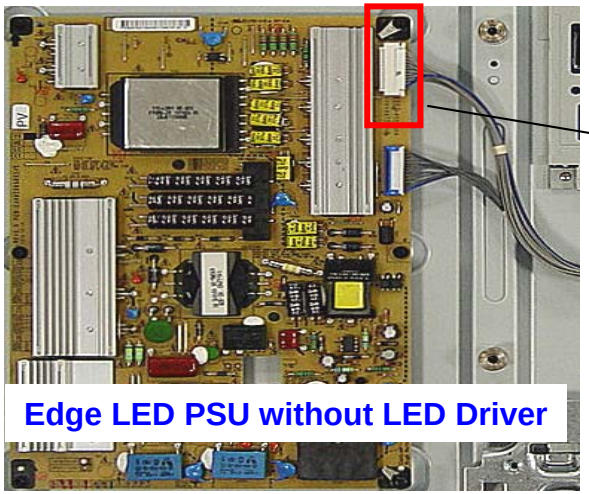
Checking method

1. Press the MENU button on the remote controller
2. Select the AUDIO function of the Menu
3. Select TV Speaker from Off to On

Standard Repair Process Detail Technical Manual

LCD TV	Error symptom	C. Audio error_No audio/Normal video	Established date	2011. 2 .07	
	Content	Voltage and speaker checking method when there is no audio	Revised date		A25

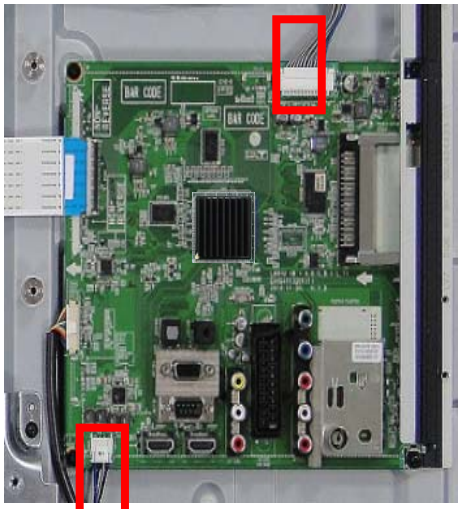
<ALL MODELS>



②

24 Pin (Power Board ↔ Main Board) - 공통			
SMAW200-H24S (YEONHO)			
1	Power on	2	20V (24V)
3	20V (24V)	4	20V (24V)
5	GND	6	GND
7	GND	8	GND
9	3.5V	10	3.5V
11	3.5V	12	3.5V
13	GND	14	GND
15	GND	16	N.C
17	12V	18	Inverter On/off
19	12V	20	Lamp : A-Dim LED : N.C
21	12V	22	PWM Dim #1
23	N.C • Lamp SCANNING Model : PWM Dim #2	24	Error-out

①



③

Checking order when there is no audio

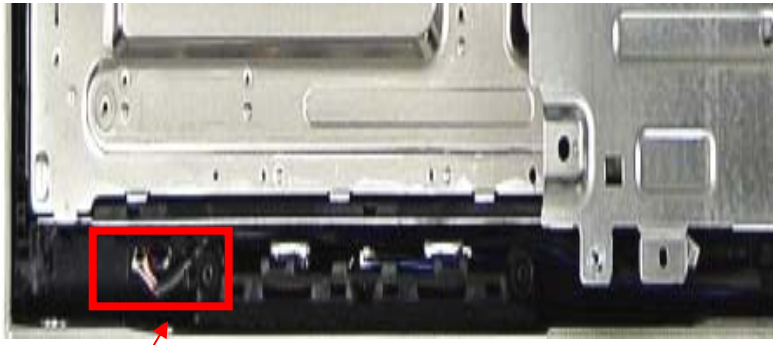
① Check the contact condition of 20V or 24V connector of Main Board

② Measure the 24V input voltage supplied from Power Board
(If there is no input voltage, remove and check the connector)

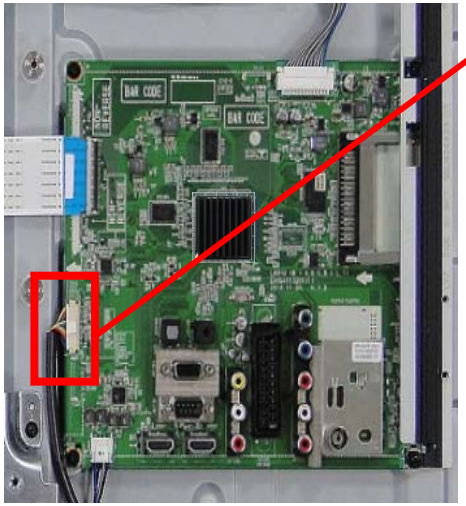
③ Connect the tester RX1 to the speaker terminal and if you hear the Chik Chik sound when you touch the GND and output terminal, the speaker is normal.

LCD TV	Error symptom	D. Function error_ No response in remote controller, key error	Established date	2011. 2 .07	
	Content	Remote controller operation checking method	Revised date		A27

<ALL MODELS>



1



2

3

4

P2401, P2402	
1	EYEQ_SCL
2	EYEQ_SDA
3	GND
4	KEY1
5	KEY2
6	St 3.3V
7	GND
8	LED_B
9	IR
10	GND
11	Normal 3.3V
12	LED_R
13	GND
14	Soft Touch_SCL
15	Soft Touch_SDA

Checking order

1, 2. Check IR cable condition between IR & Main board.
3. Check the st-by 3.3V on the terminal 6.
4. When checking the Pre-Amp when the power is in ON condition, it is normal when the Analog Tester needle moves slowly, and defective when it does not move at all.